CCITT SGXV Working Party XV/1 Specialists Group on Coding for Visual Telephony

Source: TOSHIBA Title : Mismatch Error Accumulation in a Fast DCT

. INTRODUCTION

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In previous documents, methods to ensure compatibility between codecs implemented with different inverse discrete cosine transform (IDCT) algorithms were investigated. Document number 281 proposed defining a maximum allowable amount of mismatch between encoder and decoder, as well as minimum amount of refresh in order to control the accumulation of error. Document number 284 proposed determining the refresh cycle using an analytical analysis of mismatch error. The purpose of this paper is to report on test results which extend the analysis of previous documents by

Examining mismatch error using not only matrix IDCT algorithm, but also a fast IDCT algorithm.
 Verifying that mismatch error accumulation varies greatly with parameters such as quantizer step size.

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2.FAST INVERSE COSINE TRANSFORM ⁽¹⁾

Previous documents conducted studies using a matrix IDCT and varying the bit precision. Here simulation is conducted to determine how well the results hold for an actual Fast IDCT algorithm. A flowgraph of the one-dimensional fast IDCT algorithm used is shown in Figure 1. The bit precision at every location is indicated in the figure using the following notation:

([bits above the binary point].[bits below the binary point])

The results of every calculation are rounded to the nearest LSB. All coefficients, including those described in Figure 2, are re presented with 16 bits. The two-dimensional version is realized as shown in Figure 3. The mismatch error (MME) of this algorithm computed according to method of Document number 281 is 0.02.

3.SIMULATION

Simulation was conducted using RM4 with the baseline IDCT of Document 281 in the encoder. The following three algorithms were used as the decoder's IDCT algorithm:

1.The above fast IDCT (FAST)
2.The baseline IDCT of Document 281 with precision reduced
to 12 bits (12.0) between one-dimensional transforms
(MATRIX N=12) to 12 bits (12.0) between one-dimensional (MATRIX,N=12) 3.The baseline IDCT of Document 281 (MATRIX,N=16)

The third algorithm corresponds to the case of no mismatch between encoder and decoder.

The three second scene "Trevor" was repeated (forward, backward, forward, backward, forward) a total of five times to generate a fifteen second sequence and coded at 10 frames per second. A frame from the original sequence was placed in the frame memories of encoder and decoder before coding.

Figure 4 shows the results of coding the above sequence at a bit rate of 768 Kbps. Because of their similiarity, the results of the fast IDCT and the bit reduced baseline IDCT are plotted separately against the no mismatch case for clarity. For both cases the SNR loss due to using the mismatched IDCT grows to about 1 dB in 15 seconds (fast-IDCT : 1.15 dB loss, 12bit baseline : 1.05 dB loss).

Coding the same sequence at a rate of 384 Kbps produces the results shown in Figure 5. Here the SNR loss is a mere 0.09 dB for the fast-IDCT case and 0.03 dB for the 12-bit baseline case.

3.1 Fast-Algorithm vs. 12-bit Baseline Algorithm

The above results are at first glance somewhat counter-intuitive. Namely, an examination of mismatch error (MME) calculations for each algorithm (based on the method described in Document number 281) shown in Figure 6 reveal that the MME of the fast-IDCT algorithm is three times less. Inspite of this the SNR loss in Figures 4 and 5 is roughly the same for both algorithms.

Figure 7 which plots the distortion between the locally decoded signal and the decoded signal indicates that initially distortion is less for the fast-IDCT decoder, but that error tends to accumulate at a faster rate.

initially distortion The fact that is less for the fast-IDCT case reconciles the apparent disparity between MME and SNR loss figures; however it is not fully understood why the error due to the fast algorithm accumulates faster. It is thought that perhaps the uneven pixel-by-pixel MME characteristics of the fast-algorithm (see Figure 7) may cause error to accumulate at a faster rate.

3.2 Quantizer Dependent Performance

One other observation based on these simulation results supports statements from previous documents that the mismatch effect slowly appears if a coarse quantizer is used. At a bit rate of 768 Kbps (average quantizer step = 7.7) the SNR loss for both algorithms grows to about 1 dB, while at a bit rate of 384 Kbps (average quantizer step = 15.2) the SNR loss is negligible.

4. SUMMARY

Although further study is needed two conclusions are suggested from the above discussion.

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1. The way in which mismatch error is accumulated may vary significantly from algorithm to algorithm (particulary when comparing matrix- and fast-IDCT algorithms), thus MME alone may not completely specify how error accumulates in the decoder.

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2.Quantizer step size can have a great effect on the appearance and accumulation of MME.

(1)N.Suehiro,M.Hatori,"Fast Algorithm for the DFT and DCT", . Tech Rep of IECE,IE85-4,1985



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FIGURE 2: REPRESENTATION OF TRIGONOMETRIC

SORA

COEFFICIENTS IN FIGURE 1



FIGURE 3: IMPLEMENTATION OF 2-DIMENSIONAL

INVERSE TRANSFORM AND BIT PRECISION BETWEEN STAGES





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MHE (PI	XEL-BY-PC	XEL BASIS	5)				
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0,05	0.05	0.05	0,05	0.00	0.05	<u> </u>	0.06
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0.06	0.05	0,06	0,05	0.05	0.05	<u> </u>	0.04
0.46	0,00		0.00	0.00	0.07	0,03	0.03
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0.01	0.02	0.02	0.0?	50.0	0.02	0.02	9.02
0.02	50.0	0.02	0,02	0,02	0.02	C.02	0.02
0,02	50.0	0,02	0,02	0,02	0.02	50.0	20.02
0,06	0,02	50.0	0,02	0,02	0,02	C, OZ	0,02
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3		J.		10		(230	

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FIGURE 7 : ACCUMULATION OF ERROR BETWEEN DECODED

SIGNAL AND LOCAL PECODED SIGNAL - 8 -#340

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