CCITT SG-XV Working Party XV/1 Specialists Group on Coding for Visual Telephony

Document #222 June 1987

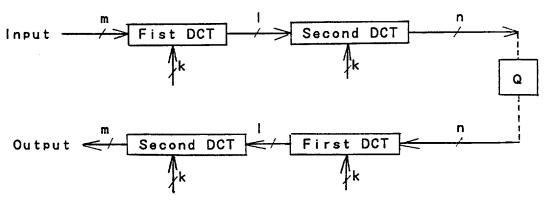
Source: NTT, KDD, NEC and FUJITSU
Title: PRECISION OF DCT CALCULATION

1. Introduction

According to the current Flexible Hardware specification, accuracy of Inverse DCT calculation is strictly specified to ensure compatibility between independently manufactured programmable codecs [1]. It is specified that Inverse DCT calculation is carried out with 16-bit accuracy for matrix elements and 16-bit accuracy for the first one-dimensional DCT output to be supplied to the second one in the cascaded DCT [2]. To facilitate DCT implementation, appropriate accuracy should be investigated [3].

This contribution shows experimental results when these accuracies (number of bits) are changed in the calculation using actual video sequences.

2. Accuracy of Forward and Inverse DCT ---- Definition



In the current Flexible Hardware specification, the values for m, k, I, and n are 9, 16, 16, and 12 bits, respectively. It is noted that I bits are obtained by truncation from the first DCT output expressed with (m+k-1) or (n+k-1) bits by truncation.

3. Experiments Using Edge Pattern

(1) Truncation or Round-off? (Table 1)

In this experiment when no quantizer is used, approximation is made at a point between the first and the second DCT for both Forward and Inverse DCT. The result is shown in Table 1. For a combination of m=9, k=12, l=8, and n=12, round-off approximation gives a better result in that distortion by round-off is apparently much less biased than by truncation. No doubt that the

difference is small when the accuracy of the parameters is high.

(2) Quantizer and DCT Accuracy (Table 2)

For a quantizer with (4, 4) i.e. a threshold and a step size both equal to 4 in RM4 but in open-loop operation, difference is hardly found even if the accuracy for (k, 1) is changed from (16, 16) to (16, 10). For a coarser quantizer with (16, 16), difference is hardly seen for the same parameter change. However, when k and I are taken to be 12 and 8, respectively, distortion increases slightly. Round-off is used for approximation.

It seems that a combination of $(k,\ l)$ equal to $(16,\ l6)$ is more than enough as far as this experiment is concerned.

4. Experiment Using 'Split Screen' (Fig.1)

Maximum accuracy here is given by a calculation which fully meets the Flexible Hardware specification. SNR values are shown as a function of (k, l) values in Fig. 1.

Picture quality difference is hardly found for (k, l) values of (12, 12), (10, 12), (10, 10), and (8, 10) when the two quantizers above i.e. (threshold, step size) = (4, 4) and (16, 16) are used. Round-off is employed in approximating the output value from the first DCT.

Picture quality degradation is apparent when the accuracy for this output value is reduced to 8 bits. For reduced accuracy of this output, round-off is in particular effective [4].

The 16-bit accuracy for k and I seems more than enough.

Conclusion

An appropriate accuracy for DCT calculation seems to be 12 bits for both k and l, though further close investigation is needed. As an approximation in DCT calculation, round-off is recommended. (VTR demonstration follows.)

Reference

- [1] Doc.#182 (Nov. 1986)
- [2] Doc.#142 (Sept. 1986)
- [3] Doc.#121 (June 1986)
- [4] Section 4.2/Doc.#181R (Nov. 1986)

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Table 1 Approximation

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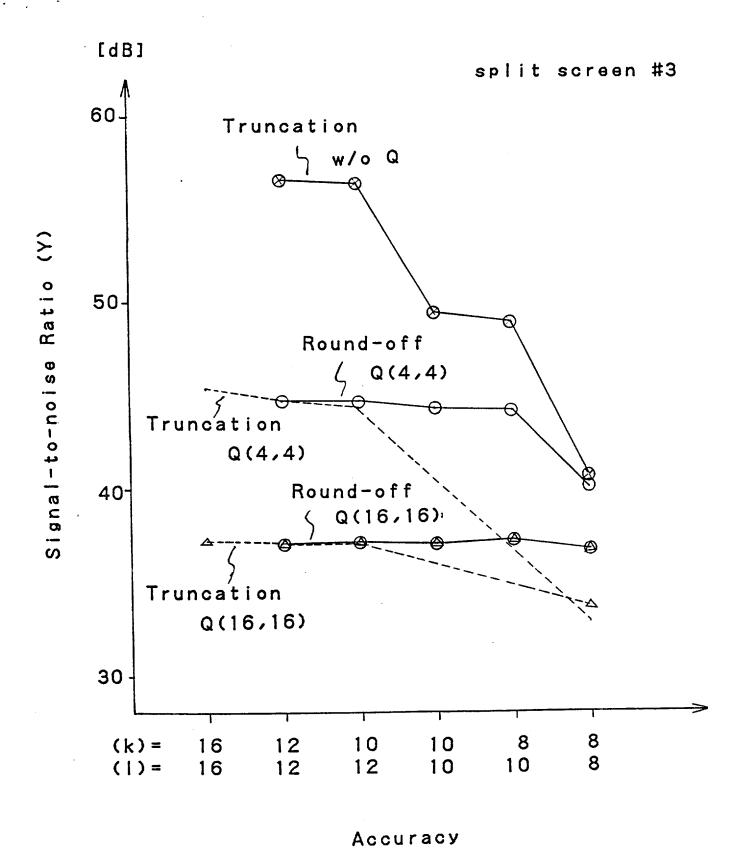


Figure 1 Accuracy and Distortion