

Title: Dual mode digital interface

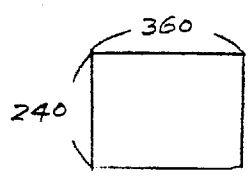
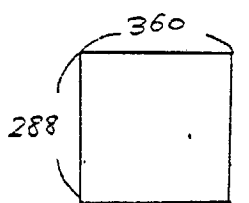
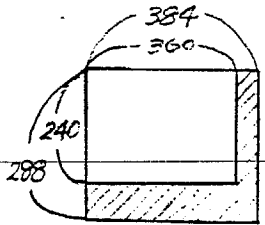
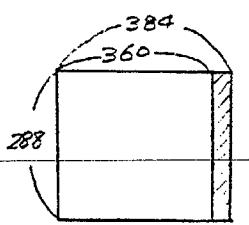
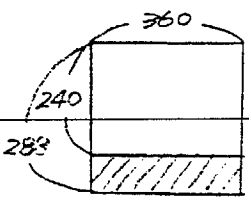
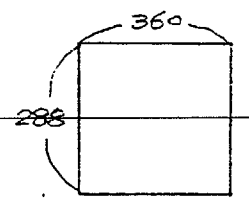
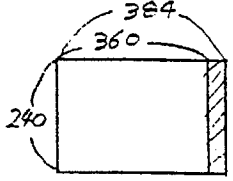
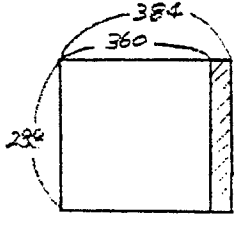
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4/25/'85

NTT, KDD, NEC, FUJITSU

(1) Possible approaches

 indicates idle pels

Candidates	Pel structures processed in a source coder/decoder		COMMENTS
	525/60	625/50	
E1			(1) Dual mode operation (2)
E2			(1) Regarded virtually as single mode operation (2) multiple no. of 16
E3			(1) Same as E2 (2)
E4			(1) Same as E1 (2) multiple no. of 16

(2) Proposal

Differences among them are not essential.
Selection should be discussed in relation to coding algorithms.