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| **Joint Video Experts Team (JVET)**  **of ITU-T SG 16 WP 3 and ISO/IEC JTC 1/SC 29/WG 11**  15th Meeting: Gothenburg, SE, 3–12 July 2019 | Document: JVET-O\_Notes\_d0 |

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| *Title:* | **Meeting Report of the 15th Meeting of the Joint Video Experts Team (JVET), Gothenburg, SE, 3–12 July 2019** | | |
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| *Purpose:* | Report | | |
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| *Source:* | Chairs of JVET | | |

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# Summary

The Joint Video Experts Team (JVET) of ITU-T WP3/16 and ISO/IEC JTC 1/ SC 29/ WG 11 held its fifteenth meeting during 3–12 July 2019 at the at the Clarion Post Hotel in Gothenbug, SE. The JVET meeting was held under the chairmanship of Dr Gary Sullivan (Microsoft/USA) and Dr Jens-Rainer Ohm (RWTH Aachen/Germany). For rapid access to particular topics in this report, a subject categorization is found (with hyperlinks) in section 2.13 of this document. It is further noted that the unabbreviated name of JVET was formerly known as “Joint Video *Exploration* Team”, but the parent bodies modified it when entering the phase of formal development of a new standard. The name Versatile Video Coding (VVC) was chosen in April 2018 as the informal nickname for the new standard.

The JVET meeting began at approximately 0900 hours on Wednesday 3 July 2019. Meeting sessions were held on all days (including weekend days) until the meeting was closed at approximately XXXX hours on Friday 12 July 2019. Approximately XXX people attended the JVET meeting, and approximately XXX input documents, 11 CE summary reports, and 17 AHG reports were discussed. The meeting took place in a collocated fashion with a meeting of WG11 – one of the two parent bodies of the JVET. The subject matter of the JVET meeting activities consisted of developing video coding technology with a compression capability that significantly exceeds that of the current HEVC standard, or otherwise gives better support regarding the requirements of future application domains of video coding. As a primary goal, the JVET meeting reviewed the work that was performed in the interim period since the fourteenth JVET meeting in producing a fifth draft of the VVC standard and the fifth version of the associated VVC test model (VTM). Further important goals were reviewing the results of 11 Core Experiments (CE), reviewing other technical input on novel aspects of video coding technology, producing the next versions of the VVC draft text and VTM, and plan next steps for further investigation of candidate technology towards the formal standard development.

The JVET produced XX output documents from the meeting (update):

* JVET-N1001 Versatile Video Coding specification text (Draft 5)
* JVET-N1002 Algorithm description for Versatile Video Coding and Test Model 5 (VTM 5)
* JVET-N1003 Guidelines for VVC reference software development
* JVET-N1005 Methodology and reporting template for coding tool testing
* JVET-N1010 and JVET-N1011, JVET common test conditions and software reference configurations for SDR and, HDR/WCGvideo, respectively
* JVET-N1021 through JVET-N1031, Description of Core Experiments 1 through 11

For the organization and planning of its future work, the JVET established XX “ad hoc groups” (AHGs) to progress the work on particular subject areas. At this meeting, XX Core Experiments (CE) were defined. The next four JVET meetings were planned for 1–9 October 2019 under ITU-T SG16 auspices in Geneva, CH, during 8–17 January 2020 under WG 11 auspices in Brussels, BE, during 15–24 April 2020 under WG 11 auspices in Alpbach, AT, and during 23 June – 01 July 2020 under ITU-T SG16 auspices in Geneva, CH.

The document distribution site <http://phenix.it-sudparis.eu/jvet/> was used for distribution of all documents.

The reflector to be used for discussions by the JVET and all its AHGs is the JVET reflector:  
[jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de) hosted at RWTH Aachen University. For subscription to this list, see  
<https://lists.rwth-aachen.de/postorius/lists/jvet.lists.rwth-aachen.de/>.

# Administrative topics

## Organization

The ITU-T/ISO/IEC Joint Video Experts Team (JVET) is a group of video coding experts from the ITU-T Study Group 16 Visual Coding Experts Group (VCEG) and the ISO/IEC JTC 1/SC 29/WG 11 Moving Picture Experts Group (MPEG). The parent bodies of the JVET are ITU-T WP3/16 and ISO/IEC JTC 1/SC 29/WG 11.

The Joint Video Experts Team (JVET) of ITU-T WP3/16 and ISO/IEC JTC 1/SC 29/WG 11 held its fifteenth meeting during 3–12 July 2019 at the Clarion Post Hotel in Gothenburg, SE. The JVET meeting was held under the chairmanship of Dr Gary Sullivan (Microsoft/USA) and Dr Jens-Rainer Ohm (RWTH Aachen/Germany).

It is further noted that the unabbreviated name of JVET was formerly known as “Joint Video *Exploration* Team”, but the parent bodies modified it when entering the phase of formal development of a new standard. The name Versatile Video Coding (VVC) was chosen in April 2018 as the informal nickname for the new standard.

## Meeting logistics

Information regarding logistics arrangements for the meeting had been provided via the email reflector [jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de) and at <http://wftp3.itu.int/av-arch/jvet-site/2019_07_O_Gothenburg/>.

## Primary goals

As a primary goal, the JVET meeting reviewed the work that was performed in the interim period since the fourteenth JVET meeting in producing a fifth draft of the VVC standard and the fifth version of the associated VVC test model (VTM). Further important goals were reviewing the results of 11 Core Experiments (CE), reviewing other technical input on novel aspects of video coding technology, and producing the next versions of draft text and VTM, and plan next steps for further investigation of candidate technology towards the formal standard development.

## Documents and document handling considerations

### General

The documents of the JVET meeting are listed in Annex A of this report. The documents can be found at <http://phenix.it-sudparis.eu/jvet/>.

Registration timestamps, initial upload timestamps, and final upload timestamps are listed in Annex A of this report.

The document registration and upload times and dates listed in Annex A and in headings for documents in this report are in Paris/Geneva time. Dates mentioned for purposes of describing events at the meeting (other than as contribution registration and upload times) follow the local time at the meeting facility.

Highlighting of recorded decisions in this report is practised as follows:

* Decisions made by the group that might affect the normative content of a future standard are identified in this report by prefixing the description of the decision with the string “Decision:”.
* Decisions that affect the VTM software but have no normative effect are marked by the string “Decision (SW):”.
* Decisions that fix a “bug” in the VTM description (an error, oversight, or messiness) or in the software are marked by the string “Decision (BF):”.

This meeting report is based primarily on notes taken by the JVET chairs. The preliminary notes were also circulated publicly by ftp and http during the meeting on a daily basis. It should be understood by the reader that 1) some notes may appear in abbreviated form, 2) summaries of the content of contributions are often based on abstracts provided by contributing proponents without an intent to imply endorsement of the views expressed therein, and 3) the depth of discussion of the content of the various contributions in this report is not uniform. Generally, the report is written to include as much information about the contributions and discussions as is feasible (in the interest of aiding study), although this approach may not result in the most polished output report.

### Late and incomplete document considerations

The formal deadline for registering and uploading non-administrative contributions had been announced as Tuesday, 25 June 2019. Any documents uploaded after 1159 hours Paris/Geneva time on Wednesday 26 June were considered “officially late”, giving a grace period of 12 hours to accommodate those living in different time zones of the world. The deadline does not apply to AHG reports, and other such reports which can only be produced after the availability of other input documents.

As agreed by the fourteenth meeting, contributions related to CE proposals were to be uploaded 1 week ahead of the above mentioned deadline, such that more thourough study was possible, and in particular the CE summary reports could be provided in time by the regular deadline. Consequently, CE proposal documents which were uploaded after 1159 hours Paris/Geneva time on Wednesday 19 June were considered “officially late”. After the positive experience, it was agreed to continue having a 2-week deadline for CE proposal documents, including draft text.

It was suggested to have CE description documents include a description of how the results are planned to be reported – e.g., the form of the tables to be used for the results data. Complexity analysis characterizations were suggested to be a particular issue where this applies.

All contribution documents with registration numbers higher than JVET-O0673 were registered after the “officially late” deadline (and therefore were also uploaded late). Likewise, CE proposal documents with registration numbers higher than JVET-O0132 were registered late. However, some documents in the “late” range might include break-out activity reports that were generated during the meeting, and are therefore better considered as report documents rather than as late contributions. Also, many cross-check reports were uploaded late.

In many cases, contributions were also revised after the initial version was uploaded. The contribution document archive website retains publicly accessible prior versions in such cases. The timing of late document availability for contributions is generally noted in the section discussing each contribution in this report.

One suggestion to assist with the issue of late submissions was to require the submitters of late contributions and late revisions to describe the characteristics of the late or revised (or missing) material at the beginning of discussion of the contribution. This was agreed to be a helpful approach to be followed at the meeting.

The following technical design proposal contributions were registered and/or uploaded late:qq

* JVET-O0XXX (a proposal on …), uploaded 06-XX.
* ….

It may be observed that some of the above-listed contributions were submissions made in response to issues that arose in discussions during the meeting or from the study of other contributions, and thus could not have been submitted by the ordinary deadline. For example, some of them were proposing combinations or simplifications of other proposals.

The following other document not proposing normative technical content, but with some need for consideration, were registered and/or uploaded late:

* JVET-O0XXX (a document on …), uploaded 06-XX.
* ….

All cross-verification reports at this meeting were registered late and therefore also uploaded late (all with numbers higher than JVET-O0XXX) and therefore are not specifically identified here, in the interest of brevity. Initial upload times for each document are recorded in Annex A of this report.

The following (X) contribution registrations were later cancelled, withdrawn, never provided, were cross-checks of a withdrawn contribution, or were registered in error: JVET-O0XXX (withdrawn), JVET-O0XXX (missing by the end of meeting), … .

The following crosschecks had not been uploaded yet by the end of the meeting, but were provided later:

…

“Placeholder” contribution documents that were basically empty of content, or lacking any results showing benefit for the proposed technology, and obviously uploaded with an intent to provide a more complete submission as a revision, had been agreed to be considered unacceptable and to be rejected in the document management system until a more complete version was available (which would then typically be counted as a late contribution). At the current meeting, this situation applied to the initial uploads of documents JVET-O0XXX and … .

Contributions that had significant problems with uploaded versions included the following:

* JVET-O0XXX (…)
* …

As a general policy, missing documents were not to be presented, and late documents (and substantial revisions) could only be presented when there was a consensus to consider them and there was sufficient time available for their review. Again, an exception is applied for AHG reports, CE summaries, and other such reports which can only be produced after the availability of other input documents. There were no objections raised by the group regarding presentation of late contributions, although there was some expression of annoyance and remarks on the difficulty of dealing with late contributions and late revisions.

It was remarked that documents that are substantially revised after the initial upload can also be a problem, as this becomes confusing, interferes with study, and puts an extra burden on synchronization of the discussion. This can especially be a problem in cases where the initial upload is clearly incomplete, and in cases where it is difficult to figure out what parts were changed in a revision. For document contributions, revision marking is very helpful to indicate what has been changed. Also, the “comments” field on the web site can be used to indicate what is different in a revision although participants tend to seldom notice what is recorded there.

A few contributions may have had some problems relating to IPR declarations in the initial uploaded versions (missing declarations, declarations saying they were from the wrong companies, etc.). These issues were corrected by later uploaded versions in a reasonably timely fashion in all cases (to the extent of the awareness of the responsible coordinators).

Some other errors were noticed in other initial document uploads (wrong document numbers or meeting dates or meeting locations in headers, etc.) which were generally sorted out in a reasonably timely fashion. The document web site contains an archive of each upload.

### Outputs of the preceding meeting

All output documents of the previous meeting, particularly the meeting report JVET-N1000, the Versatile Video Coding specification text (Draft 5) JVET-N1001, the Algorithm description for Versatile Video Coding and Test Model 5 (VTM 5) JVET-N1002, the Guidelines for VVC software development JVET-N1003, the Methodology and reporting template for coding tool testing JVET-N1005, the JVET common test conditions and software reference configurations for SDR video JVET-N1010 and for HDR/WCG video JVET-N1011, and the Description of Core Experiments 1 through 11 (JVET-N1021 through JVET-N1031), had been completed and were approved. The software implementation of VTM (versions 5.0 and 5.1) was also approved.

The group was initially asked to review the meeting report of the previous meeting for finalization. The meeting report was later approved without modification.

The available output documents of the previous meeting and the software had been made available in a reasonably timely fashion.

## Attendance

The list of participants in the JVET meeting can be found in Annex B of this report.

The meeting was open to those qualified to participate either in ITU-T WP3/16 or ISO/IEC JTC 1/‌SC 29/‌WG 11 (including experts who had been personally invited as permitted by ITU-T or ISO/IEC policies).

Participants had been reminded of the need to be properly qualified to attend. Those seeking further information regarding qualifications to attend future meetings may contact the responsible coordinators.

## Agenda

The agenda for the meeting was as follows:

* Opening remarks and review of meeting logistics and communication practices
* IPR policy reminder and declarations
* Contribution document allocation
* Review of results of the previous meeting
* Reports of *ad hoc* group (AHG) activities
* Reports of core experiments planned at the previous meeting
* Consideration of contributions and communications on project guidance
* Consideration of additional video coding technology contributions
* Consideration of information contributions
* Coordination activities
* Approval of output documents and associated editing periods
* Future planning: Determination of next steps, discussion of working methods, communication practices, establishment of coordinated experiments, establishment of AHGs, meeting planning, other planning issues
* Other business as appropriate for consideration

## IPR policy reminder

Participants were reminded of the IPR policy established by the parent organizations of the JVET and were referred to the parent body websites for further information. The IPR policy was summarized for the participants.

The ITU-T/ITU-R/ISO/IEC common patent policy shall apply. Participants were particularly reminded that contributions proposing normative technical content shall contain a non-binding informal notice of whether the submitter may have patent rights that would be necessary for implementation of the resulting standard. The notice shall indicate the category of anticipated licensing terms according to the ITU-T/ITU-R/ISO/IEC patent statement and licensing declaration form.

This obligation is supplemental to, and does not replace, any existing obligations of parties to submit formal IPR declarations to ITU-T/ITU-R/ISO/IEC.

Participants were also reminded of the need to formally report patent rights to the top-level parent bodies (using the common reporting form found on the database listed below) and to make verbal and/or document IPR reports within the JVET necessary in the event that they are aware of unreported patents that are essential to implementation of a standard or of a draft standard under development.

Some relevant links for organizational and IPR policy information are provided below:

* <http://www.itu.int/ITU-T/ipr/index.html> (common patent policy for ITU-T, ITU-R, ISO, and IEC, and guidelines and forms for formal reporting to the parent bodies)
* <http://ftp3.itu.int/av-arch/jvet-site> (JVET contribution templates)
* <http://www.itu.int/ITU-T/dbase/patent/index.html> (ITU-T IPR database)
* <http://www.itscj.ipsj.or.jp/sc29/29w7proc.htm> (JTC 1/‌SC 29 Procedures)

It is noted that the ITU TSB director’s AHG on IPR had issued a clarification of the IPR reporting process for ITU-T standards, as follows, per SG 16 TD 327 (GEN/16):

“TSB has reported to the TSB Director’s IPR Ad Hoc Group that they are receiving Patent Statement and Licensing Declaration forms regarding technology submitted in Contributions that may not yet be incorporated in a draft new or revised Recommendation. The IPR Ad Hoc Group observes that, while disclosure of patent information is strongly encouraged as early as possible, the premature submission of Patent Statement and Licensing Declaration forms is not an appropriate tool for such purpose.

In cases where a contributor wishes to disclose patents related to technology in Contributions, this can be done in the Contributions themselves, or informed verbally or otherwise in written form to the technical group (e.g. a Rapporteur’s group), disclosure which should then be duly noted in the meeting report for future reference and record keeping.

It should be noted that the TSB may not be able to meaningfully classify Patent Statement and Licensing Declaration forms for technology in Contributions, since sometimes there are no means to identify the exact work item to which the disclosure applies, or there is no way to ascertain whether the proposal in a Contribution would be adopted into a draft Recommendation.

Therefore, patent holders should submit the Patent Statement and Licensing Declaration form at the time the patent holder believes that the patent is essential to the implementation of a draft or approved Recommendation.”

The responsible coordinators invited participants to make any necessary verbal reports of previously-unreported IPR in technology that might be considered as prospective candidate for inclusion in future standards, and opened the floor for such reports: No such verbal reports were made.

## Software copyright disclaimer header reminder

It was noted that the VTM software implementation package uses the same software copyright license header as the HEVC reference software, where the latter had been agreed at the 5th meeting of the JCT-VC and approved by both parent bodies at their collocated meetings at that time. This license header language is based on the BSD license with a preceding sentence declaring that other contributor or third party rights, including patent rights, are not granted by the license, as recorded in [N 10791](http://phenix.it-sudparis.eu/mpeg/doc_end_user/current_document.php?id=27881&id_meeting=16) of the 89th meeting of ISO/IEC JTC 1/‌SC 29/‌WG 11. Both ITU and ISO/IEC will be identified in the <OWNER> and <ORGANIZATION> tags in the header. This software is used in the process of designing the VTM software, and for evaluating proposals for technology to be potentially included in the design. This software or parts thereof might be published by ITU-T and ISO/IEC as an example implementation of a future video coding standard and for use as the basis of products to promote adoption of such technology.

Different copyright statements shall not be committed to the committee software repository (in the absence of subsequent review and approval of any such actions). As noted previously, it must be further understood that any initially-adopted such copyright header statement language could further change in response to new information and guidance on the subject in the future.

These considerations apply to the 360Lib video conversion software and and HDRtools as well.

## Communication practices

The documents for the meeting can be found at <http://phenix.it-sudparis.eu/jvet/>.

It was reminded to send a notice to the chairs in cases of changes to document titles, authors etc.

JVET email lists are managed through the site <https://lists.rwth-aachen.de/postorius/lists/jvet.lists.rwth-aachen.de/>, and to send email to the reflector, the email address is [jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de). Only members of the reflector can send email to the list. However, membership of the reflector is not limited to qualified JVET participants.

It was emphasized that reflector subscriptions and email sent to the reflector must use real names when subscribing and sending messages and subscribers must respond to inquiries regarding the nature of their interest in the work. The current number of subscribers was 1179.

For distribution of test sequences, a password-protected ftp site had been set up at RWTH Aachen University, with a mirror site at FhG-HHI. Accredited members of JVET may contact the responsible JVET coordinators to obtain the password information (but the site is not open for use by others).

## Terminology

Some terminology used in this report is explained below:

(check for completeness with JVET-N0013, and draft text)

* **ACT**: Adaptive colour transform.
* **AI**: All-intra.
* **AIF**: Adaptive interpolation filtering.
* **ALF**: Adaptive loop filter.
* **AMP**: Asymmetric motion partitioning – a motion prediction partitioning for which the sub-regions of a region are not equal in size (in HEVC, being N/2x2N and 3N/2x2N or 2NxN/2 and 2Nx3N/2 with 2N equal to 16 or 32 for the luma component).
* **AMVP**: Adaptive motion vector prediction.
* **AMT or MTS**: Adaptive multi-core transform, or multiple transform set.
* **AMVR**: (Locally) adaptive motion vector resolution.
* **APS**: Adaptation parameter set.
* **ARC**: Adaptive resolution conversion (synonymous with DRC, and a form of RPR).
* **ARSS**: Adaptive reference sample smoothing.
* **ATMVP or “**subblock-based temporal merging candidates**”** : Alternative temporal motion vector prediction.
* **AU**: Access unit.
* **AUD**: Access unit delimiter.
* **AVC**: Advanced video coding – the video coding standard formally published as ITU-T Recommendation H.264 and ISO/IEC 14496-10.
* **BA**: Block adaptive.
* **BC**: See CPR or IBC.
* **BD**: Bjøntegaard-delta – a method for measuring percentage bit rate savings at equal PSNR or decibels of PSNR benefit at equal bit rate (e.g., as described in document VCEG-M33 of April 2001).
* **BDOF**: Bi-directional optical flow (formerly known as **BIO**).
* **BDPCM**: …
* **BL**: Base layer.
* **BMS**: Benchmark set (no longer used), a former preliminary compilation of coding tools on top of VTM, which provide somewhat better compression performance, but are not deemed mature for standardzation.
* **BoG**: Break-out group.
* **BCW**: Bi-prediction with CU based weighting
* **BR**: Bit rate.
* **BV**: Block vector (used for intra BC prediction).
* **CABAC**: Context-adaptive binary arithmetic coding.
* **CBF**: Coded block flag(s).
* **CC**: May refer to context-coded, common (test) conditions, or cross-component.
* **CCLM**: Cross-component linear model.
* **CCP**: Cross-component prediction.
* **CE**: Core Experiment – a coordinated experiment conducted toward assessment of coding technology.
* **CG**: Coefficient group.
* **CGS**: Colour gamut scalability (historically, coarse-grained scalability).
* **CIIP**: Combined Inter/Intra prediction.
* **CL-RAS**: Cross-layer random-access skip.
* **CPMV**: Control-point motion vector.
* **CPMVP**: Control-point motion vector prediction (used in affine motion model).
* **CPR**: Current-picture referencing, also known as IBC – a technique by which sample values are predicted from other samples in the same picture by means of a displacement vector called a block vector, in a manner conceptually similar to motion-compensated prediction.
* **CTC**: Common test conditions.
* **CVS**: Coded video sequence.
* **DCT**: Discrete cosine transform (sometimes used loosely to refer to other transforms with conceptually similar characteristics).
* **DCTIF**: DCT-derived interpolation filter.
* **DF**: Deblocking filter.
* **DMVR**: Decoder-side motion vector refinement.
* **DRC**: Dynamic resolution conversion (synonymous with ARC, and a form of RPR).
* **DT**: Decoding time.
* **ECS**: Entropy coding synchronization (typically synonymous with WPP).
* **EMT**: Explicit multiple-core transform.
* **EOTF**: Electro-optical transfer function – a function that converts a representation value to a quantity of output light (e.g., light emitted by a display.
* **EPB**: Emulation prevention byte (as in the emulation\_prevention\_byte syntax element).
* **ECV**: Extended Colour Volume (up to WCG).
* **EL**: Enhancement layer.
* **ET**: Encoding time.
* **FRUC**: Frame rate up conversion (pattern matched motion vector derivation).
* **GBI**: …
* **GRA**: …
* **HDR**: High dynamic range.
* **HEVC**: High Efficiency Video Coding – the video coding standard developed and extended by the JCT-VC, formalized by ITU-T as Rec. ITU-T H.265 and by ISO/IEC as ISO/IEC 23008-2.
* **HLS**: High-level syntax.
* **HM**: HEVC Test Model – a video coding design containing selected coding tools that constitutes our draft standard design – now also used especially in reference to the (non-normative) encoder algorithms (see WD and TM).
* **HMVP**: History based motion vector prediction.
* **HyGT**: Hyper-cube Givens transform (a type of NSST).
* **IBC** (also **Intra BC**): Intra block copy, also known as CPR – a technique by which sample values are predicted from other samples in the same picture by means of a displacement vector called a block vector, in a manner conceptually similar to motion-compensated prediction.
* **IBDI**: Internal bit-depth increase – a technique by which lower bit-depth (8 bits per sample) source video is encoded using higher bit-depth signal processing, ordinarily including higher bit-depth reference picture storage (ordinarily 12 bits per sample).
* **IBF**: Intra boundary filtering.
* **ILP**: Inter-layer prediction (in scalable coding).
* **IPCM**: Intra pulse-code modulation (similar in spirit to IPCM in AVC and HEVC).
* **ISP**: Intra subblock partitioning
* **JEM**: Joint exploration model – the software codebase for future video coding exploration.
* **JM**: Joint model – the primary software codebase that has been developed for the AVC standard.
* **JSVM**: Joint scalable video model – another software codebase that has been developed for the AVC standard, which includes support for scalable video coding extensions.
* **KLT**: Karhunen-Loève transform.
* **LB** or **LDB**: Low-delay B – the variant of the LD conditions that uses B pictures.
* **LD**: Low delay – one of two sets of coding conditions designed to enable interactive real-time communication, with less emphasis on ease of random access (contrast with RA). Typically refers to LB, although also applies to LP.
* **LFNST**: …
* **LIC**: Local illumination compensation.
* **LM**: Linear model.
* **LMCS**: Luma mapping with chroma scaling (formerly sometimes called “in-loop reshaping”)
* **LP** or **LDP**: Low-delay P – the variant of the LD conditions that uses P frames.
* **LUT**: Look-up table.
* **LTRP**: Long-term reference pictures.
* **MC**: Motion compensation.
* **MCP**: Motion compensated prediction.
* **MDNSST**: Mode dependent non-separable secondary transform.
* **MIP**: …
* **MMLM**: Multi-model (cross component) linear mode.
* **MPEG**: Moving picture experts group (WG 11, the parent body working group in ISO/IEC JTC 1/‌SC 29, one of the two parent bodies of the JVET).
* **MPM**: Most probable mode (in intra prediction).
* **MV**: Motion vector.
* **MVD**: Motion vector difference.
* **NAL**: Network abstraction layer (as in AVC and HEVC).
* **NSQT**: Non-square quadtree.
* **NSST**: Non-separable secondary transform.
* **NUH**: NAL unit header.
* **NUT**: NAL unit type (as in AVC and HEVC).
* **OBMC**: Overlapped block motion compensation (e.g., as in H.263 Annex F).
* **OETF**: Opto-electronic transfer function – a function that converts to input light (e.g., light input to a camera) to a representation value.
* **OOTF**: Optical-to-optical transfer function – a function that converts input light (e.g. l,ight input to a camera) to output light (e.g., light emitted by a display).
* **PDPC**: Position dependent (intra) prediction combination.
* **PMMVD**: Pattern-matched motion vector derivation.
* **POC**: Picture order count.
* **PoR**: Plan of record.
* **PROF**: …
* **PPS**: Picture parameter set (as in AVC and HEVC).
* **QM**: Quantization matrix (as in AVC and HEVC).
* **QP**: Quantization parameter (as in AVC and HEVC, sometimes confused with quantization step size).
* **QT**: Quadtree.
* **BT**: Binary tree.
* **TT**: Ternary tree.
* **RA**: Random access – a set of coding conditions designed to enable relatively-frequent random access points in the coded video data, with less emphasis on minimization of delay (contrast with LD).
* **RADL**: Random-access decodable leading.
* **RASL**: Random-access skipped leading.
* **R-D**: Rate-distortion.
* **RDO**: Rate-distortion optimization.
* **RDOF**: …
* **RDOQ**: Rate-distortion optimized quantization.
* **RDPCM**: …
* **ROT**: Rotation operation for low-frequency transform coefficients.
* **RPLM**: Reference picture list modification.
* **RPR**: Reference picture resampling (e.g., as in H.263 Annex P), a special case of which is also known as ARC or DRC.
* **RPS**: Reference picture set.
* **RQT**: Residual quadtree.
* **RRU**: Reduced-resolution update (e.g. as in H.263 Annex Q).
* **RVM**: Rate variation measure.
* **SAO**: Sample-adaptive offset.
* **SBT**: Subblock transform.
* **SbTMVP**: Subblock based temporal motion vector prediction.
* **SD**: Slice data; alternatively, standard-definition.
* **SDT**: Signal-dependent transform.
* **SEI**: Supplemental enhancement information (as in AVC and HEVC).
* **SH**: Slice header.
* **SHM**: Scalable HM.
* **SHVC**: Scalable high efficiency video coding.
* **SIMD**: Single instruction, multiple data.
* **SMVD**: Symmetric MVD.
* **SPS**: Sequence parameter set (as in AVC and HEVC).
* **STMVP**: Spatial-temporal motion vector prediction.
* **TBA/TBD/TBP**: To be announced/determined/presented.
* **TGM**: Text and graphics with motion – a category of content that primarily contains rendered text and graphics with motion, mixed with a relatively small amount of camera-captured content.
* **TPM**: Triangular partitioning mode
* **UCBDS**: Unrestricted center-biased diamond search.
* **UWP**: Unequal weight prediction.
* **VCEG**: Visual coding experts group (ITU-T Q.6/16, the relevant rapporteur group in ITU-T WP3/16, which is one of the two parent bodies of the JVET).
* **VPS**: Video parameter set – a parameter set that describes the overall characteristics of a coded video sequence – conceptually sitting above the SPS in the syntax hierarchy.
* **VTM**: VVC Test Model.
* **VVC**: Versatile Video Coding, the standardization project developed by JVET.
* **WAIP**: …
* **WCG**: Wide colour gamut.
* **WG**: Working group, a group of technical experts (usually used to refer to WG 11, a.k.a. MPEG).
* **WPP**: Wavefront parallel processing (usually synonymous with ECS).
* Block and unit names in HEVC:
  + **CTB**: Coding tree block (luma or chroma) – unless the format is monochrome, there are three CTBs per CTU.
  + **CTU**: Coding tree unit (containing both luma and chroma, synonymous with LCU), with a size of 16x16, 32x32, or 64x64 for the luma component.
  + **CB**: Coding block (luma or chroma), a luma or chroma block in a CU.
  + **CU**: Coding unit (containing both luma and chroma), the level at which the prediction mode, such as intra versus inter, is determined in HEVC, with a size of 2Nx2N for 2N equal to 8, 16, 32, or 64 for luma.
  + **PB**: Prediction block (luma or chroma), a luma or chroma block of a PU, the level at which the prediction information is conveyed or the level at which the prediction process is performed in HEVC.
  + **PU**: Prediction unit (containing both luma and chroma), the level of the prediction control syntax within a CU, with eight shape possibilities in HEVC:
    - **2Nx2N**: Having the full width and height of the CU.
    - **2NxN (or Nx2N)**: Having two areas that each have the full width and half the height of the CU (or having two areas that each have half the width and the full height of the CU).
    - **NxN**: Having four areas that each have half the width and half the height of the CU, with N equal to 4, 8, 16, or 32 for intra-predicted luma and N equal to 8, 16, or 32 for inter-predicted luma – a case only used when 2N×2N is the minimum CU size.
    - **N/2x2N** paired with **3N/2x2N** or **2NxN/2** paired with **2Nx3N/2**: Having two areas that are different in size – cases referred to as AMP, with 2N equal to 16 or 32 for the luma component.
  + **TB**: Transform block (luma or chroma), a luma or chroma block of a TU, with a size of 4x4, 8x8, 16x16, or 32x32.
  + **TU**: Transform unit (containing both luma and chroma), the level of the residual transform (or transform skip or palette coding) segmentation within a CU (which, when using inter prediction in HEVC, may sometimes span across multiple PU regions).
* Block and unit names in VVC:
  + **CTB**: Coding tree block (luma or chroma) – there are three CTBs per CTU in a P or B slice or in an I slice that uses a single tree, and one CTB per luma CTU and two CTBs per chroma CTU in an I slice that uses separate trees.
  + **CTU**: Coding tree unit (synonymous with LCU, containing both luma and chroma in a P or B slice or in an I slice that uses a single tree, containing only luma or only chroma in an I slice that uses separate trees), with a size of 16x16, 32x32, 64x64, or 128x128 for the luma component.
  + **CB**: Coding block, a luma or chroma block in a CU.
  + **CU**: Coding unit (containing both luma and chroma in P/B slice, containing only luma or chroma in I slice), a leaf node of a QTBT. It’s the level at which the prediction process and residual transform are performed in JEM. A CU can be square or rectangle shape.
  + **PB**: Prediction block, a luma or chroma block of a PU.
  + **PU**: Prediction unit, has the same size as a CU in the VVC context.
  + **TB**: Transform block, a luma or chroma block of a TU.
  + **TU**: Transform unit, has the same size as a CU in the VVC context.

## Opening remarks

Remarks during the opening session of the meeting 0900 Wednesday 3 July (initially chaired by GJS and later joined by JRO) were as follows.

* The meeting logistics, agenda, working practices, policies, and document allocation were reviewed.
* The results of the previous meeting were reviewed.
* On placeholders – there were a number of cases where there was some description of a concept but no test results (see section 2.4.2).
* The primary goals of the meeting were to review the results of CEs, identify promising technology directions, and adopt proposed technology into the VVC draft text and VTM.
* Due to the high number of input contributions, parallelization and breakout work were planned to be used at the meeting.
* Planning of viewing & equipment setup
* Principles of standards development were discussed.
* Standardization timelines [CD July, DIS October (possibly Jan), Consent & FDIS July; or CD October, DIS Jan, Consent & FDIS July]

## Scheduling of discussions

Scheduling: Generally meeting time was scheduled during 0900–2100+ hours, with coffee and lunch breaks as convenient. Ongoing scheduling refinements were announced on the group email reflector as needed. Some particular scheduling notes are shown below, although not necessarily 100% accurate or complete:

* Wed. 3 July, 1st day
  + 0900– JVET opening plenary (chaired by GJS & JRO)
  + …
* …

## Contribution topic overview

The approximate subject categories and quantity of contributions per category for the meeting were summarized as follows (note that the noted document counts do not include crosschecks, and may not be completely accurate):

* AHG reports (17) (section 3) (Plenary)
* Project development (X) (section 4) (Plenary)
  + Text and software development (1)
  + Test conditions (1)
  + Performance assessment (5)
  + Coding studies on specific use cases (0)
  + Test Material (0)
* Core Experiments (xx) (section 5) with subtopics
  + CE1: CABAC initialization (4) (section 5.1) (Track A)
  + CE2: Luma/chroma dependency reduction (5) (section 5.2) (Track B)
  + CE3: Intra prediction and mode coding (12) (section 5.3) (Track A)
  + CE4: Inter prediction (8) (section 5.4) (Track B)
  + CE5: Loop filtering (7) (section 5.5) (Track B)
  + CE6: Transforms and transform signalling (4) (section 5.6) (Track A)
  + CE7: Quantization and coefficient coding (8) (section 5.7) (Track B)
  + CE8: Screen content coding tools (16) (section 5.8) (Track B)
  + CE9: Decoder motion vector derivation (7) (section 5.9) (Track B)
  + CE10: Neural network based loop filtering (2) (section 5.10) (Track B)
  + CE11: Gradual random access (6) (section 5.11) (Track A)
* Non-CE technology proposals (xx) (section 6) with subtopics
  + CE1 related – CABAC initialization (3) (section 6.1) (Track A)
  + CE2 related – Luma/chroma dependency reduction (23) (section 6.2) (Track B)
  + CE3 related – Intra prediction and mode coding (96) (section 6.3) (Track A)
  + CE4 related – Inter prediction (104) (section 6.4) (Track B)
  + CE5 related – Loop filtering (38) (section 6.5) (Track B)
  + CE6 related – Transforms and transform signalling (50) (section 6.6) (Track A)
  + CE7 related – Quantization and coefficient coding (25) (section 6.7) (Track B)
  + CE8 related – Screen content coding tools (46) (section 6.8) (Track B)
  + CE9 related – Decoder motion vector derivation (26) (section 6.9) (Track B)
  + CE10 related – Neural network based loop filtering (2) (section 6.10) (Track B)
  + CE11 related – Gradual random access (0) (section 6.11) (Track A)
  + Quantization parameter signalling (8) (section 6.12) (Track B)
  + Entropy coding (6) (section 6.13) (Track A)
  + Chroma sampling and chroma formats (2) (section 6.14) (Track A)
  + Other coding tools (13) (section 6.15) (Track B)
  + 360 degree video (4) (section 6.16) (Track X)
  + AHG17: General high-level syntax (60) (section 6.17) (Track A)
  + AHG12: High-level parallelism and coded pisture regions (41) (section 6.18) (Track A)
  + AHG8: Layered coding and resolution adaptation (24) (section 6.19) (Track A)
* Complexity analysis and reduction (0) (section 7) (Track A)
* Encoder optimization (4) (section 8) (Track A)
* Metrics and evaluation criteria (1) (section 9) (Track A)
* Withdrawn (6) (Track none)
* Joint meetings, plenary discussions, BoG reports, Summary of actions (section 10)
* Project planning (section 12)
* Establishment of AHGs (section 13)
* Output documents (section 14)
* Future meeting plans and concluding remarks (section 15)

The document counts above do not include cross-checks and CE summary reports.

Track A (313) was generally chaired by GJS, and Track B (338) by JRO.

# AHG reports (17)

These reports were discussed Wednesday 3 July XXXX–XXXX (chaired by GJS and JRO).

[JVET-O0001](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7578) JVET AHG report: Project management (AHG1) [J.-R. Ohm, G. J. Sullivan]

JVET AHG report: Draft text and test model algorithm description editing (AHG2) [B. Bross, J. Chen, J. Boyce, S. Kim, S. Liu, Y. Ye]

JVET AHG report: Test model software development (AHG3) [F. Bossen, X. Li, A. Norkin, K. Sühring]

[JVET-O0004](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7544) JVET AHG report: Test material and visual assessment (AHG4) [T. Suzuki, M. Wien, V. Baroncini, R. Chernyak, P. Hanhart, A. Norkin, J. Ye]

[JVET-O0005](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7546) JVET AHG report: Memory bandwidth consumption of coding tools (AHG5) [R. Hashimoto, T. Ikai, X. Li, D. Luo, H. Yang, M. Zhou]

[JVET-O0006](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7543) JVET AHG Report: 360° video coding tools, software and test conditions (AHG6) [Y. He, K. Choi, J. Boyce, P. Hanhart, J.-L. Lin]

JVET AHG Report: Coding of HDR/WCG material (AHG7) [A. Segall, E. François, W. Husak, D. Rusanovskyy]

JVET AHG Report: Layered coding and resolution adaptivity (AHG8) [S. Wenger, A. Segall, M. M. Hannuksela, Hendry, S. McCarthy, Y.-C. Sun, P. Topwala, M. Zhou]

[JVET-O0009](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7565) JVET AHG report: Neural Networks in Video Coding (AHG9) [S. Liu, K. Kawamura, Y. Li, L. Wang, P. Wu, H. Yang]

JVET AHG Report: Encoding algorithm optimization (AHG10) [A. Duenas, A. Tourapis (co-chairs), C. Helmrich, S. Ikonin, A. Norkin, R. Sjöberg (vice-chairs)]

[JVET-O0011](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7567) JVET AHG report: Screen Content Coding (AHG11) [S. Liu, J. Boyce, A. Filippov, Y.-C. Sun, J. Xu, M. Zhou]

[JVET-O0012](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7576) JVET AHG report: High-level parallelism and coded picture regions (AHG12) [S. Deshpande, M. M. Hannuksela, R Sjöberg, R. Skupin, W. Wan, Y.-K. Wang, S. Wenger]

JVET AHG report: Tool reporting procedure (AHG13) [W.-J. Chien, J. Boyce, Y.-W. Chen, R. Chernyak, K. Choi, R. Hashimoto, Y.-W. Huang, H. Jang, R.-L. Liao, S. Liu, D. Luo]

[JVET-O0014](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7581) JVET AHG report: Operation modes for low latency support (AHG14) [J.-M. Thiesse, S. Deshpande, A. Duenas, Hendry, K. Kazui, R. Sjöberg, A. Tourapis]

[JVET-O0015](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7500) JVET AHG report: Quantization control (AHG15) [R. Chernyak, E. François, C. Helmrich, S. McCarthy, A. Segall]

[JVET-O0016](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7349) JVET AHG report: Implementation studies (AHG16) [M. Zhou, J. An, E. Chai, K. Choi, S. Sethuraman, T. Hsieh, X. Xiu]

JVET AHG report: High-level syntax (AHG17) [R. Sjöberg, J. Boyce, S. Deshpande, M. M. Hannuksela, R. Skupin, A. Tourapis, Y.-K. Wang, W. Wan, S. Wenger]

# Project development (X)

Contributions in this category were discussed XXday X July XXXX–XXXX (chaired by XXX)

## Text and software development (1)

[JVET-O0756](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7377) HDRTools: VTM software integration [D. Mehlem, A.M. Tourapis, D. Singer (Apple)] [late]

## Test conditions (1)

[JVET-O0483](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7090) AHG11: SCC test sequences for VVC standardization and suggestion for class F update [X. Xu, X. Li, S. Liu (Tencent)]

## Performance assessment (5)

[JVET-O0451](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7057) AHG4: Subjective comparison of VVC and HEVC [P. Philippe, J. Fournier (Orange), W. Hamidouche (INSA), J. Y. Aubie (b-com)]

[JVET-O0551](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7164) AHG13: Tool-off tests on OpenImage database images [J. Ström, D. Saffar, P. Wennersten, R. Sjöberg]

[JVET-O0641](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7257) AHG 8: JVET Common test conditions for adaptive resolution change (ARC) [M. G. Sarwer, R.-L. Liao, J. Chen, J. Luo, Y. Ye (Alibaba)]

[JVET-O0657](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7274) AHG7: Tool-on/off experiments for HLG CTC sequences [S. Iwamura, S. Nemoto, A. Ichigaya (NHK)]

[JVET-O0898](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7520) Extra results to JVET-N0605 “Comparative study of video coding solutions VVC, AV1 and EVC versus HEVC” [E. François (InterDigital)] [late] [miss]

## Coding studies on specific use cases (X)

## Test material (X)

# Core Experiments

## CE1: CABAC initialization (4)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0021](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7180) CE1: Summary report on CABAC initialization [F. Bossen, J. Dong, H. Kirchhoffer]

[JVET-O0065](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6669) CE1-1.2 and CE1-2.1: Simplification of CABAC initialization process [J. Dong, A. Said, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0085](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6689) CE1-1.1: Simplification of the initialization process for context variables [H. Kirchhoffer, D. Marpe, H. Schwarz, T. Wiegand (HHI)]

[JVET-O0112](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6716) CE1: CABAC Initialization, all experiments [F. Bossen (Sharp)]

## CE2: Luma/chroma dependency reduction (5)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0022](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7272) CE2: Summary report on luma-chroma dependency reduction [J. Chen, E. François, P. Yin]

[JVET-O0049](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6653) CE2-1.1: Simplified LMCS [Z.-Y. Lin, T.-D. Chuang, C.-Y. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0087](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6691) CE2: report of CE2 experiments from InterDigital [E. François, T. Poirier (InterDigital)]

[JVET-O0098](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6702) CE2.1.2: luma-chroma dependency reduction for LMCS [T. Lu, F. Pu, P. Yin, S. McCarthy, W. Husak, T. Chen (Dolby)]

[JVET-O0102](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6706) CE2-1.4: luma-chroma dependency reduction for chroma scaling [J. Chen, R.-L. Liao, Y. Ye (Alibaba)]

## CE3: Intra prediction and mode coding (12)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0023](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7250) CE3: Summary Report on Intra Prediction and Mode Coding [G. Van der Auwera, L. Li, A. Filippov]

[JVET-O0048](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6652) CE3: Simplification on Intra sub-partition coding mode (tests 3-1.2.1 and 3-1.2.2) [B. Wang, A. M. Kotra, S. Esenlik, H. Gao, E. Alshina (Huawei)]

[JVET-O0050](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6654) CE3-2.1.1 and CE3-2.1.2: Removing 2x2, 2x4, and 4x2 chroma CBs [Z.-Y. Lin, T.-D. Chuang, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek), Y. Zhao, H. Yang (Huawei)]

[JVET-O0080](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6684) CE3-1.1: CU-based prediction for 1xN and 2xN sub-partitions [K. Unno, K. Kawamura, S. Naito (KDDI)]

[JVET-O0088](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6692) CE3-3.1: Modified beta derivation for CCLM [M. Ikeda, T. Suzuki (Sony)]

[JVET-O0095](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6699) CE3-3.2: Simplified and robust CCLM parameter derivation [A. Filippov, V. Rufitskiy, E. Alshina (Huawei), C. Helmrich, H. Schwarz, D. Marpe, T. Wiegand (HHI)]

[JVET-O0099](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6703) CE3-1.3: CU-level low latency intra sub-partitions for 4x8 and 8x4 CUs [H. Yang, Y. He, R. Vanam (InterDigital)]

[JVET-O0106](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6710) CE3-1.6: On 1xN and 2xN subblocks of ISP [A. K. Ramasubramonian, G. Van der Auwera, T. Hsieh, V. Seregin, L. Pham Van, M. Karczewicz (Qualcomm), S. De-Luxan Hernandez, B. Bross, T. Nguyen, V. George, B. Stabernack, H. Schwarz, D. Marpe, T. Wiegand (HHI)]

[JVET-O0174](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6778) Crosscheck of JVET-O0106 (CE3-1.6: On 1xN and 2xN subblocks of ISP) [H. Yang (InterDigital)]

[JVET-O0107](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6711) CE3-1.7: On 1xN and 2xN subblocks of ISP with vertical intra mode restriction for 4xN vertically split ISP-coded blocks [A. K. Ramasubramonian, G. Van der Auwera, T. Hsieh, V. Seregin, L. Pham Van, M. Karczewicz (Qualcomm)]

[JVET-O0109](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6713) CE3-1.4: Intra Sub-Partitions Coding without Thin Partitions [T.-C. Ma, X. Xiu, Y.-W. Chen, X. Wang, H.-J. Jhu (Kwai Inc.)]

[JVET-O0118](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6722) CE3-2.2: Enabling parallel reconstruction of small intra-coded chroma blocks [L. Pham Van, G. Van der Auwera, A. K. Ramasubramonian, V. Seregin, H. Huang, T. Hsieh, M. Karczewicz (Qualcomm), Z.-Y. Lin, T.-D. Chuang, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

## CE4: Inter prediction (8)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0024](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7289) CE4: Summary report on inter prediction [H. Yang, C.-C. Chen]

[JVET-O0051](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6655) CE4-2.5: Phase-variant affine MC [T.-D. Chuang, C.-Y. Lai, Z.-Y. Lin, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0901](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7523) Crosscheck of JVET-O0051 (CE4-2.5: Phase-variant affine MC) [H. Huang (Qualcomm)]

[JVET-O0057](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6661) CE4: Switchable interpolation filter [A. Henkel, I. Zupancic, B. Bross, M. Winken, H. Schwarz, D. Marpe, T. Wiegand (HHI)]

[JVET-O0066](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6670) CE4-3.1a and CE4-3.1b: Unidirectional local illumination compensation with affine prediction [V. Seregin, W.-J. Chien, T. Hsieh, N. Hu, M. Karczewicz (Qualcomm), C.-M. Tsai, C.-C. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek), H. Chen, X. Ma, H. Yang (Huawei)]

[JVET-O0069](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6673) CE4-2.3: Interweaved Prediction for Affine Motion Compensation [K. Zhang, L. Zhang, H. Liu, J. Xu, Y. Wang (Bytedance)]

[JVET-O0070](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6674) CE4-2.1: Prediction refinement with optical flow for affine mode [J. Luo, Y. He (InterDigital)]

[JVET-O0082](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6686) CE4-2.4: Simplifications of interweaved affine mode [G. Li, X. Li, X. Xu, S. Liu (Tencent)]

[JVET-O0086](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6690) CE4-3.2a and CE4-3.2b: Unidirectional local illumination compensation with affine prediction and simplified linear model parameter derivation based on min / max method [A. Filippov, V. Rufitskiy, E. Alshina (Huawei)]

[JVET-O0890](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7512) Crosscheck of JVETO0086 (CE4-3.2a and CE4-3.2b: Unidirectional local illumination compensation with affine prediction and simplified linear model parameter derivation based on min / max method) [F. Henry (Orange), G. Clare (bcom)]

## CE5: Loop filtering (7)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0025](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7295) CE5: Summary Report on Loop Filtering [A. Norkin, C.-Y. Chen] [miss] [late]

[JVET-O0060](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6664) CE5-2.1 and CE5-2.2: Deblocking on 4x4 sample grids [K. Andersson, J. Enhorn, Z. Zhang, R. Sjöberg (Ericsson), A. M. Kotra, S. Esenlik, B. Wang, H. Gao, E. Alshina (Huawei), C.-M. Tsai, C.-W. Hsu, T.-D. Chuang, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0061](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6665) CE5-3.1 Sub-sample MV threshold for deblocking decisions [K. Andersson, J. Enhorn (Ericsson)]

[JVET-O0083](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6687) CE5-1.1: Modification of sub-pu deblocking and longer tap filter [A. M. Kotra, S. Esenlik, B. Wang, H. Gao, E. Alshina (Huawei)]

[JVET-O0090](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6694) CE5-4: alternative luma filter sets and alternative chroma filters for ALF [J. Taquet, P. Onno, C. Gisquet, G. Laroche (Canon)]

[JVET-O0091](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6695) CE5-2.3 and CE5-2.4: filter length decision which support parallel processing and aligned filtering block edge [H. Jang, J. Nam, N. Park, J. Lim, S. Kim (LGE)]

[JVET-O0092](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6696) CE5-2.5: De-block filtering with 4x4 chroma sample grid [H. Jang, J. Nam, N. Park, J. Lim, S. Kim (LGE)]

## CE6: Transforms and transform signalling (4)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0026](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7166) CE6: Summary Report on Transforms and Transform Signalling [X. Zhao, H.E. Egilmez]

[JVET-O0062](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6666) CE6-1.2 Transform Candidate Ordering [C. Hollmann, D. Saffar, P. Wennersten, J. Ström (Ericsson)]

[JVET-O0076](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6680) CE6-1.1: An Explicit MTS Design with Fast Encoder [C.-H. Hung, H. E. Egilmez, N. Hu, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0094](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6698) CE6-2.1: Simplification of Low Frequency Non-Separable Transform [M. Siekmann, H. Schwarz, D. Marpe, T. Wiegand (HHI)]

## CE7: Quantization and coefficient coding (8)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0027](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6795) CE7: Summary report on quantization and coefficient coding [H. Schwarz, M. Coban]

[JVET-O0052](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6656) CE7-1: TB-level constraints on context-coded bins for coefficient coding [T.-D. Chuang, C.-Y. Chen, S.-T. Hsiang, Y.-W. Huang, S.-M. Lei (MediaTek), M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0089](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6693) CE7-3.3/4: Context Modelling of sign for TS residual coding [T. Nguyen, B. Bross, H. Schwarz., D. Marpe, T. Wiegand (HHI)]

[JVET-O0104](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6708) CE7: Modified limitation on context coded bins for residual coding of Transform Skip mode (CE7-3.5 and CE7-3.6) [X. Zhao, X. Li, X. Xu, S. Liu (Tencent)]

[JVET-O0105](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6709) CE7: Joint chroma residual coding with multiple modes (tests CE7-2.1, CE7-2.2) [C. Helmrich, H. Schwarz, T. Nguyen, C. Rudat, D. Marpe, T. Wiegand (Fraunhofer HHI), B. Ray, G. Van der Auwera, A. K. Ramasubramonian, M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0110](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6714) CE7-4.1: Simplification of cbf coding [Y.-W. Chen, X. Xiu, T.-C. Ma, X. Wang (Kwai Inc.)]

[JVET-O0115](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6719) CE7-2.3: Joint coding of chroma residuals [B. Ray, G. Van der Auwera, A. K. Ramasubramonian, M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0122](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6726) CE7: Sign context, level mapping, and bitplane coding for TS residual coding (CE7-3.7, CE7-3.8, CE7-3.9, CE7-3.10, and CE7-3.11) [M. Karczewicz, H. Wang, M. Coban, Y.-H. Chao, Y. Han (Qualcomm)]

[JVET-O0692](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7312) Crosscheck of JVET-O0122: CE7-3.9 + parity plane change [T. Nguyen (HHI)]

## CE8: Screen content coding tools (16)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0028](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7052) CE8: Summary Report on Screen Content Coding Tools [X. Xu, Y.-C. Chao, Y.-C. Sun, J. Xu]

[JVET-O0053](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6657) CE8-1.1: Unified MVD coding for inter and IBC [S.-T. Hsiang, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0059](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6663) CE8: Palette mode with neighboring pixel copy (CE8-2.2) [Y.-C. Sun, T.-S. Chang, J. Lou (Alibaba)]

[JVET-O0071](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6675) CE8-2.3: Compound Palette Mode [W. Zhu, J. Xu, L. Zhang, K. Zhang, H. Liu, Y. Wang (Bytedance)]

[JVET-O0072](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6676) CE8-2.5: Combined test of CE8-2.2 and CE8-2.3 [W. Zhu, J. Xu, L. Zhang, K. Zhang, H. Liu (Bytedance), Y.-C. Sun, T.-S. Chang, J. Lou (Alibaba)]

[JVET-O0073](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6677) CE8-1.6: Dedicated IBC Buffer [J. Xu, L. Zhang, K. Zhang, H. Liu, Y. Wang (Bytedance)]

[JVET-O0074](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6678) CE8-1.6-related: IBC Dedicated Buffer with 120x128 8-bit Setting [J. Xu, L. Zhang, K. Zhang, H. Liu, Y. Wang (Bytedance)]

[JVET-O0078](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6682) CE8-1.7: Single HMVP table for all CUs inside the shared merge list region for IBC [S.H. Wang (PKU), X. Zheng (DJI), S.S. Wang, S.W. Ma (PKU), L. Zhang, K. Zhang, H. Liu, J. Xu, Y. Wang (Bytedance), X. Xu, X. Li, G. Li, S. Liu (Tencent)]

[JVET-O0081](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6685) CE8-3.2: Chroma Transform Skip [T. Tsukuba, M. Ikeda, Y. Yagasaki, T. Suzuki (Sony)]

[JVET-O0093](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6697) CE8-1.3: BVD coding method [J. Nam, H. Jang, J. Choi, J. Heo, J. Lim, S. Kim (LGE)]

[JVET-O0097](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6701) CE8-3.1: Enable Transform Skip in CUs using ISP [S. De-Luxán-Hernández, T. Nguyen, B. Bross, H. Schwarz, D. Marpe, T. Wiegand (HHI), T.-C. Ma, X.-Y. Xiu, Y.-W. Chen, X. Wang (Kwai)]

[JVET-O0114](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6718) CE8: Unified MVD and Block Vector Difference Coding (CE8-1.2) [M. Salehifar, S. Paluri, S. Kim (LGE)]

[JVET-O0117](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6721) CE8-1.4 and CE8-1.5: Default Processing for IBC Mode [Y. Han, W.-J. Chien, M. Karczewicz (Qualcomm), J. Li, C. Lim (Panasonic), H. Jang, J. Nam, J. Lim (LGE)]

[JVET-O0119](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6723) CE8-2.1: Palette mode in HEVC [Y.-H. Chao, C.-H. Hung, W.-J. Chien, V. Seregin, M. Karczewicz (Qualcomm), Y.-C. Sun, T.-S. Chang, J. Lou (Alibaba), W. Zhu, L. Zhang, J. Xu, K. Zhang, H. Liu (Bytedance)]

[JVET-O0120](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6724) CE8-2.4: Line-based CG Palette Mode [Y.-H. Chao, C.-H. Hung, W.-J. Chien, T. Hsieh, M. Karczewicz (Qualcomm)]

[JVET-O0121](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6725) CE8-4.2: Dual tree disabled for YUV4:4:4 format [Y.-H. Chao, C.-H. Hung, H. Wang, W.-J. Chien, V. Seregin, M. Karczewicz (Qualcomm)]

## CE9: Decoder motion vector derivation (7)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0029](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7161) CE9: Summary report on decoder motion vector derivation [S. Esenlik, X. Xiu]

[JVET-O0054](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6658) CE9-1.1: BDOF early termination [C.-Y. Lai, T.-D. Chuang, C.-Y. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek), K. Kondo, M. Ikeda, T. Suzuki (Sony)]

[JVET-O0055](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6659) CE9-1.2 and CE9-1.4: BDOF early termination [C.-Y. Lai, T.-D. Chuang, C.-Y. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek), K. Kondo, M. Ikeda, T. Suzuki (Sony), K. Unno, K. Kawamura, S. Naito (KDDI), T. Chujoh, T. Ikai (Sharp)]

[JVET-O0077](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6681) CE9-2.1: Harmonization of DMVR and MMVD [E.Sasaki, T. Hashimoto, T. Chujoh, T. Ikai (Sharp)]

[JVET-O0096](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6700) CE9-1.5: Best DMVR cost based early termination for BDOF [S. Sethuraman (Ittiam)]

[JVET-O0103](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6707) CE9-2.3: CIIP with disabled BDOF [L. Zhao, X. Li, X. Zhao, S. Liu (Tencent)]

[JVET-O0108](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6712) CE9-2.2: Disabled decoder-side motion refinement (DMVR) and bidirectional optical flow (BDOF) for combined inter and intra prediction (CIIP) [X. Xiu, Y.-W. Chen, T.-C. Ma, X. Wang (Kwai Inc.)]

## CE10: Neural network based loop filtering (9)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0030](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7063) CE10: Summary Report on Neural Network based Filter for Video Coding [Y. Li, S. Liu, K. Kawamura]

[JVET-O0056](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6660) CE10-1.2: Convolutional neural network loop filter [Y.-L. Hsiao, O. Chubach, C.-Y. Chen, T.-D. Chuang, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0346](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6951) Crosscheck of JVET-O0056 (CE 10-1.2: Convolutional neural network loop filter) [H. Yin, R. Yang, X. Fang, S. Ma (Intel)]

[JVET-O0063](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6667) CE10-1.7: Adaptive convolutional neural network loop filter [H. Yin, R. Yang, X. Fang, S. Ma (Intel)]

[JVET-O0079](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6683) CE10: Integrated in-loop filter based on CNN (Tests 2.1, 2.2 and 2.3) [S. Wan, M.-Z.Wang, H. Gong, C.-Y. Zou (NPU), Y.-Z. Ma, J.-Y. Huo (Xidian Univ.), Y.-F. Yu, Y. Liu (OPPO)]

[JVET-O0101](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6705) CE10: Dense Residual Convolutional Neural Network based In-Loop Filter (Tests 2.5 and 2.7) [Y. Wang, T. Ouyang, C. Zou, Y. Li, Z. Chen (Wuhan Univ.), L. Zhao, S. Liu, X. Li (Tencent)]

[JVET-O0175](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6779) Crosscheck of JVET-O0101 (CE10: Dense Residual Convolutional Neural Network based In-Loop Filter (Tests 2.5 and 2.7)) [S. Wan, M.-Z.Wang, H. Gong, C.-Y. Zou (NPU), Y.-Z. Ma, J.-Y. Huo (Xidian Univ.), Y.-F. Yu, Y. Liu (OPPO)]

[JVET-O0131](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6735) CE10-1.10/CE10-1.11: Evaluation results of CNN-based filtering with on-line learning model [Y. Kidani, K. Kawamura, K. Unno, S. Naito (KDDI)]

[JVET-O0132](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6736) CE10-2.10/CE10-2.11: Evaluation results of CNN-based filtering with off-line learning model [Y. Kidani, K. Kawamura, K. Unno, S. Naito (KDDI)]

[JVET-O0347](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6952) CE10: Neural Network based Filter for Video Coding [H. Zhao, Y. Dai, D. Liu, N. Yan, H. Li (USTC)] [miss] [late]

## CE11: Gradual random access (6)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0031](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7308) CE11: Summary Report on Gradual Random Access [K. Kazui, J.-M. Thiesse, Hendry, K. Kawamura] [miss] [late]

[JVET-O0075](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6679) CE11-1: GRA support by restriction to encoder operation [K. Kazui (Fujitsu)]

[JVET-O0100](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6704) CE11: Gradual Random Access (GRA) using encoder and normative restrictions (Tests CE11-2.1.a, CE11-2.1.b and CE11-2.2) [D. Gommelet, J.-M. Thiesse, D. Nicholson (VITEC)]

[JVET-O0116](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6720) CE11-4: Distributed decoding refresh DDR [M. Sychev (Huawei)]

[JVET-O0124](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6728) CE11/AHG14: Test 3.1 − Support of gradual random access / gradual decoding refresh [Hendry, Y.-K. Wang, J. Chen, S. Hong (Futurewei)] [late]

[JVET-O0663](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7280) CE11-3.2: Gradual Random Access (GRA) with Sub-Pictures [K. K. Sreedhar, M. M. Hannuksela, A. Aminlou (Nokia)] [late]

# Non-CE Technology proposals

## CE1 related – CABAC initialization (3)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0191](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6796) CE1-related: Simplified CABAC initialization method [F. Bossen (Sharp)]

[JVET-O0677](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7296) Crosscheck of JVET-O0191 (CE1-related: Simplified CABAC initialization method) [J. Dong (Qualcomm)]

[JVET-O0741](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7362) Non-CE1: Context memory reduction via selective adaptation [A. Said, T. Hsieh, J. Dong (Qualcomm)] [late]

[JVET-O0946](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7573) CE1-related: Simplification of JVET-O0191 using 4 or 6 bit per initialization value [H. Kirchhoffer, D. Marpe, H. Schwarz, T. Wiegand (HHI)] [late] [miss]

## CE2 related – Luma/chroma dependency reduction (23)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0129](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6733) Non-CE2: CCLM with Chroma Separate Tree [C.-W. Kuo, J. Li, C. Lim (Panasonic)]

[JVET-O0797](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7418) Crosscheck of JVET-O0129 on CCLM with Chroma Separate Tree [C. Chevance (Interdigital)]

[JVET-O0130](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6734) Non-CE2: CRS with Chroma Separate Tree [C.-W. Kuo, J. Li, C. Lim (Panasonic)]

[JVET-O0723](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7343) Crosscheck of JVET-O0130 (Non-CE2: CRS with Chroma Separate Tree) [T. Biatek (Qualcomm)]

[JVET-O0798](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7419) Crosscheck of JVET-O0130 on CRS with Chroma Separate Tree [C. Chevance (Interdigital)]

[JVET-O0196](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6801) CE2-related: CCLM for dual tree with 32x32 latency [Y. Zhao, H. Yang (Huawei)]

[JVET-O0805](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7426) Crosscheck of JVET-O0196 (CE2-related: CCLM for dual tree with 32x32 latency) [C.-M. Tsai (MediaTek)]

[JVET-O0212](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6817) On an enable condition for LMCS [T.Chujoh, T.Ikai (Sharp)]

[JVET-O0715](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7335) Crosscheck of JVET-O0212 (On an enable condition for LMCS) [P. Bordes (InterDigital)]

[JVET-O0232](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6837) CE2-related: No-latency LMCS Chroma Residual Scaling [J. Zhao, S. Kim (LGE)]

[JVET-O0745](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7366) Crosscheck of JVET-O0232 (CE2-related: No-latency LMCS Chroma Residual Scaling) [T. Lu (Dolby)]

[JVET-O0233](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6838) CE2-related: Linear LMCS [J. Zhao, S. Paluri, S. Kim (LGE)]

[JVET-O0796](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7417) Crosscheck of JVET-O0233 on Linear LMCS [C. Chevance (Interdigital)]

[JVET-O0262](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6867) Non-CE2: Removal of division operations in LMCS [K. Zhang, L. Zhang, H. Liu, J. Xu, Y. Wang (Bytedance)]

[JVET-O0746](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7367) Crosscheck of JVET-O0262 (Non-CE2: Removal of division operations in LMCS) [T. Lu (Dolby)]

[JVET-O0271](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6876) CE2-related: Chroma scaling for CIIP [Z.-Y. Lin, T.-D. Chuang, C.-Y. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0781](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7402) Crosscheck of JVET-O0271: CE2-related: Chroma scaling for CIIP [X. Xiu (Kwai Inc.)]

[JVET-O0272](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6877) CE2-related: Simplified inverse luma mapping [Z.-Y. Lin, T.-D. Chuang, C.-Y. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0910](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7532) Crosscheck of JVET-O0272: CE2-related: Simplified inverse luma mapping [W. Zhu (Bytedance)]

[JVET-O0273](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6878) CE2-related: Luma-chroma latency reduction for chroma separate tree [C.-M. Tsai, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0274](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6879) CE2-related: Luma-chroma dependency reduction for chroma separate tree by constraining CCLM usage [C.-M. Tsai, T.-D. Chuang, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0754](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7375) Crosscheck of JVET-O0274 (CE2-related: Luma-chroma dependency reduction for chroma separate tree by constraining CCLM usage) [Y. Zhao (Huawei)]

[JVET-O0427](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7032) Non-CE2: Remove differential coding for ALF coefficients [X. W. Meng (PKU), X. Zheng (DJI), S. S. Wang, S. W. Ma (PKU)]

[JVET-O0895](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7517) Crosscheck of JVET-O0427 (Non-CE5: Remove differential coding for ALF coefficients) [N. Hu (Qualcomm)]

[JVET-O0428](file:///D:\2019\JVET\JVET-O\current_document.php%3fid=7033) AHG16/AHG17: LMCS related clean-ups [T. Lu, F. Pu, P. Yin, S. McCarthy, W. Husak, T. Chen (Dolby), J. Zhao, S.H. Kim (LGE)]

Note that if the 3rd part of this proposal is agreed, the specification text for that part should be reviewed and confirmed by HLS experts.

[JVET-O0842](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7463) Crosscheck of JVET-O0428: AHG16/AHG17: LMCS related clean-ups [J. Chen (Alibaba)]

[JVET-O0429](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7034) Non-CE2: alternative solutions for chroma residual scaling factors derivation for dualTree [T. Lu, P. Yin (Dolby), E. François, F. Hiron (InterDigital)]

[JVET-O0744](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7365) Crosscheck of JVET-O0429 (Non-CE2: alternative solutions for chroma residual scaling factors derivation for dualTree) [K. Zhang (Bytedance)]

[JVET-O0448](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7054) Non-CE2: Inverse LUT carriage for LMCS [P. Andrivon, E. François, C. Chevance, H. Hiron, R. Jullian (InterDigital)]

[JVET-O0524](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7131) Non-CE2: alternative solutions for reducing the luma-chroma latency [T. Poirier, E. François, F. Le Léannec (InterDigital)]

[JVET-O0806](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7427) Crosscheck of JVET-O0524 (Non-CE2: Alternative solutions for reducing the luma-chroma latency) [C.-M. Tsai (MediaTek)]

[JVET-O0550](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7163) CE2-related: Further simplification of chroma residual scaling factors derivation [M. Sychev, E. Alshina (Huawei)]

[JVET-O0603](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7218) CE2-related: Prediction dependent luma residual scaling for adaptive in-loop re-shaper [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, S. Ye, X. Wang (Kwai Inc.)]

[JVET-O0844](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7465) Crosscheck of JVET-O0603: CE2-related: Prediction dependent luma residual scaling for adaptive in-loop re-shaper [J. Chen (Alibaba)]

[JVET-O0608](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7223) CE2-related: On latency reduction for chroma residual scaling [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, S. Ye, X. Wang (Kwai Inc.)]

[JVET-O0611](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7226) CE2-3 related: On CCLM restriction for 32x32 luma latency [K. Kawamura, K. Unno, S. Naito (KDDI)]

[JVET-O0708](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7328) Crosscheck of JVET-O0611 (CE2-3 related: On CCLM restriction for 32x32 luma latency) [K. Abe (Panasonic)]

[JVET-O0618](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7233) CE2-related: Removal of luma-chroma dependency in LMCS [Z. Deng, L. Zhang, H. Liu, K. Zhang, Y. Wang (Bytedance)]

[JVET-O0767](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7388) Crosscheck of JVET-O0618: CE2-related: Removal of luma-chroma dependency in LMCS [J. Zhao (LGE)]

[JVET-O0627](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7242) CE2-related: luma-chroma dependency reduction on chroma scaling [J. Chen, R.-L. Liao, Y. Ye, J. Luo (Alibaba)]

[JVET-O0691](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7311) Crosscheck of JVET-O0627 (CE2-related: luma-chroma dependency reduction on chroma scaling) [Y. Sun, S. Ye, F. Chen, L. Wang (Hikvision)]

[JVET-O0747](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7368) Crosscheck of JVET-O0627 (CE2-related: luma-chroma dependency reduction on chroma scaling) [T. Lu (Dolby)]

[JVET-O0668](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7285) Non-CE2: CCLM latency reduction for single tree [T. Biatek, G. Van Der Auwera, M. Karczewicz (Qualcomm)]

## CE3 related – Intra prediction and mode coding (96)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0084](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6688) 8-bit implementation and simplification of MIP [J. Pfaff, T. Hinz, P. Helle, P. Merkle, B. Stallenberger, M. Schäfer, H. Schwarz, D. Marpe, T. Wiegand (Fraunhofer HHI)]

[JVET-O0111](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6715) Non-CE3: Modified CABAC Coding for Intra Prediction Mode [B. Wang, A. M. Kotra, S. Esenlik, H. Gao, E. Alshina (Huawei)]

[JVET-O0139](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6743) CE3 Related: Low Memory and Computational Complexity Matrix Based Intra Prediction (MIP) [M. Salehifar, S. Kim (LGE)]

[JVET-O0721](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7341) Crosscheck of JVET-O0139 (CE3 Related: Low Memory and Computational Complexity Matrix Based Intra Prediction (MIP)) [T. Biatek (Qualcomm)]

[JVET-O0160](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6764) Non-CE3: Sample value clipping on MIP reduced prediction [Z. Zhang, K. Andersson, R. Sjöberg, J. Ström, P. Wennersten, R. Yu (Ericsson)]

[JVET-O0161](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6765) Non-CE3: Simplification on MIP boundary sample downsampling process [Z. Zhang, K. Andersson, R. Sjöberg, J. Ström, P. Wennersten, R. Yu (Ericsson)]

[JVET-O0168](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6772) Non-CE3: MIP low resolution clipping [T. Biatek, G. Van der Auwera, M. Karczewicz (Qualcomm)]

[JVET-O0882](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7504) Crosscheck of JVET-O0168 (Non-CE3: MIP low resolution clipping) [Z. Zhang (Ericsson)]

[JVET-O0169](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6773) CE3 Related: No MPM Derivation for Matrix Based Intra Prediction (MIP) [M. Salehifar, S. Kim (LGE)]

[JVET-O0722](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7342) Crosscheck of JVET-O0169, (CE3 Related: No MPM Derivation for Matrix Based Intra Prediction (MIP)) [T. Biatek (Qualcomm)]

[JVET-O0170](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6774) Non-CE3: Simplification on Matrix-based intra prediction [B. Wang, A. M. Kotra, S. Esenlik, H. Gao, E. Alshina (Huawei)]

[JVET-O0883](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7505) Crosscheck of JVET-O0170 (Non-CE3: Simplifications of Intra Mode Coding for Matrix-based Intra Prediction) [Z. Zhang (Ericsson)]

[JVET-O0171](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6775) Non-CE3: Simplifications for MIP [L. Zhao, X. Zhao, X. Li, S. Liu (Tencent)]

[JVET-O0850](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7471) Crosscheck of JVET-O0171 (Non-CE3: Simplifications for MIP) [Jie Yao (Fujitsu)]

[JVET-O0943](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7570) Crosscheck of JVET-O0171 (Non-CE3: Simplifications for MIP) [T.-C. Ma (Kwai Inc.)]

[JVET-O0187](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6791) Non-CE3: MIP simplifications [A. K. Ramasubramonian, G. Van der Auwera, T. Biatek, L. Pham van, M. Karczewicz (Qualcomm)]

[JVET-O0192](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6797) Non-CE3: MPM flag first signaling for intra luma mode coding [Jie Yao, Jianqing Zhu, Wenting Cai, Kimihiko Kazui (Fujitsu)]

[JVET-O0768](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7389) Crosscheck of JVET-O0192: Non-CE3: MPM flag first signaling for intra luma mode coding [L. Pham Van, G. Van der Auwera, M. Karczewicz (Qualcomm)]

[JVET-O0197](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6802) Non-CE3: cleanup for ISP flag and MRL index signalling [H. Liu, L. Zhang, K. Zhang, N. Zhang, J. Xu, Y. Wang (Bytedance)]

[JVET-O0851](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7472) Crosscheck of JVET-O0197 (Non-CE3: Cleanup for ISP flag and MRL index signalling) [Jie Yao (Fujitsu)]

[JVET-O0202](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6807) Non-CE3: MIP Modifications [Y.-U. Yoon, D.-H. Park, J.-G. Kim (KAU), J. Lee, J. Kang (ETRI)]

[JVET-O0203](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6808) CE3-related: Simplification of Matrix-Based Intra Prediction (MIP) [A. Filippov, V. Rufitskiy, B. Wang, S. Esenlik, E. Alshina (Huawei)]

[JVET-O0811](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7432) Crosscheck of JVET-O0203 (CE3-related: Simplification of Matrix-based Intra Prediction (MIP)) [[T.-H. Li](mailto:thli@fginnov.com), J.-Y. Deng (Foxconn)]

[JVET-O0224](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6829) CE3-related: Further simplifications of the unified MPM list for intra mode coding [G. Rath, F. Racapé, F. Urban (InterDigital)]

[JVET-O0229](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6834) CE3-related: Simplifications of PDPC for diagonal and adjacent diagonal directions [G. Rath, F. Racapé, F. Urban (InterDigital)]

[JVET-O0230](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6835) CE3-related: Interpolation filtering for intra prediction in non-diagonal directions [G. Rath, F. Racapé, F. Urban, F. Le Léannec (InterDigital)]

[JVET-O0255](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6860) Non-CE3: Fixed MPMs for MIP [K. Zhang, Z. Deng, L. Zhang, H. Liu, N. Zhang (Bytedance)]

[JVET-O0260](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6865) Non-CE3: Rounding offset removal for intra prediction [Z. Deng, H. Liu, L. Zhang, K. Zhang, Y. Wang (Bytedance)]

[JVET-O0862](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7483) Cross-check of JVET-O0260: "Non-CE3: Rounding offset removal for intra prediction" [F. Le Léannec (Interdigital)]

[JVET-O0261](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6866) Non-CE3: MIP with one-stage boundary sample downsampling [Z. Deng, H. Liu, K. Zhang, L. Zhang, Y. Wang (Bytedance)]

[JVET-O0826](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7447) Crosscheck of JVET-O0261 (Non-CE3: MIP with one-stage boundary sample downsampling) [H. Chen (Huawei)]

[JVET-O0275](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6880) CE3-related: Chroma DM derivation for IBC and PCM [M.-S. Chiang, O. Chubach, C.-W. Hsu, T.-D. Chuang, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0823](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7444) Crosscheck of JVET-O0275 (CE3-related: Chroma DM derivation for IBC and PCM) [S. Yoo, J. Lim (LGE)]

[JVET-O0276](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6881) CE3-related: Simplifications for MIP [Z.-Y. Lin, T.-D. Chuang, C.-Y. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0827](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7448) Crosscheck of JVET-O0276 (CE3-related: Simplifications for MIP) [H. Chen (Huawei)]

[JVET-O0277](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6882) CE3-related: Simplification and unification for intra reference sample filtering [C.-M. Tsai, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0825](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7446) Crosscheck of JVET-O0277 (CE3-related: Simplification and unification for intra reference sample filtering) [J. Shingala (Ittiam)]

[JVET-O0311](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6916) CE3-related: Intra sub-partition improvement for low decoding latency and hardware implementation efficiency [[H. Yang](mailto:Hua.Yang@InterDigital.com), [Y. He (InterDigital)](mailto:Yuwen.He@InterDigital.com)]

[JVET-O0312](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6917) Non-CE3: Simplifications of MIP [P.-H. Lin (Foxconn)]

[JVET-O0316](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6921) Non-CE3：Unification of ISP and Non-ISP [D. Jiang, J. Lin, F. Zeng, C. Fang (Dahua)]

[JVET-O0317](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6922) CE3-related: MPM List Modification for Zero Reference Line [D. Jiang, J. Lin, F. Zeng, C. Fang (Dahua)]

[JVET-O0318](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6923) Non-CE3: Intra Combined predict [D. Jiang, J. Lin, F. Zeng, C. Fang (Dahua)]

[JVET-O0320](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6925) Non-CE3: CCLM simplification [J.-Y. Huo, Y.-Z. Ma, X.-W. Li, F.-Z. Yang (Xidian Univ.), S. Wan (NPU), Y.-F. Yu, Y. Liu, M. Li (OPPO)]

[JVET-O0321](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6926) Non-CE3: Removal of MIP mapping table [J.-Y. Huo, Y.-Z. Ma, X.-W. Li, H.-X. Wang, F.-Z. Yang (Xidian Univ.), S. Wan (NPU), Y.-F. Yu, Y. Liu, M. Li (OPPO)]

[JVET-O0322](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6927) Non-CE3: Simplification of PDPC for chroma component [J.-Y. Huo (Xidian Univ.), S. Wan (NPU), Y.-Z. Ma (Xidian Univ.), J.-K. Guo (NPU), F.-Z. Yang (Xidian Univ.), Y.-F. Yu, Y. Liu, M. Li (OPPO)]

[JVET-O0323](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6928) Non-CE3: Unification of shifting for MIP mode [J.-Y. Huo (Xidian Univ.), S. Wan (NPU), Y.-Z. Ma (Xidian Univ.), J.-K. Guo (NPU), H.-X. Wang, F.-Z. Yang (Xidian Univ.), Y.-F. Yu, Y. Liu, M. Li (OPPO)]

[JVET-O0717](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7337) Crosscheck of JVET-O0323 (CE3-related: Unification of shifting for MIP mode) [L. Xu, F. Chen, L. Wang (Hikvision)]

[JVET-O0324](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6929) Non-CE3: MIP Simplification [J.-Y. Huo (Xidian Univ.), S. Wan (NPU), Y.-Z. Ma (Xidian Univ.), J.-K. Guo (NPU), F.-Z. Yang (Xidian Univ.), Y.-F. Yu, Y. Liu, M. Li (OPPO)]

[JVET-O0718](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7338) Crosscheck of JVET-O0324 (Non-CE3: MIP Simplification) [L. Xu, F. Chen, L. Wang (Hikvision)]

[JVET-O0341](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6946) Non-CE3: Unification of intra interpolation filter selection [J. Lee, J. Kang, H. Lee, S.-C. Lim, H. Y. Kim (ETRI)]

[JVET-O0765](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7386) Crosscheck of JVET-O0341: Non-CE3: Unification of intra interpolation filter selection [F. Racapé, G. Rath (Interdigital)]

[JVET-O0343](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6948) CE3-related: Simplification on CCLM process [D.-Y. Kim, J.-O. Lee (Chips&Media), S.-C. Lim, J. Lee, J. Kang (ETRI)]

[JVET-O0845](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7466) Crosscheck of JVET-O0343 (CE3-related: Simplification on CCLM process) [M. Ikeda (Sony)]

[JVET-O0345](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6950) Non-CE3: MIP simplification [C.-H. Yau, C.-C. Lin, C.-L. Lin (ITRI)]

[JVET-O0351](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6956) Non-CE3: Clean-up intra type signaling [J. Heo, J. Nam, J. Choi, J. Lim, S. Kim (LGE)]

[JVET-O0353](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6958) Non-CE3: Simplification on transform selection for multiple reference line (MRL) [J. Heo, M. Koo, J. Choi, J. Lim, S. Kim (LGE)]

[JVET-O0707](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7327) Crosscheck of JVET-O0353 (Non-CE3: Simplification on transform selection for multiple reference line (MRL)) [L. Zhao (Tencent)]

[JVET-O0355](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6960) Non-CE3: MRL restriction [J. Heo, J. Choi, H. Jang, J. Lim, S. Kim (LGE)]

[JVET-O0942](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7569) Crosscheck of JVET-O0355 (Non-CE3: MRL restriction) [T.-C. Ma (Kwai Inc.)]

[JVET-O0356](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6961) Non-CE3: Restriction on WAIP for MRL and ISP intra prediction [J. Heo, J. Choi, L. Li, J. Lim, S. Kim (LGE)]

[JVET-O0836](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7457) Crosscheck of JVET-O0356 (Non-CE3: Restriction on WAIP for MRL and ISP intra prediction) [A. Tamse (Samsung)]

[JVET-O0363](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6968) CE3-related: Context reduction for intra MPM related syntax element [L. Xu, X. Cao, F. Chen, L. Wang (Hikvision)]

[JVET-O0816](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7437) Crosscheck of JVET-O0363 (CE3-related: Context reduction for intra MPM related syntax element) [T. Ikai (Sharp)]

[JVET-O0364](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6969) On general intra sample prediction [F. Bossen (Sharp)]

[JVET-O0365](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6970) CE3-related: Simplification of Multi-reference line prediction scheme [L. Xu, X. Cao, F. Chen, L. Wang (Hikvision)]

[JVET-O0749](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7370) Crosscheck of JVET-O0365 (CE3-related: Simplification of Multi-reference line prediction scheme) [?? (??)]

[JVET-O0369](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6974) CE3-related: Context reduction for ISP related syntax element [L. Xu, X. Cao, F. Chen, L. Wang (Hikvision)]

[JVET-O0817](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7438) Crosscheck of JVET-O0369 (CE3-related: Context reduction for ISP related syntax element) [T. Ikai (Sharp)]

[JVET-O0371](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6976) CE3-related: On 1xN and 2xN sub-partition in ISP [Y. Sun, L. Xu, X. Cao (Hikvision)]

[JVET-O0771](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7392) Crosscheck of JVET-O0371 (CE3-related: On 1xN and 2xN sub-partition in ISP) [Y.-W. Chen (Kwai Inc.)]

[JVET-O0377](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6982) CE3-related: Context reduction for CCLM syntax element [L. Xu, X. Cao, F. Chen (Hikvision)]

[JVET-O0818](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7439) Crosscheck of JVET-O0377 (CE3-related: Context reduction for CCLM syntax element) [T. Ikai (Sharp)]

[JVET-O0396](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7001) Non-CE3: Simplified mode mapping in MIP [J. Choi, J. Heo, J. Lim, S. Kim (LGE)]

[JVET-O0750](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7371) Crosscheck of JVET-O0396 (Non-CE3: Simplified mode mapping in MIP) [?? (??)]

[JVET-O0397](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7002) Non-CE3: MIP mode reduction [J. Choi, J. Heo, J. Lim, S. Kim (LGE)]

[JVET-O0918](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7540) Crosscheck of JVET-O0397 (Non-CE3: MIP mode reduction) [K. Zhang (Bytedance)]

[JVET-O0399](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7004) Non-CE3: CCLM block size restriction [J. Choi, J. Heo, J. Lim, S. Kim (LGE)]

[JVET-O0401](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7006) Non-CE3: MIP mode simplifications [[C. Rosewarne](mailto:chris.rosewarne@canon.com.au), J. Gan (Canon)]

[JVET-O0404](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7009) Non-CE3: Linear Model-based Intra Prediction [R. Ghaznavi-Youvalari (Nokia)]

[JVET-O0407](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7012) Non-CE3: On adaptive rounding offset of MIP [K. Kondo, M. Ikeda, T. Suzuki (Sony)]

[JVET-O0408](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7013) Non-CE3: On rounding shift of MIP [K. Kondo, M. Ikeda, T. Suzuki (Sony)]

[JVET-O0412](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7017) CE3-related: Sampling point extension for CCLM [M. Ikeda, Y. Yagasaki, T. Suzuki (Sony)] [miss] [late]

[JVET-O0833](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7454) Crosscheck of JVET-O0412 (CE3-related: Sampling point extension for CCLM) [S.-C. Lim, J. Lee, H. Lee, J. Kang (ETRI)]

[JVET-O0416](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7021) Non-CE3: Removal of CCLM context derivation dependency [J. Q. Zhang (PKU), X. Zheng (DJI), S. S. Wang, S. W. Ma (PKU)]

[JVET-O0841](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7462) Crosscheck of JVET-O0416: Non-CE3: Removal of CCLM context derivation dependency [J. Chen (Alibaba)]

[JVET-O0426](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7031) Non-CE3: DC intra prediction mode alignment [A. K. Ramasubramonian, G. Van der Auwera, L. Pham Van, M. Karczewicz (Qualcomm)]

[JVET-O0762](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7383) Crosscheck of JVET-O0426 (Non-CE3: DC intra prediction mode alignment) [H.-J. Jhu (Kwai Inc.)]

[JVET-O0449](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7055) Non-CE3: Decoder-side Intra Mode Derivation (DIMD) with prediction fusion using Planar [M. Abdoli, T. Guionnet, E. Mora, M. Raulet (ATEME), S. Blasi, A. Seixas Dias, G. Kulupana (BBC)]

[JVET-O0720](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7340) Crosscheck of JVET-O0449 Decoder-side Intra Mode Derivation with Prediction Fusion Using Planar [V. Drugeon (Panasonic)]

[JVET-O0464](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7071) Non-CE3: Unification of Neighbouring Location for MPM List Construction [D. Kim, J. Jung, G. Ko, J. Son, J. Kwak (WILUS)]

[JVET-O0467](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7074) Non-CE3: simplified MRL and ISP mode coding [F. Le Léannec, Y. Chen, T. Poirier (InterDigital)]

[JVET-O0856](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7477) Crosscheck of JVET-O0467 (Non-CE3: simplified MRL and ISP mode coding) [H. Liu (Bytedance)]

[JVET-O0469](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7076) Non-CE3: Simplification of MIP MPM Derivation [D. Kim, J. Jung, G. Ko, J. Son, J. Kwak (WILUS)]

[JVET-O0477](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7084) Non-CE3: Simplified entropy coding for intra prediction mode [D. Park, Y.-U. Yoon, J. Do, J.-G. Kim (KAU), J. Lee, J. Kang (ETRI)]

[JVET-O0481](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7088) Variations of the 8-bit implementation of MIP [P. Helle, J. Pfaff, T. Hinz, P. Merkle, B. Stallenberger, M. Schäfer, H. Schwarz, D. Marpe, T. Wiegand (HHI)]

[JVET-O0484](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7091) CE3-related: Improved intra mode coding based on unified MPM list generation [J. Pfaff, T. Hinz, P. Merkle, P. Helle, B. Stallenberger, M. Schäfer, H. Schwarz, D. Marpe, T. Wiegand (HHI), B. Wang, S. Esenlik, A. M. Kotra, H. Gao, E. Alshina (Huawei)]

[JVET-O0938](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7563) Crosscheck of JVET-O0484 (CE3-related: Improved intra mode coding based on unified MPM list generation) [M. Salehifar (LGE)]

[JVET-O0485](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7092) Non-CE3: Harmonization of 8-Bit MIP with Unified-MPM and LFNST [J. Pfaff, T. Hinz, P. Helle, P. Merkle, B. Stallenberger, M. Schäfer, H. Schwarz, D. Marpe, T. Wiegand (HHI)]

[JVET-O0885](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7507) Crosscheck of JVET-O0485 (Non-CE3: Harmonization of 8-Bit MIP with Unified-MPM and LFNST) [Z. Zhang (Ericsson)]

[JVET-O0486](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7093) Non-CE3: Harmonization of 8-Bit MIP with Unified-MPM and LFNST using a single type of MIP prediction modes [J. Pfaff, T. Hinz, P. Helle, P. Merkle, B. Stallenberger, M. Schäfer, H. Schwarz, D. Marpe, T. Wiegand (HHI)]

[JVET-O0886](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7508) Crosscheck of JVET-O0486 (Non-CE3: Harmonization of 8-Bit MIP with Unified-MPM and LFNST using a single type of MIP prediction modes) [Z. Zhang (Ericsson)]

[JVET-O0502](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7109) Non-CE3: Proposed ISP cleanup [S. De-Luxán-Hernández, V. George, G. Venugopal, J. Brandenburg, B. Bross, T. Nguyen, H. Schwarz, D. Marpe, T. Wiegand (HHI)]

[JVET-O0944](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7571) Crosscheck of JVET-O0502 (Non-CE3: Proposed ISP cleanup) [T.-C. Ma (Kwai Inc.)]

[JVET-O0507](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7114) Non-CE3 : Intra-sub-partition coding mode only performing on non-square blocks [W. Gwun, W. Lee, D. Y. Lee, T. H. Kim, G. H. Park (KHU), G. Bang, W. Lim, H.Y. Kim (ETRI)]

[JVET-O0509](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7116) Non-CE3: MPM flag signaling first [D. Y. Lee, W. Gwun, W. Lee, T. H. Kim, G. H. Park (KHU), J. Lee, J. Kang (ETRI)]

[JVET-O0510](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7117) CE3-related: Extensions to CCLM reference sample filtering in CE3-3.2.1 [S. Keating, K. Sharman (Sony)]

[JVET-O0512](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7119) Non-CE3: CCLM Coding bug fix [S. Keating, K. Sharman (Sony)]

[JVET-O0716](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7336) Crosscheck of JVET-O0512 (Non-CE3: CCLM Coding bug fix) [L. Xu, F. Chen, L. Wang (Hikvision)]

[JVET-O0514](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7121) Non-CE3: Analysis of Matrix-based Intra Prediction (Informative) [S. Keating, K. Sharman (Sony)]

[JVET-O0515](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7122) Non CE3: Intra mode coding of non-MPM modes [S. Blasi, G. Kulupana, A. Seixas Dias (BBC)]

[JVET-O0523](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7130) CE3-related: simplification of Matrix Intra Prediction [T. Dumas, F. Galpin, G. Rath, F. Racapé (InterDigital)]

[JVET-O0533](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7140) Crosscheck of JVET-O0229 (CE3-related: Simplifications of PDPC for diagonal and adjacent diagonal directions)) [L. Zhao (Tencent)] [miss] [late]

[JVET-O0535](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7142) Non-CE3: Unification of PDPC for diagonal intra prediction modes [L. Zhao, X. Zhao, X. Li, S. Liu (Tencent)]

[JVET-O0766](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7387) Crosscheck of JVET-O0535: Non-CE3: Unification of PDPC for diagonal intra prediction modes [F. Racapé, G. Rath (Interdigital)]

[JVET-O0536](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7143) CE3-related: Redundant bit saving for CE3-2.2 [L. Zhao, X. Zhao, X. Li, S. Liu (Tencent)]

[JVET-O0779](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7400) Crosscheck of JVET-O0536: CE3-related: Redundant bit saving for CE3-2.2 [L. Pham Van, G. Van der Auwera, M. Karczewicz (Qualcomm)]

[JVET-O0547](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7159) Non-CE: Modified MIP Matrices for Fast Implementation [K. Naser, L. Kerofsky, F. Le Léannec, T. Poirier (InterDigital)]

[JVET-O0571](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7186) Non-CE3: Up-sampling with a fixed order in MIP [H. Liu, L. Zhang, K. Zhang, Z. Deng, J. Xu (Bytedance)]

[JVET-O0843](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7464) Crosscheck of JVET-O0571: Non-CE3: Up-sampling with a fixed order in MIP [J. Chen (Alibaba)]

[JVET-O0598](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7213) CE3-related: Simplification on MIP Look-up Table [T.-C. Ma, X. Xiu, Y.-W. Chen, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0693](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7313) Crosscheck of JVET-O0598 (CE3-related: Simplification on MIP Look-up Table) [L. Zhao (Tencent)]

[JVET-O0599](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7214) CE3-related: Simplification on MIP flag context model [T.-C. Ma, X. Xiu, Y.-W. Chen, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0815](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7436) Crosscheck of JVET-O0599 (CE3-related: Simplification on MIP flag context model) [J. Heo, L. Li (LGE)]

[JVET-O0606](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7221) Non-CE3: Context Reduction for MPM Signaling [H.-J. Jhu, X. Xiu, Y.-W. Chen, T.-C. Ma, X. Wang (Kwai Inc.)]

[JVET-O0621](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7236) Non-CE3: MIP simplification [Y. Yasugi, T. Ikai (Sharp)] [miss] [late]

[JVET-O0855](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7476) Crosscheck of JVET-O0621 (Non-CE3: MIP simplification) [Z. Deng (Bytedance)]

[JVET-O0632](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7247) Non-CE3: WAIP restriction for square sub-CU blocks partitioned by ISP [A. Kumar, S. Shrestha, B. Lee (Chosun Univ), Y. Lee, J. Park (Humax)]

[JVET-O0640](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7256) CE3-related: On 2x2/2x4/4x2 chroma blocks at the corner of pictures [L. Pham Van, G. Van der Auwera, A. K. Ramasubramonian, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0647](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7263) Non-CE3: On signalling of intra coded blocks [L. Pham Van, G. Van der Auwera, A. K. Ramasubramonian, V. Seregin, H. Huang, M. Karczewicz (Qualcomm)]

[JVET-O0649](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7265) Non-CE3: Simplification of reference sample smoothing in intra prediction [A. K. Ramasubramonian, G. Van der Auwera, M. Karczewicz (Qualcomm)]

[JVET-O0655](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7271) Non-CE3: On chroma DM derivation table for 4:2:2 chroma format [S. Iwamura, S. Nemoto, A. Ichigaya (NHK)]

[JVET-O0687](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7306) Crosscheck of JVET-O0655 (Non-CE3: On chroma DM derivation table for 4:2:2 chroma format) [T. Chujoh (Sharp)]

[JVET-O0671](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7288) Non-CE3: Intra chroma mode coding simplification [B. Ray, G. Van der Auwera, M. Karczewicz (Qualcomm)]

[JVET-O0672](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7290) Non-CE3: MRL intra prediction for chroma DM [C.-C. Kuo, C.-C. Lin, C.-L. Lin (ITRI)]

[JVET-O0673](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7291) Harmonized MIP/CCLM boundary downsampling [T. Biatek, G. Van Der Auwera, M. Karczewicz (Qualcomm)]

[JVET-O0674](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7292) Non-CE3: Multiple reference set for CCLM [P.-H. Lin, J.-Y. Deng (Foxconn)] [late]

[JVET-O0760](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7381) Crosscheck of JVET-O0674 (Non-CE3: Multiple reference set for CCLM) [H.-J. Jhu (Kwai Inc.)]

[JVET-O0748](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7369) Non-CE3: On signalling for intra coding mode in the MPM list [L. Pham Van, G. Van der Auwera, A. K. Ramasubramonian, M. Karczewicz (Qualcomm)] [late]

[JVET-O0755](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7376) Non-CE3: On signalling of MIP parameters [A. K. Ramasubramonian, G. Van der Auwera, T. Hsieh, T. Biatek, L. Pham Van, M. Karczewicz (Qualcomm)] [late]

[JVET-O0793](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7414) CE3-related: MIP with simplified mode index coding [Z.-Y. Lin, T.-D. Chuang, C.-Y. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)] [late]

[JVET-O0848](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7469) Non CE3: Modification of MIP interpolation [B.-J. Fuh, C.-H. Yau, C.-C. Lin, C.-C. Kuo, C.-L. Lin (ITRI)] [late]

[JVET-O0925](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7550) Non-CE3: Simplifications of MIP [J. Pfaff, P. Merkle, P. Helle, H. Schwarz, D. Marpe, T. Wiegand (HHI), A. K. Ramasubramonian, T. Biatek, G. Van der Auwera, L. Pham Van, M. Karczewicz (Qualcomm), J. Choi, J. Heo, J. Lim, M. Salehifar, S. Kim (LGE), K. Kondo, M. Ikeda, T. Suzuki (Sony), Z. Zhang, K. Andersson, R. Sjöberg, J. Ström, P. Wennersten (Ericsson)] [late]

## CE4 related – Inter prediction (104)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0123](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6727) Non-CE4: Alignment of BDOF refinement process with PROF [J. Li, C. Lim (Panasonic)]

[JVET-O0733](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7354) Crosscheck of JVET-O0123: Non-CE9: Alignment of BDOF refinement process with PROF [[W. Chen (InterDigital)](mailto:Wei.Chen@InterDigital.com)]

[JVET-O0125](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6729) Non-CE4: Alignment of DMVR padding process with specification [J. Li, C.S. Lim (Panasonic)]

[JVET-O0714](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7334) Crosscheck of JVET-O0125: Non-CE9: Alignment of DMVR padding process with specification [X. Xiu (Kwai Inc.)]

[JVET-O0126](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6730) Non-CE4: Align coding of BPWA index with normal truncated rice binarization process [J. Li, C.S. Lim (Panasonic)]

[JVET-O0794](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7415) Crosscheck of JVET-O0126 (Non-CE4: Align coding of BPWA index with normal truncated Rice binarization process) [S.-T. Hsiang (MediaTek)]

[JVET-O0128](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6732) Non-CE4: simplification of triangle mode MV context storage [J. Li, C.S. Lim (Panasonic)]

[JVET-O0724](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7344) Crosscheck of JVET-O0128 (Non-CE4: Simplification of triangle mode MV context storage) [R.-L. Liao (Alibaba)]

[JVET-O0163](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6767) Non-CE4: Simplification on SbTMVP [H. Chen, H. Yang (Huawei)]

[JVET-O0926](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7551) Crosscheck of JVET-O0163 (Non-CE4: Simplification on SbTMVP) [Y.-C. Lin (MediaTek)]

[JVET-O0164](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6768) Non-CE4: Simplification on AMVP list construction [H. Chen, H. Yang (Huawei)]

[JVET-O0742](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7363) Crosscheck of JVET-O0164 (Non-CE4: Simplification on AMVP list construction) [J. Li (Bytedance)]

[JVET-O0801](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7422) Crosscheck of JVET-O0164 (Non-CE4: Simplification on AMVP list construction) [J. Li (Bytedance)]

[JVET-O0167](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6771) Non-CE4: On SbTMVP motion data derivation [J. Zhao, S. Kim (LGE)]

[JVET-O0867](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7488) Crosscheck of JVET-O0167 (Non-CE4: On SbTMVP base motion data derivation) [N. Zhang (Bytedance)]

[JVET-O0190](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6794) Non-CE4: LIC model parameters using forward mapped luma samples in LMCS [J. Shingala, A. Natesan (Ittiam)]

[JVET-O0804](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7425) Crosscheck of JVET-O0190 (Non-CE4: LIC model parameters using forward mapped luma samples in LMCS) [C.-M. Tsai (MediaTek)]

[JVET-O0217](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6822) Non-CE4: Simplification of motion vector storing process for triangle merge mode [Y. Ahn, D. Sim (Digital Insights)]

[JVET-O0220](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6825) Non-CE4: On maximum number of subblock merge candidates [R. Yu, D. Liu (Ericsson)]

[JVET-O0221](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6826) Non-CE4: On merge data syntax [[R. Yu](mailto:ruoyang.yu@ericsson.com), D. Liu (Ericsson)]

[JVET-O0222](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6827) Non-CE4: Modification of triangle merge mode enabling criterion [D. Liu, R. Yu (Ericsson)]

[JVET-O0249](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6854) Non-CE4: Merge Modes Signaling [H. Huang, W.-J. Chien, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0728](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7348) Crosscheck of JVET-O0249 (Non-CE4: Merge Modes Signaling) [R.-L. Liao (Alibaba)]

[JVET-O0252](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6857) CE4-related: Alignment and simplification of PROF and BDOF [H. Huang, W.-J. Chien, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0688](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7307) Crosscheck of JVET-O0252: CE4-related: Alignment and simplification of PROF and BDOF [X. Xiu (Kwai Inc.)]

[JVET-O0253](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6858) Non-CE4: Simplification of HMVP in AMVP mode [H. Huang, W.-J. Chien, M. Karczewicz (Qualcomm)]

[JVET-O0949](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7577) Cross-check of JVET-O253 (Non-CE4: Simplification of HMVP in AMVP mode) [F. Galpin (InterDigital)]

[JVET-O0254](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6859) Non-CE4: Size constrain for inherited affine motion prediction [H. Huang, W.-J. Chien, M. Karczewicz (Qualcomm)]

[JVET-O0257](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6862) CE4-related: Simplified local illumination compensation [N. Zhang, H. Liu, L. Zhang, K. Zhang, Y. Wang (Bytedance)]

[JVET-O0712](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7332) Crosscheck of JVET-O0257 (CE4-related: Simplified local illumination compensation) [N. Park (LGE)]

[JVET-O0263](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6868) Non-CE4: Cleanups on syntax design for sub-block coding tools [K. Zhang, L. Zhang, H. Liu, N. Zhang, Z. Deng, Y. Wang (Bytedance)]

[JVET-O0265](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6870) Non-CE4: Simplified motion field storage for TPM [N. Zhang, H. Liu, L. Zhang, K. Zhang, Y. Wang (Bytedance)]

[JVET-O0676](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7294) Crosscheck of JVET-O0265: Non-CE4: Simplified motion field storage for TPM [J. Zhao (LGE)]

[JVET-O0278](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6883) CE4-related: On signalling of merge flags [Y.-L. Hsiao, C.-C. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0828](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7449) Crosscheck of JVET-O0278 (CE4-related: On signalling of merge flags) [H. Chen (Huawei)]

[JVET-O0279](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6884) CE4-related: Simplification of motion vector storing process for triangle prediction mode [Y.-L. Hsiao, C.-C. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0835](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7456) Crosscheck of JVET-O0279 (CE4-related: Simplification of motion vector storing process for triangle prediction mode) [H. Lee, S.-C. Lim, J. Lee, J. Kang (ETRI)]

[JVET-O0280](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6885) CE4-related: SIMD implementation for weighted sample prediction process of triangle prediction mode [Y.-L. Hsiao, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0859](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7480) Crosscheck of JVET-O0280 (CE4-related: SIMD implementation for weighted sample prediction process of triangle prediction mode) [T. Hashimoto (Sharp)]

[JVET-O0281](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6886) CE4-related: Alignment of displacement bitdepths for BDOF and PROF [C.-Y. Lai, T.-D. Chuang, C.-Y. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0900](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7522) Crosscheck of JVET-O0281 (CE4-related: Alignment of displacement bitdepths for BDOF and PROF) [H. Huang (Qualcomm)]

[JVET-O0283](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6888) CE4-related: Simplification for SbTMVP [Y.-C. Lin, C.-C. Chen, C.-W. Hsu, T.-D. Chuang, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0284](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6889) CE4-related: Interaction between symmetric MVD and mvd\_l1\_zero\_flag [M.-S. Chiang, C.-W. Hsu, T.-D. Chuang, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0285](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6890) CE4-related: On merging candidate list construction [Y.-C. Lin, C.-C. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0879](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7501) Crosscheck of JVET-O0285 (CE4-related: On merging candidate list construction) [H. Gao (Huawei)]

[JVET-O0286](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6891) CE4-related: Extension of phase-variant affine subblock motion compensation in CE4-2.5 [T.-D. Chuang, C.-Y. Lai, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0820](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7441) Crosscheck of JVET-O0286 (CE4-related: Extension of phase-variant affine subblock motion compensation in CE4-2.5) [T.-S Chang (Alibaba)]

[JVET-O0305](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6910) Non-CE4: Undefined condition of merge mode syntax [K. Abe, T. Toma, J. Li, V. Drugeon (Panasonic)]

[JVET-O0309](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6914) Non-CE4: Merge mode signalling overhead reduction [G. Ko, D. Kim, J. Jung, J. Son, J. Kwak (WILUS)]

[JVET-O0829](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7450) Crosscheck of JVET-O0309 (Non-CE4: Merge mode signalling overhead reduction) [H. Chen (Huawei)]

[JVET-O0313](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6918) CE4-related: On Conditions for enabling PROF [Y.-C. Yang, P.-H. Lin (Foxconn)]

[JVET-O0734](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7355) Crosscheck of JVET-O0313: CE4-related: On Conditions for enabling PROF [[W. Chen (InterDigital)](mailto:Wei.Chen@InterDigital.com)]

[JVET-O0314](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6919) Non-CE4: Modification of MV storage in triangle merge mode [P.-H. Lin (Foxconn)]

[JVET-O0759](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7380) Crosscheck of JVET-O0314 (Non-CE4: Modification of MV storage in triangle merge mode) [H.-J. Jhu (Kwai Inc.)]

[JVET-O0328](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6933) Non-CE4: On Sub-block Merge Candidates List [H. Lee, S.-C. Lim, J. Lee, J. Kang, H. Y. Kim (ETRI)]

[JVET-O0329](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6934) Non-CE4: Simplification of motion vector storage process for triangle merge mode [H. Lee, S.-C. Lim, J. Lee, J. Kang, H. Y. Kim (ETRI)]

[JVET-O0881](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7503) Crosscheck of JVET-O0329 (Non-CE4: Simplification of motion vector storage process for triangle merge mode) [[R. Yu](mailto:ruoyang.yu@ericsson.com), D. Liu (Ericsson)]

[JVET-O0790](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7411) Crosscheck of JVET-O0328 (Non-CE4: On sub-block merge candidates list) [Y.-L. Hsiao (MediaTek)]

[JVET-O0330](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6935) Non-CE4: Syntax element ‘regular\_merge\_flag’ signalling [H. Lee, S.-C. Lim, J. Lee, J. Kang, H. Y. Kim (ETRI)]

[JVET-O0770](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7391) Crosscheck of JVET-O0330 (Non-CE4: Syntax element ‘regular\_merge\_flag’ signaling) [Y.-W. Chen (Kwai Inc.)]

[JVET-O0361](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6966) Non-CE4: Remove the redundancy among the flag coding for merge modes [F. Chen, L. Wang (Hikvision)]

[JVET-O0362](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6967) Non-CE4: Modified enabling condition for triangle mode [F. Chen, L. Wang (Hikvision)]

[JVET-O0725](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7345) Crosscheck of JVET-O0362 (Non-CE4: Modified Enabling Condition for Triangle Mode) [R.-L. Liao (Alibaba)]

[JVET-O0366](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6971) Non-CE4: Simplifications on BCW index derivation process [N. Park, J. Nam, H. Jang, J. Lim, S. Kim (LGE)]

[JVET-O0685](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7304) Crosscheck of JVET-O0366 (Non-CE4: Simplifications on BCW index derivation process) [T. Chujoh (Sharp)]

[JVET-O0367](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6972) Non-CE4: The proposed BCW index derivation for pairwise candidate [N. Park, J. Nam, H. Jang, J. Lim, S. Kim (LGE)]

[JVET-O0378](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6983) Non-CE4: Simplification of MV context storage for triangle mode [F. Chen, L. Wang (Hikvision)]

[JVET-O0880](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7502) Crosscheck of JVET-O0378 (Non-CE4: Simplification of MV context storage for triangle mode) [[D. Liu](mailto:du.liu@ericsson.com), R. Yu (Ericsson)]

[JVET-O0379](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6984) Non-CE4: Modified mode decision process for triangle mode [F. Chen, L. Wang (Hikvision)]

[JVET-O0726](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7346) Crosscheck of JVET-O0379 (Non-CE4: Modified Mode Decision Process for Triangle Mode) [R.-L. Liao (Alibaba)]

[JVET-O0384](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6989) CE4-related: CIIP Simplification [J. Y. Lee (Sejong Univ.), W. Lim, G. Bang, H. Y. Kim (ETRI)] [miss] [late]

[JVET-O0388](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6993) Non-CE4: On the context of triangle merge index [F. Chen, L. Wang (Hikvision)]

[JVET-O0727](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7347) Crosscheck of JVET-O0388 (Non-CE4: On the Context of Triangle Merge Index) [R.-L. Liao (Alibaba)]

[JVET-O0411](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7016) Non-CE4: Simplification of bi-prediction MV generation for triangle partition mode storage [K. Reuzé, C.-C. Chen, W.-J. Chien, H. Huang, M. Karczewicz (Qualcomm)]

[JVET-O0414](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7019) Non-CE4: Symmetric-MVD control considering the reference picture type [N. Park, J. Nam, H. Jang, J. Lim, S. Kim (LGE)]

[JVET-O0730](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7351) Crosscheck of JVET-O0414: Non-CE4 : Symmetric-MVD control considering the reference picture type [[Y. He (InterDigital)](mailto:yuwen.he@interdigital.com)]

[JVET-O0415](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7020) Non-CE4: Fix of POC distance condition for MMVD [N. Park, J. Nam, H. Jang, J. Lim, S. Kim (LGE)]

[JVET-O0868](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7489) Crosscheck of JVET-O0415 (Non-CE4: Fix of POC distance condition for MMVD) [N. Zhang (Bytedance)]

[JVET-O0418](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7023) CE4-related: Simplification of motion vector storage operation for triangle merge mode [X. W. Meng (PKU), X. Zheng (DJI), S. S. Wang, S. W. Ma (PKU)]

[JVET-O0420](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7025) CE4-related: Reduce scaling operation number for MMVD [S. H. Wang (PKU), X. Zheng (DJI), S. S. Wang, S. W. Ma (PKU)]

[JVET-O0761](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7382) Crosscheck of JVET-O0420 (CE4-related: Reduce scaling operation number for MMVD) [H.-J. Jhu (Kwai Inc.)]

[JVET-O0421](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7026) Non-CE4: On CIIP Merge mode with no residual [G. Laroche, C. Gisquet, J. Taquet, P. Onno (Canon)]

[JVET-O0422](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7027) Non-CE4: On mode flags signalling for Merge [G. Laroche, C. Gisquet, J. Taquet, P. Onno (Canon)]

[JVET-O0423](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7028) Non-CE4: On SbTMVP derivation using HMVP [C. Gisquet, G. Laroche, P. Onno, J. Taquet (Canon)]

[JVET-O0887](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7509) Crosscheck of JVET-O0423 (Non-CE4: On SbTMVP derivation using HMVP) [F. Henry (Orange), G. Clare (bcom)]

[JVET-O0424](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7029) Non-CE4: On TMVP reference frame selection using HMVP [C. Gisquet, G. Laroche, P. Onno, J. Taquet (Canon)]

[JVET-O0869](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7490) Crosscheck of JVET-O441

0424 (Non-CE4: On TMVP reference frame selection using HMVP) [N. Zhang (Bytedance)]

[JVET-O0431](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7036) Non-CE4: On Merge data signaling [H. Chen, X. Chen, H. Yang (Huawei)]

[JVET-O0434](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7039) Non-CE4: Simplification of TMVP [H. Chen, H. Yang (Huawei)]

[JVET-O0947](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7574) Crosscheck of JVET-O0434 (Non-CE4: Simplification of TMVP) [G. Ko, D. Kim, J. Jung, J. Son, J. Kwak (WILUS)]

[JVET-O0438](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7043) Non-CE4: Cleanup for affine AMVR enabling flag in SPS level [G. Ko, D. Kim, J. Jung, J. Son, J. Kwak (WILUS)]

[JVET-O0439](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7044) Non-CE4: Modification of the temporal merging candidate for the regular merge mode [G. Lee, G. Kim, S. Cha, D. Nam, J. Han (Sejong University)]

[JVET-O0834](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7455) Crosscheck of JVET-O0439 (Non-CE4: Modification of the temporal merging candidate for the regular merge mode) [S.-C. Lim, H. Lee, J. Kang, J. Lee (ETRI)]

[JVET-O0440](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7045) Non-CE4: A modified method to derive the temporal merge candidate for long-term reference picture [G. Lee, G. Kim, S. Cha, D. Nam, J. Han (Sejong University)]

[JVET-O0441](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7046) Non-CE4: Short-tap interpolation for bi-prediction [[X. W. Meng (PKU)](mailto:xwmeng@pku.edu.cn), [X. Zheng (DHI)](mailto:xiaozhen.zheng), S. S. Wang, S. W. Ma (PKU)]

[JVET-O0870](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7491) Crosscheck of JVET-O0441 (Non-CE4: Short-tap interpolation filter for bi-prediction) [N. Zhang (Bytedance)]

[JVET-O0456](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7062) Non-CE4: Grouping the syntax elements of motion vector differences [Y.-J. Chang, Y. Zhang, C.-C. Chen, W.-J. Chien, M. Karczewicz (Qualcomm)]

[JVET-O0802](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7423) Crosscheck of JVET-O0456: Non-CE4: Grouping the syntax elements of motion vector differences [Y. He (InterDigital)]

[JVET-O0457](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7064) CE4-related: LIC flag inherited per component [P. Bordes, F. Urban, F. Le Léannec, F. Galpin, T. Poirier (InterDigital)]

[JVET-O0686](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7305) Crosscheck of JVET-O0457 (CE4-related: LIC flag inherited per component) [T. Chujoh (Sharp)]

[JVET-O0468](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7075) Non-CE4: simplified SMVD mode coding [F. Le Léannec, Y. Chen, T. Poirier (InterDigital)]

[JVET-O0470](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7077) CE4-related: encoder simplification of local illumination compensation [F. Urban, P. Bordes, F. Le Léannec, F. Galpin, T. Poirier (InterDigital)]

[JVET-O0489](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7096) Non-CE4: Geometrical partitioning for inter blocks [S. Esenlik, H. Gao, A. Filippov, V. Rufitskiy, A. M. Kotra, B. Wang, E. Alshina (Huawei), M. Bläser, J. Sauer (RWTH Aachen)]

[JVET-O0500](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7107) Non-CE4: Affine and sub-block modes coding clean-up [F. Galpin, F. Le Léannec, A. Robert, T. Poirier (InterDigital)]

[JVET-O0903](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7525) Crosscheck of JVET-O0500 (non-CE4: affine and sub-block modes coding clean-up) [H. Huang (Qualcomm)]

[JVET-O0501](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7108) Non-CE4: Temporal merge modes simplification [F. Galpin, F. Le Léannec, A. Robert, T. Poirier (InterDigital)]

[JVET-O0904](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7526) Crosscheck of JVET-O0501 (non-CE4: temporal merge modes simplification) [H. Huang (Qualcomm)]

[JVET-O0513](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7120) Non-CE4: Adaptive blending filtering for TPM [S. Esenlik, H. Gao, B. Wang, A. Kotra, Z. Zhao, E. Alshina (Huawei), M. Bläser, J. Sauer (RWTH Aachen)]

[JVET-O0921](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7545) Crosscheck of JVET-O0513: Non-CE4: Adaptive blending filtering for TPM [X. Xiu (Kwai Inc.)]

[JVET-O0516](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7123) Non-CE4: BCW for pairwise candidate [A. Robert, F. Le Léannec, T. Poirier, F. Galpin (InterDigital)]

[JVET-O0522](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7129) Non-CE4: CIIP using triangular partitions [S. Blasi, G. Kulupana, A. Seixas Dias (BBC)]

[JVET-O0530](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7137) Non-CE4: Adaptive subblock size for affine motion compensation [G. Li, X. Li, X. Xu, S. Liu (Tencent)]

[JVET-O0531](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7138) Non-CE4: On LIC for Affine [G. Li, X. Li, X. Xu, S. Liu (Tencent)]

[JVET-O0713](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7333) Crosscheck of JVET-O0531: Non-CE4: On LIC for Affine [X. Xiu (Kwai Inc.)]

[JVET-O0534](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7141) Non-CE4: Redundancy removal for merge mode signaling [J. Ye, X. Li, S. Liu (Tencent)]

[JVET-O0552](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7165) Non-CE4: Extension of triangle partition mode to P-slices [K. Reuzé, C.-C Chen, W.-J. Chien, H. Huang, M. Karczewicz (Qualcomm)]

[JVET-O0553](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7167) Non-CE4: On prediction refinement with optical flow [X. Li, G. Li, X. Xu, S. Liu (Tencent)]

[JVET-O0736](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7357) Crosscheck of JVET-O0553: Non-CE4: On prediction refinement with optical flow [W. Chen (InterDigital)]

[JVET-O0560](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7174) Merge Mode Syntax [L. Wang, K. Panusopone (Nokia)]

[JVET-O0567](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7182) Non-CE4: Cleanups on MVD ranges [Z. Deng, H. Liu, L. Zhang, K. Zhang, Y. Wang (Bytedance)]

[JVET-O0572](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7187) Non-CE4: On availability of default reference pictures used in SMVD [H. Liu, L. Zhang, K. Zhang, Z. Deng, N. Zhang, Y. Wang (Bytedance)]

[JVET-O0579](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7194) Non-CE4: SMVD processing when mvd\_l1\_zero\_flag is set equal to 1 [H. Jang, N. Park, J. Nam, S. Kim, J. Lim (LGE)]

[JVET-O0864](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7485) Crosscheck of JVET-O0579: "Non-CE4: SMVD processing when mvd\_l1\_zero\_flag is set equal to 1" [F. Le Léannec (Interdigital)]

[JVET-O0580](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7195) CE4-related: Additional tests for prediction refinement with optical flow for affine mode (CE4-2.1) with 4x8 sub-block size [Y. He, W. Chen (InterDigital), W. Zeng, E. Cai (Ubilinx)]

[JVET-O0930](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7555) Crosscheck of JVET-O0580 (CE4-related: Additional tests for prediction refinement with optical flow for affine mode (CE4-2.1) with 4x8 sub-block size) [G. Li (Tencent)]

[JVET-O0583](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7198) CE4-related: Prediction refinement with optical flow for chroma components for affine mode [Y. He, W. Chen (InterDigital)]

[JVET-O0812](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7433) Crosscheck of JVET-O0583 (CE4-related: Prediction refinement with optical flow for chroma components for affine mode) [[T.-H. Li](mailto:thli@fginnov.com), J.-Y. Deng (Foxconn)]

[JVET-O0585](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7200) Non-CE4: Merge syntax for removing redundant syntax signaling or fall back merge mode [H. Jang, N. Park, J. Nam, S. Kim, J. Lim (LGE)]

[JVET-O0586](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7201) Non-CE4: Redundancy removal in merge modes signaling [Y.-W. Chen, X. Xiu, T.-C. Ma, H.-J. Jhu, S. Ye, X. Wang (Kwai Inc.)]

[JVET-O0697](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7317) Crosscheck of JVET-O0586 (Non-CE4: Redundancy removal in merge modes signaling) [Y.-H. Chao (Qualcomm)]

[JVET-O0587](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7202) Non-CE4: Simplification of MVP list construction [Y.-W. Chen, X. Xiu, T.-C. Ma, X. Wang (Kwai Inc.)]

[JVET-O0931](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7556) Crosscheck of JVET-O0587 (Non-CE4: Simplification of MVP list construction) [G. Li (Tencent)]

[JVET-O0588](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7203) CE4-related: On SbTMVP motion shift derivation [Y.-W. Chen, X. Xiu, T.-C. Ma, X. Wang (Kwai Inc.)]

[JVET-O0780](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7401) Crosscheck of JVET-O0588: CE4-related: On SbTMVP motion shift derivation [Y.-C. Sun (Alibaba)]

[JVET-O0589](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7204) Non-CE4: Simplification of HMVP in merge list construction [Y.-W. Chen, X. Xiu, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0698](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7318) Crosscheck of JVET-O0589 (Non-CE4: Simplification of HMVP in merge list construction) [Y.-H. Chao (Qualcomm)]

[JVET-O0592](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7207) CE4-related: Motion estimation improvements [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0932](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7557) Crosscheck of JVET-O0592 (CE4-related: Motion estimation improvements) [G. Li (Tencent)]

[JVET-O0593](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7208) CE4-related: Harmonization of BDOF and PROF [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0735](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7356) Crosscheck of JVET-O0593: CE4-related: Harmonization of BDOF and PROF [[W. Chen (InterDigital)](mailto:Wei.Chen@InterDigital.com)]

[JVET-O0594](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7209) CE4-related: Prediction sample padding unification for BDOF and PROF [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0905](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7527) Crosscheck of JVET-O0594 (CE4-related: Prediction sample padding unification for BDOF and PROF) [H. Huang (Qualcomm)]

[JVET-O0595](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7210) CE4-related: Modified adaptive motion vector resolution (AMVR) for affine mode [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0933](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7558) Crosscheck of JVET-O0595 (CE4-related: Modified adaptive motion vector resolution (AMVR) for affine mode) [G. Li (Tencent)]

[JVET-O0601](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7216) CE4-related: On motion vector population under triangle prediction mode [X. Wang, Y.-W. Chen, X. Xiu, T.-C. Ma (Kwai Inc.)]

[JVET-O0604](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7219) CE4-related: Simplification on block location validation in SbTMVP [Y.-W. Chen, X. Xiu, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0785](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7406) Crosscheck of JVET-O0604: CE4-related: Simplification on block location validation in SbTMVP [Y.-C. Sun (Alibaba)]

[JVET-O0605](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7220) CE4-related: On exclusively applying LIC and PROF [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0830](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7451) Crosscheck of JVET-O0605 (CE4-related: On exclusively applying LIC and PROF) [H. Chen (Huawei)]

[JVET-O0607](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7222) CE4-related: On handling the interaction of the mvd\_l1\_zero\_flag and sym\_mvd\_flag [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0624](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7239) CE4-related: Unified padding for BDOF and PROF [K. Zhang, L. Zhang, H. Liu, J. Xu, Y. Wang (Bytedance)]

[JVET-O0814](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7435) Crosscheck of JVET-O0624 (CE4-related: Unified padding for BDOF and PROF) [[T.-H. Li](mailto:thli@fginnov.com), J.-Y. Deng (Foxconn)]

[JVET-O0629](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7244) Non-CE4: Simplification of motion field for triangle partition [R.-L. Liao, Y. Ye, J. Chen (Alibaba)]

[JVET-O0792](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7413) Crosscheck of JVET-O0629 (Non-CE4: Simplification of motion field for triangle partition) [Y.-L. Hsiao (MediaTek)]

[JVET-O0630](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7245) Non-CE4: DMVR on merge mode [R.-L. Liao, J. Chen, Y. Ye, J. Luo (Alibaba)]

[JVET-O0906](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7528) Crosscheck of JVET-O0630 (Non-CE4: DMVR on merge mode) [H. Huang (Qualcomm)]

[JVET-O0631](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7246) Non-CE4: DMVR on SMVD and AMVP mode [R.-L. Liao, Y. Ye, J. Chen, J. Luo (Alibaba)]

[JVET-O0907](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7529) Crosscheck of JVET-O0631 (Non-CE4: DMVR on SMVD and AMVP mode) [H. Huang (Qualcomm)]

[JVET-O0633](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7248) Non-CE4: On MMVD candidate flag coding [X. Chen, H. Yang (Huawei)]

[JVET-O0927](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7552) Crosscheck of JVET-O0633 (Non-CE4: On MMVD candidate flag coding) [T. Hashimoto, T. Ikai (Sharp)]

[JVET-O0639](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7255) Non-CE4: Context reduction for merge mode [J. Ye, X. Li, C. Auyeung, S. Liu (Tencent)]

[JVET-O0911](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7533) Crosscheck of JVET-O0639: Non-CE4: Context reduction for merge mode [W. Zhu (Bytedance)]

[JVET-O0652](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7268) CE4-related: Line buffer size reduction method for switchable interpolation filter [T. Solovyev, S. Ikonin, E. Alshina (Huawei)] [late]

[JVET-O0653](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7269) Non-CE4: Motion field storage simplification for triangular prediction mode [T. Solovyev, S. Ikonin, E. Alshina (Huawei)]

[JVET-O0660](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7277) Simplification of MV storage for blending block in triangle mode [K. Panusopone, L. Wang (Nokia)] [late]

[JVET-O0664](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7281) CE4-related: Simplifications on PROF [H. Chen, H. Yang (Huawei)]

[JVET-O0732](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7353) Crosscheck of JVET-O0664: CE4-related: Simplifications on PROF [[Y. He (InterDigital)](mailto:yuwen.he@interdigital.com)]

[JVET-O0666](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7283) Non-CE4: Fix for missing MV in 4xN and Nx4 triangle partition mode [K. Reuzé, C.-C Chen, W.-J. Chien, H. Huang, M. Karczewicz (Qualcomm)]

[JVET-O0789](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7410) CE4-related: On the context variables and syntax elements of merge index [C.-Y. Lai (MediaTek)] [miss] [late]

[JVET-O0795](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7416) CE4-related: On the context variables and syntax elements of merge index [?? (Mediatek)] [miss] [late]

[JVET-O0849](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7470) Crosscheck of JVET-O0795 (CE4-related: On the context variables and syntax elements of merge index) [R.-L. Liao (Alibaba)]

## CE5 related – Loop filtering (38)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0047](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6651) Non-CE5: Alternative signalling for ALF clipping parameters [A. M. Kotra, V. Stepin, S. Esenlik, S. Ikonin, B. Wang, H. Gao, E. Alshina (Huawei)]

[JVET-O0058](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6662) Non-CE5: Simplification on ALF clipping parameter coding [S.-C. Lim, J. Kang, H. Lee, J. Lee, H. Y. Kim (ETRI)]

[JVET-O0064](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6668) Non-CE5: Modification of clipping value signalling for adaptive loop filter [N. Hu, H. E. Egilmez, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0839](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7460) Crosscheck of JVET-O0064: Non-CE5: Modification of clipping value signalling for adaptive loop filter [L. Zhang (Bytedance)]

[JVET-O0067](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6671) Non-CE5: Modified coding method for ALF clipping parameters [L. Zhang, K. Zhang, H. Liu, J. Xu, Y. Wang (Bytedance)]

[JVET-O0892](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7514) Crosscheck of JVET-O0067 (Non-CE5: Modified coding method for ALF clipping parameters) [N. Hu (Qualcomm)]

[JVET-O0158](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6762) Non-CE5: Deblocking based on DMVR refined motion [K. Andersson, J. Enhorn]

[JVET-O0159](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6763) Non-CE5: Deblocking tC table defined for 10-bit video [K. Andersson, J. Enhorn]

[JVET-O0188](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6792) Non-CE5: Non-linear ALF simplification [S. Ikonin, V. Stepin, E. Alshina (Huawei)]

[JVET-O0216](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6821) Non-CE5: Modification of ALF coefficient signalling [A. M. Kotra, S. Esenlik, B. Wang, H. Gao, E. Alshina (Huawei)]

[JVET-O0786](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7407) Crosscheck of JVET-O0216 (Non-CE5: Modification of ALF coefficient signalling) [C.-Y. Lai (MediaTek)]

[JVET-O0247](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6852) AHG17: Multiple ALF APS for chroma [V. Seregin, N. Hu, M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0287](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6892) CE5-related: Simplification of subblock deblocking [C.-M. Tsai, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek), A. M. Kotra, S. Esenlik, B. Wang, H. Gao, E. Alshina (Huawei)]

[JVET-O0288](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6893) CE5-related: On the syntax constraints of ALF APS [O. Chubach, C.-Y. Chen, T.-D. Chuang, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0831](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7452) Crosscheck of JVET-O0288 (CE5-related: On the syntax constraints of ALF APS) [S.-C. Lim, J. Kang, H. Lee, J. Lee (ETRI)]

[JVET-O0289](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6894) CE5-related: Redundancy removal in ALF coefficient coding [O. Chubach, C.-Y. Chen, T.-D. Chuang, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0290](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6895) CE5-related: Modified syntax design of ALF clipping parameters signalling [O. Chubach, C.-Y. Chen, T.-D. Chuang, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0832](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7453) Crosscheck of JVET-O0290 (CE5-related: Modified syntax design of ALF clipping parameters signalling) [S.-C. Lim, J. Kang, H. Lee, J. Lee (ETRI)]

[JVET-O0374](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6979) CE5-related: Context reduction for ALF enable flag syntax element [L. Xu, X. Cao, F. Chen, L. Wang (Hikvision)]

[JVET-O0386](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6991) Non-CE5: A simplification of clipping thresholds for Non-Linear ALF [K. Unno, K. Kawamura, S. Naito (KDDI)]

[JVET-O0413](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7018) Non-CE5: Chroma deblocking filter adjustments for 4:4:4 and 4:2:2 [M. Ikeda, T. Suzuki (Sony)]

[JVET-O0751](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7372) Crosscheck of JVET-O0413 (Chroma deblocking filter adjustments for 4:4:4 and 4:2:2) [T. Chujoh (Sharp)]

[JVET-O0425](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7030) Non-CE5: Supplementary results on alternative filter sets for Adaptive Loop Filter (JVET-O0090) [J. Taquet, P. Onno, C. Gisquet, G. Laroche (Canon)]

[JVET-O0430](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7035) Non-CE5: Modification of clipping value signalling for ALF [X. W. Meng (PKU), X. Zheng (DJI), S. S. Wang, S. W. Ma (PKU)]

[JVET-O0896](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7518) Crosscheck of JVET-O0430 (Non-CE5: Modification of clipping value signalling for ALF) [N. Hu (Qualcomm)]

[JVET-O0437](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7042) Non-CE5: Unification of non-linear ALF luma/chroma clipping parameters [X. W. Meng (PKU), X. Zheng (DJI), S. S. Wang, S. W. Ma (PKU)]

[JVET-O0897](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7519) Crosscheck of JVET-O0437 (Non-CE5: Unification of non-linear ALF luma/chroma clipping parameters) [N. Hu (Qualcomm)]

[JVET-O0499](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7106) Non-CE5: Parameter alignment for pipelined ALF [A. Wieckowski, K. Sühring, R. Skupin, B. Bross, H. Schwarz, D. Marpe, T. Wiegand (HHI)]

[JVET-O0894](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7516) Crosscheck of JVET-O0499 (Non-CE5: Parameter alignment for pipelined ALF) [N. Hu (Qualcomm)]

[JVET-O0532](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7139) Non-CE5: Simplification on clipping value calculation for adaptive loop filter [N. Hu, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0566](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7181) Non-CE5: Consistent Deblocking for Chroma Components [J. Xu, J. Wang, L. Zhang, W. Zhu, Y. Wang (Bytedance)]

[JVET-O0846](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7467) Crosscheck of JVET-O0566 (Non-CE5: Consistent Deblocking for Chroma Components) [M. Ikeda (Sony)]

[JVET-O0578](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7193) Non-CE5: Long-tap deblocking filter on vertical tile boundary [H. Jang, S. Lee, N. Park, J. Nam, S. Kim, J. Lim (LGE)]

[JVET-O0582](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7197) Non-CE5: Deblock filtering process for BDPCM block [H. Jang, J. Nam, S. Kim, J. Lim (LGE)]

[JVET-O0807](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7428) Crosscheck of JVET-O0582 (Non-CE5: Deblock filtering process for BDPCM block) [C.-M. Tsai (MediaTek)]

[JVET-O0625](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7240) Non-CE5: Unified padding method for samples at variant boundaries in ALF [H. Liu, L. Zhang, K. Zhang, Z. Deng, N. Zhang, J. Xu (Bytedance)]

[JVET-O0865](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7486) Crosscheck of JVET-O0625: "Non-CE5: Unified padding method for samples at variant boundaries in ALF" [F. Le Léannec (Interdigital)]

[JVET-O0636](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7252) Cross-Component Adaptive Loop Filter for chroma [K. Misra, F. Bossen, A. Segall (Sharp Labs of America)]

[JVET-O0637](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7253) Non-CE5: On chroma line selection for gradient computation in deblocking [K. Misra, P. Cowan, A. Segall (Sharp Labs of America)]

[JVET-O0638](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7254) Non-CE5: On derivation of QP predictors for DUAL\_TREE\_CHROMA in deblocking [K. Misra, A. Segall (Sharp Labs of America)]

[JVET-O0648](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7264) Non-CE5: On Exp-Golomb Coding of ALF Filter Coefficients and Clip Values [H. E. Egilmez, N. Hu, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0142](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6746) AHG12: On turning off ALF filtering at brick and slice boundaries [Y.-K. Wang, J. Chen, Hendry (Futurewei)]

Also included in the agenda item 6.17.6.

[JVET-O0494](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7101) AHG12: On filtering of independently coded region [R. Skupin, Y. Sanchez, K. Sühring, T. Schierl (HHI)]

Also included in the agenda item 6.17.6.

[JVET-O0643](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7259) Modification on Delta QP for Deblocking [Y. Han, G. Van Der Auwera, M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0654](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7270) AHG12/Non-CE5: Unification of boundary handling for adaptive loop filter [N. Hu, V. Seregin, M. Karczewicz (Qualcomm)]

Also included in the agenda item 6.17.6.

[JVET-O0939](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7564) Crosscheck of JVET-O0654 (AHG12/Non-CE5: Unification of boundary handling for adaptive loop filter) [S. Paluri (LGE)]

[JVET-O0662](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7279) Non-CE5: Modified ALF filtering for Slice, Brick and Virtual boundaries [A. M. Kotra, S. Esenlik, B. Wang, H. Gao, E. Alshina (Huawei)]

Also included in the agenda item 6.17.6.

[JVET-O0656](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7273) Non-CE5: Boundary strength derivation for transform skipped block [S. Iwamura, S. Nemoto, A. Ichigaya (NHK)]

[JVET-O0847](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7468) Crosscheck of JVET-O0656 (Non-CE5: Boundary strength derivation for transform skipped block) [[K. Kondo](mailto:Kenji.Kondo@sony.com), M. Ikeda (Sony)]

[JVET-O0665](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7282) Non-CE5: Syntax redundancy removal in adaptive loop filter [X. W. Meng (PKU), X. Zheng (DJI), S. S. Wang, S. W. Ma (PKU)]

[JVET-O0923](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7548) Crosscheck of JVET-O0665 (Non-CE5: Syntax redundancy removal in adaptive loop filter) [S. Ye (Hikvision)]

[JVET-O0669](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7286) Non-CE5: A Simplification of ALF Coefficient Signalling [H. E. Egilmez, N. Hu, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0803](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7424) Non-CE5: Unified design for longer tap deblocking line buffer reduction [A. M. Kotra, S. Esenlik, B. Wang, H. Gao, E. Alshina (Huawei)] [late]

[JVET-O0809](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7430) Crosscheck of JVET-O0803 (Non-CE5: Unified design for longer tap deblocking line buffer reduction) [C.-M. Tsai (MediaTek)]

## CE6 related – Transforms and transform signalling (50)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0195](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6800) Non-CE6: Simplification on LFNST [Y. Zhao, H. Yang (Huawei)]

[JVET-O0209](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6814) Non-CE6: LFNST simplification based on the methods proposed in CE6-2.1a [M. Koo, J. Nam, J. Lim, M. Salehifar, S. Kim (LGE)]

[JVET-O0213](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6818) Non-CE6: Block size restriction of LFNST [M. Koo, J. Nam, J. Lim, M. Salehifar, S. Kim (LGE)]

[JVET-O0884](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7506) Crosscheck of JVET-O0213 (Non-CE6: Block size restriction of LFNST) [Z. Zhang (Ericsson)]

[JVET-O0214](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6819) Non-CE6: Harmonization of implicit MTS and LFNST [T. Ikai (Sharp)]

[JVET-O0219](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6824) Non-CE6: On LFNST transform set selection for a CCLM coded block [Z. Zhang, R. Sjöberg, R. Yu (Ericsson)]

[JVET-O0264](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6869) CE6-related: On DCT-8 in VVC [K. Zhang, L. Zhang, Z. Deng, H. Liu, N. Zhang (Bytedance)]

[JVET-O0916](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7538) Crosscheck of JVET-O0264: CE6-related: On DCT-8 in VVC [C. Hollmann (Ericsson)]

[JVET-O0876](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7497) Crosscheck of JVET-O0266 (Non-CE6: Simplifications on LFNST) [T. Tsukuba (Sony)]

[JVET-O0266](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6871) Non-CE6: Simplifications on LFNST [K. Zhang, L. Zhang, Z. Deng, H. Liu, N. Zhang (Bytedance)]

[JVET-O0291](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6896) CE6-related: Constraint and simplification for LFNST signalling [M.-S. Chiang, C.-W. Hsu, T.-D. Chuang, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0292](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6897) CE6-related: Simplifications for LFNST [M.-S. Chiang, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0293](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6898) CE6-related: Latency reduction for LFNST signalling [M.-S. Chiang, C.-W. Hsu, T.-D. Chuang, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0294](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6899) CE6-related: Context modelling for signalling MTS index [O. Chubach, M.-S. Chiang, C.-Y. Chen, T.-D. Chuang, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0917](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7539) Crosscheck of JVET-O0294: CE6-related: Context modelling for signalling MTS index [C.-H. Hung (Qualcomm)]

[JVET-O0349](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6954) CE6-related: Simplified LFNST [J. Lainema (Nokia)]

[JVET-O0350](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6955) CE6-related: LFNST with one mode [J. Lainema (Nokia)]

[JVET-O0352](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6957) Non-CE6: TU/TB-level LFNST index coding [T. Tsukuba, M. Ikeda, Y. Yagasaki, T. Suzuki (Sony)]

[JVET-O0743](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7364) Crosscheck of JVET-O0352 (Non-CE6: TU/TB-level LFNST index coding) [K. Zhang (Bytedance)]

[JVET-O0354](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6959) Non-CE6: Context modeling for MTS index coding [T. Tsukuba, M. Ikeda, Y. Yagasaki, T. Suzuki (Sony)]

[JVET-O0711](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7331) Crosscheck of JVET-O0354 (Non-CE6: Context modeling for MTS index coding) [J. Nam (LGE)]

[JVET-O0357](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6962) Non-CE6: Maximum TU size for chroma format 4:2:2 and 4:4:4 [L. Li, J. Nam, J. Lim, S. Kim (LGE)]

[JVET-O0368](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6973) Non-CE6: An MTS-based Restriction for LFNST beyond Transform Skip [H. E. Egilmez, A. K. Ramasubramonian, A. Said, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0863](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7484) Crosscheck of JVET-O0368: "Non-CE6: An MTS-based Restriction for LFNST beyond Transform Skip" [F. Le Léannec (Interdigital)]

[JVET-O0370](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6975) CE6-related: Applying LFNST Only for Top-Left Subblocks [H. E. Egilmez, A. Nalci, A. Said, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0372](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6977) Non-CE6: A Simplification for MTS Index Signaling [A. Nalci, H. E. Egilmez, A. Said, M. Coban, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0373](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6978) Non-CE6: An Improved Context Modeling for LFNST [A. Nalci, H. E. Egilmez, A. Said, M. Coban, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0380](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6985) Non-CE6: Simplification on MTS index coding [X. Cao, F. Chen, L. Wang (Hikvision)]

[JVET-O0381](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6986) Non-CE6: Simplification on SBT coding [X. Cao, F. Chen, L. Wang (Hikvision)]

[JVET-O0752](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7373) Crosscheck of JVET-O0381 (Non-CE6: Simplification on SBT coding) [Y. Zhao (Huawei)]

[JVET-O0382](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6987) Non-CE6: Unification between implicit MTS and ISP [X. Cao, F. Chen, L. Wang (Hikvision)]

[JVET-O0387](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6992) Non-CE6: MTS Index Signaling Simplification [A. Tamse, M. W. Park, K. Choi (Samsung)]

[JVET-O0852](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7473) Crosscheck of JVET-O0387 (Non-CE6: MTS Index Signaling Simplification) [Z. Deng (Bytedance)]

[JVET-O0402](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7007) Non-CE6: Simplification of context modeling on MTS index [J. Nam, M. Koo, J. Lim, S. Kim (LGE)]

[JVET-O0877](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7498) Crosscheck of JVET-O0402 (Non-CE6: Simplification of context modeling on MTS index) [T. Tsukuba (Sony)]

[JVET-O0410](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7015) CE6-related: Signalling of Low Frequency Non-Separable Transform [Y. Kidani, K. Unno, K. Kawamura, S. Naito (KDDI)]

[JVET-O0444](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7049) Non-CE6: On LFNST index coding [C. Hollmann, D. Saffar, P. Wennersten, J. Ström (Ericsson)]

[JVET-O0873](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7494) Crosscheck of JVET-O0444 (Non-CE6: On LFNST index coding) [J. Lainema (Nokia)]

[JVET-O0446](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7051) Non-CE6: Reduced MTS [C. Hollmann, D. Saffar, P. Wennersten, J. Ström (Ericsson)]

[JVET-O0808](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7429) Crosscheck of JVET-O0446 (Non-CE6: Reduced MTS) [[C.-H. Hung](mailto:chaohsiu@qti.qualcomm.com) (Qualcomm)]

[JVET-O0465](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7072) Non-CE6: simplified SBT mode coding [F. Le Léannec, Y. Chen, T. Poirier (InterDigital)]

[JVET-O0853](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7474) Crosscheck of JVET-O0465 (Non-CE6: Simplified SBT mode coding) [Z. Deng (Bytedance)]

[JVET-O0466](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7073) Non-CE6: interaction between MTS and LFNST [F. Le Léannec, F. Urban, K. Naser, T. Poirier (InterDigital)]

[JVET-O0915](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7537) Crosscheck of JVET-O0466: Non-CE6: interaction between MTS and LFNST [C. Hollmann (Ericsson)]

[JVET-O0472](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7079) Non-CE6: Simplified LFNST signalling [J. Jung, D. Kim, G. Ko, J. Son, J. Kwak (WILUS)]

[JVET-O0474](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7081) Non-CE6: Mismatch between text specification and reference software on MTS for IBC-coded block [J. Jung, D. Kim, G Ko, J. Son, J. Kwak (WILUS)]

[JVET-O0475](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7082) Non-CE6: Context Simplification for Subblock Transform (SBT) [J. Park, B. Jeon (SKKU)]

[JVET-O0764](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7385) Crosscheck of JVET-O0475: Non-CE6: Context Simplification for Subblock Transform (SBT) [L. Pham Van, G. Van der Auwera, M. Karczewicz (Qualcomm)]

[JVET-O0476](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7083) Non-CE6: Syntax change for LFNST [D. Park, Y.-U. Yoon, J.-G. Kim (KAU), J. Lee, J. Kang (ETRI)]

[JVET-O0518](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7125) Non-CE6: LFNST selection with no intra-mode dependency [G. Kulupana, A. Seixas Dias, S. Blasi (BBC)]

[JVET-O0521](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7128) Non-CE6: Removing LFNST when MTS larger than zero [G. Kulupana, S. Blasi, A. Seixas Dias (BBC)]

[JVET-O0528](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7135) Non-CE6: Simplified Transform Selection For ISP [K. Naser, F. Le Léannec, T. Poirier, P. de Lagrange (InterDigital)]

[JVET-O0529](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7136) Non-CE6 Interaction between Implicit MTS and LFNST and MIP [K. Naser, F. Le Léannec, T. Poirier (InterDigital)]

[JVET-O0538](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7145) Non-CE6: An SPS Control of ISP and SBT Transform Selection [[K. Naser](mailto:karam.naser@interdigital.com), F. Le Léannec, M. Kerdranvat (InterDigital)]

[JVET-O0539](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7146) CE6-related: Unified LFNST using block size independent kernel [X. Zhao, X. Li, S. Liu (Tencent)]

[JVET-O0899](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7521) Crosscheck of JVET-O0539 (CE6-related: Unified LFNST using block size independent kernel) [M. Siekmann (HHI)]

[JVET-O0934](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7559) Crosscheck of JVET-O0539: CE6-related: Unified LFNST using block size independent kernel [C. Hollmann (Ericsson)]

[JVET-O0540](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7147) Non-CE6: Harmonization of LFNST, MIP and implicit MTS [X. Zhao, X. Li, S. Liu (Tencent)]

[JVET-O0541](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7148) Non-CE6: Modified high-level syntax control on implicit transform [S. Liu, X. Zhao, X. Li (Tencent)]

[JVET-O0544](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7154) Non-CE6: Modified LFNST Transform Matrices for Fast Implementation [K. Naser, L. Kerofsky, F. Le Léannec, T. Poirier (InterDigital)]

[JVET-O0545](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7156) Non-CE6: Configurable maximum transform size in VVC [X. Zhao, X. Li, S. Liu (Tencent)]

[JVET-O0941](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7568) Crosscheck of JVET-O0545 (Non-CE6: Configurable maximum transform size in VVC) [T.-C. Ma (Kwai Inc.)]

[JVET-O0546](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7157) Non-CE3/6: Transform selection for MRL [L. Zhao, X. Zhao, X. Li, S. Liu (Tencent)]

[JVET-O0680](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7299) Crosscheck of JVET-O0546 (Non-CE3/6: Transform selection for MRL) [J. Jung, D. Kim, G. Ko, J. Son, J. Kwak (WILUS)]

[JVET-O0569](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7184) Non-CE6: LFNST Signaling at the TU Level [A. Nalci, H. E. Egilmez, A. Said, M. Coban, V. Seregin, W.-J. Chien, M. Karczewicz (Qualcomm)]

[JVET-O0576](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7191) Non-CE6: Reduced contexts and context coded bins for MTS index coding [Z. Deng, L. Zhang, H. Liu, K. Zhang, Y. Wang (Bytedance)]

[JVET-O0837](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7458) Crosscheck of JVET-O0576 (Non-CE6: Reduced contexts and context coded bins for MTS index coding) [A. Tamse (Samsung)]

[JVET-O0620](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7235) Non-CE6: Simplification with new LFNST transform basis [T. Zhou, T. Hashimoto, T. Ikai (Sharp)] [miss] [late]

[JVET-O0719](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7339) Crosscheck of JVET-O0620 (Non-CE6: Simplification with new LFNST transform basis) [L. Xu, F. Chen, L. Wang (Hikvision)]

[JVET-O0642](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7258) Non-CE6: Reducing secondary transform kernel for specific residual block size [S. Shrestha, A. Kumar, B. Lee (Chosun Univ.), Y. Lee, J. Park (Humax)]

[JVET-O0699](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7319) Non-CE6: Modified DCT8/DST7 Transform Matrices for Faster Implementation [K. Naser, L. Kerofsky, F. Le Léannec, T. Poirier (InterDigital)] [late]

## CE7 related – Quantization and coefficient coding (25)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0068](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6672) AHG16/Non-CE7: A study of bin to bit ratio for VTM5.0 [M. Zhou (Broadcom)]

[JVET-O0193](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6798) CE7-related: Remove transform depth in cbf coding [Y. Zhao, H. Yang (Huawei)]

[JVET-O0773](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7394) Crosscheck of JVET-O0193 (CE7-related: Remove transform depth in cbf coding) [Y.-W. Chen (Kwai Inc.)]

[JVET-O0223](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6828) Non-CE7: Quantization matrices with single identifier and prediction from larger ones [P. de Lagrange, F. Le Léannec, E. François, K. Naser (InterDigital)]

[JVET-O0924](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7549) Crosscheck of JVET-O0223 (Non-CE7: Quantization matrices with single identifier and prediction from larger ones) [J. Le Tanou (MediaKind)]

[JVET-O0231](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6836) Non-CE7: Modified chroma coded block flag signalling [S. Esenlik, Y. Zhao, A. M. Kotra, H. Gao, B. Wang, E. Alshina (Huawei)]

[JVET-O0872](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7493) Crosscheck of JVET-O0231 (Non-CE7: Modified chroma coded block flag signalling) [J. Lainema (Nokia)]

[JVET-O0295](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6900) CE7-related: Modified subblock coding passes for coding TS residual [S.-T. Hsiang, S.-M. Lei (MediaTek)]

[JVET-O0937](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7562) Crosscheck of JVET-O0295 (CE7-related: Modified subblock coding passes for coding TS residual) [M. Salehifar (LGE)]

[JVET-O0375](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6980) Non-CE7: A Simplified CBF Coding for Luma and Chroma [A. Nalci, H. E. Egilmez, A. Said, M. Coban, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0383](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6988) Non-CE7: Harmonization of scaling matrix and LFNST [T. Hashimoto, E. Sasaki, T. Chujoh, T. Ikai (Sharp)]

[JVET-O0791](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7412) Crosscheck of JVET-O0383 (Non-CE7: Harmonization of scaling matrix and LFNST) [Y.-L. Hsiao (MediaTek)]

[JVET-O0400](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7005) CE7-related: Context-coded bin restriction [J. Choi, S. Yoo, J. Heo, J. Choi, J. Lim, S. Kim (LGE)]

[JVET-O0909](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7531) Crosscheck of JVET-O0400: CE7-related: Context-coded bin restriction [W. Zhu (Bytedance)]

[JVET-O0406](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7011) Non-CE7: Unification of syntaxes after CCB count exceeds the maximum number between transform residual and transform skip residual [Y. Kato, K. Abe, T.Toma (Panasonic)]

[JVET-O0788](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7409) Crosscheck of JVET-O0406 (Non-CE7: Unification of syntaxes after CCB count exceeds the maximum number between transform residual and transform skip residual) [C.-Y. Lai (MediaTek)]

[JVET-O0409](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7014) Non-CE7: Unification of CCB count method between transform residual and transform skip residual [Y. Kato, K. Abe, T. Toma (Panasonic)]

[JVET-O0527](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7134) Non-CE7: Use INTER quantization matrices for IBC [P. de Lagrange, Y. Chen (InterDigital)]

[JVET-O0543](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7151) CE7-related: Alternative configuration for joint chroma residual coding [C. Helmrich, H. Schwarz, T. Nguyen, C. Rudat, D. Marpe, T. Wiegand (Fraunhofer HHI)]

[JVET-O0874](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7495) Crosscheck of JVET-O0543 (CE7-related: Alternative configuration for joint chroma residual coding) [J. Lainema (Nokia)]

[JVET-O0556](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7170) CE7-related: Alternative method to RDPCM with TS level mapping [M. Karczewicz, Y. Han, Y.-H. Chao, M. Coban (Qualcomm)]

[JVET-O0774](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7395) Crosscheck of JVET-O0556 (CE7-related: Alternative method to RDPCM with TS level mapping) [Y.-W. Chen (Kwai Inc.)]

[JVET-O0557](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7171) Non-CE7: Context modeling unification in significant flag of coefficient group [Y.-H. Chao, M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0777](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7398) Crosscheck of JVET-O0557 (Non-CE7: Context modeling unification in significant flag of coefficient group) [Y.-W. Chen (Kwai Inc.)]

[JVET-O0558](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7172) CE7-related: Reduction of Gtx passes in transform skip residual coding [M. Karczewicz, Y.-H. Chao, H. Wang, M. Coban (Qualcomm)]

[JVET-O0775](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7396) Crosscheck of JVET-O0558 (CE7-related: Reduction of Gtx passes in transform skip residual coding) [Y.-W. Chen (Kwai Inc.)]

[JVET-O0559](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7173) CE7-related: QP dependent binarization in TS residual coding [M. Karczewicz, Y.-H. Chao, M. Coban (Qualcomm)]

[JVET-O0776](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7397) Crosscheck of JVET-O0559 (CE7-related: QP dependent binarization in TS residual coding) [Y.-W. Chen (Kwai Inc.)]

[JVET-O0575](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7190) CE7-related: Reduction for Context Coded Bins in Transform Skip Mode [W. Zhu, L. Zhang, J. Xu, K. Zhang, H. Liu (Bytedance)]

[JVET-O0596](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7211) CE7-related: On chroma CBF signaling of subblock transform mode [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0753](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7374) Crosscheck of JVET-O0596 (CE7-related: On chroma CBF signaling of subblock transform mode) [Y. Zhao (Huawei)]

[JVET-O0613](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7228) CE7-related: Unification of Rice parameter tables in residual coding [C. Auyeung, X. Li, S. Liu (Tencent)]

[JVET-O0758](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7379) Crosscheck of JVET-O0613 (CE7-related: Unification of Rice parameter tables in residual coding) [H.-J. Jhu (Kwai Inc.)]

[JVET-O0617](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7232) CE7-related: Context reduction of transform coefficient significance flag [C. Auyeung, X. Zhao, X. Li, S. Liu (Tencent)]

[JVET-O0936](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7561) Crosscheck of JVET-O0617 (CE7-related: Context reduction of transform coefficient significance flag) [H. Schwarz (Fraunhofer HHI)]

[JVET-O0619](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7234) Non-CE7: Simplification of transform skip residual coding [M. G. Sarwer, Y. Ye (Alibaba)]

[JVET-O0878](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7499) Crosscheck of JVET-O0619 (Non-CE7: Simplification of transform skip residual coding) [T. Tsukuba (Sony)]

[JVET-O0623](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7238) CE7-related: Interleaved coefficient coding for transform-skip mode [M. Karczewicz, H. Wang, M. Coban, Y.-H. Chao (Qualcomm)]

[JVET-O0690](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7310) Crosscheck of JVET-O0623: Interleaved coefficient coding for transform-skip mode [T. Nguyen (HHI)]

[JVET-O0670](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7287) CE7-related: Alternative joint coding of chroma residuals [B. Ray, G. Van der Auwera, M. Karczewicz (Qualcomm)]

[JVET-O0875](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7496) Crosscheck of JVET-O0670 (CE7-related: Alternative joint coding of chroma residuals) [J. Lainema (Nokia)]

[JVET-O0928](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7553) CE7-related: Context reduction for entropy coding sig\_coeff\_flag [S.-T. Hsiang, S.-M. Lei (MediaTek)] [late] [miss]

[JVET-O0935](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7560) CE7-related: Alternative configuration of CE7-2.2 joint chroma residual coding [C. Helmrich, H. Schwarz, T. Nguyen, C. Rudat, D. Marpe, T. Wiegand (Fraunhofer HHI, B. Ray, G. Van der Auwera, A. K. Ramasubramonian, M. Coban, M. Karczewicz (Qualcomm)] [late]

## CE8 related – Screen content coding tools (46)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0127](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6731) CE8-related: IBC modifications [J. Li, C.S. Lim (Panasonic)]

[JVET-O0738](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7359) Crosscheck of JVET-O0127 (CE8-related: IBC modifications) [J. Xu (Bytedance)]

[JVET-O0162](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6766) Non-CE8: On IBC motion vector list construction [H. Chen, H. Yang (Huawei)]

[JVET-O0165](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6769) CE8-related: SPS flag for BDPCM [F. Henry (Orange)]

[JVET-O0166](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6770) CE8-related: BDPCM for chroma [F. Henry (Orange), G. Clare (bcom)]

[JVET-O0696](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7316) Crosscheck of JVET-O0166 (CE8-related: BDPCM for chroma) [Y.-H. Chao (Qualcomm)]

[JVET-O0200](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6805) CE8-related: RDPCM intra mode mapping, maximum RDPCM block size [M. Coban, Y. Han, M. Karczewicz (Qualcomm)]

[JVET-O0205](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6810) CE8-related: Improvements on BDPCM [S. Yoo, J. Choi, J. Nam, J. Heo, J. Lim, S. Kim (LGE)]

[JVET-O0705](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7325) Crosscheck of JVET-O0205: CE8-related: Improvements on BDPCM [X. Zhao (Tencent)]

[JVET-O0206](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6811) CE8-related: Unified condition between BDPCM and transform skip [S. Yoo, J. Nam, J. Choi, J. Lim, S. Kim (LGE)]

[JVET-O0689](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7309) Crosscheck of JVET-O0206: Unified condition between BDPCM and transform skip [T. Nguyen (HHI)]

[JVET-O0248](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6853) CE8-related: Dedicated IBC reference buffer without bitstream restrictions [H. Gao, A.Karabutov, S. Esenlik, S. Ikonin, B. Wang, A. M. Kotra, E. Alshina (Huawei)]

[JVET-O0739](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7360) Crosscheck of JVET-O0248 (CE8-related: Dedicated IBC reference buffer without bitstream restrictions) [J. Xu (Bytedance)]

[JVET-O0258](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6863) Non-CE8: Adaptive single/dual tree with IBC simplification [W. Zhu, J. Xu, L. Zhang (Bytedance)]

[JVET-O0695](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7315) Crosscheck of JVET-O0258 (Non-CE8: Adaptive single/dual tree with IBC simplification) [C.-H. Hung, Y.-H. Chao (Qualcomm)]

[JVET-O0259](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6864) CE8-related: Palette mode with 8 bits entries [W. Zhu, J. Xu, L. Zhang, H.-C. Chuang (Bytedance)]

[JVET-O0889](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7511) Crosscheck of JVET-O0259 (CE8-related: Palette mode with 8 bits entries) [F. Henry (Orange), G. Clare (bcom)]

[JVET-O0282](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6887) CE8-releated: Bug Fix in CE8-2.1 [C.-H. Hung, Y.-H. Chao, W.-J. Chien, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0787](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7408) Crosscheck of JVET-O0282 (CE8-related: Bug fix in CE8-2.1) [C.-Y. Lai (MediaTek)]

[JVET-O0296](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6901) CE8-related: Intra prediction mode for BDPCM [M.-S. Chiang, O. Chubach, C.-W. Hsu, T.-D. Chuang, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0913](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7535) Crosscheck of JVET-O0296: CE8-related: Intra prediction mode for BDPCM [W. Zhu (Bytedance)]

[JVET-O0315](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6920) CE8-related: Intra prediction mode alignment for BDPCM [W. Zhu, L. Zhang, J. Xu (Bytedance)]

[JVET-O0325](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6930) CE8-related: Simple BVD coding scheme without adding more context [Y. Sun, F. Chen, L. Wang (Hikvision)]

[JVET-O0709](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7329) Crosscheck of JVET-O0325 (CE8-related: Simple BVD coding scheme without adding more context) [J. Nam (LGE)]

[JVET-O0326](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6931) CE8-related: JVET-O0325 combined with CE8 BVD coding scheme [Y. Sun, F. Chen, L. Wang (Hikvision)]

[JVET-O0710](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7330) Crosscheck of JVET-O0326 (CE8-related: JVET-O0325 combined with CE8 BVD coding scheme) [J. Nam (LGE)]

[JVET-O0327](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6932) CE8-related: Default filling method for IBC mode [S. Ye, Y. Sun, F. Chen, L. Wang (Hikvision)]

[JVET-O0840](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7461) Crosscheck of JVET-O0327: CE8-related: Default filling method for IBC mode [J. Chen (Alibaba)]

[JVET-O0908](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7530) Crosscheck of JVET-O0327: CE8-related: Default filling method for IBC mode [W. Zhu (Bytedance)]

[JVET-O0331](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6936) Non-CE8: On IBC BVP Candidate List Derivation [H. Lee, S.-C. Lim, J. Lee, J. Kang, H. Y. Kim (ETRI)]

[JVET-O0342](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6947) Non-CE8: Unification of intra prediction mode of BDPCM block [J. Lee, J. Kang, H. Lee, S.-C. Lim, H. Y. Kim (ETRI)]

[JVET-O0683](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7302) Crosscheck of JVET-O0342: Non-CE8: Unification of intra prediction mode of BDPCM block [X. Zhao (Tencent)]

[JVET-O0376](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6981) CE8-related: A SPS level flag for BDPCM and JCCR [L. Xu, X. Cao, F. Chen (Hikvision)]

[JVET-O0772](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7393) Crosscheck of JVET-O0376 (CE8-related: A SPS level flag for BDPCM and JCCR) [Y.-W. Chen (Kwai Inc.)]

[JVET-O0405](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7010) Non-CE8: Minimum Allowed QP for Transform Skip Mode [T. Nguyen, B. Bross, H. Schwarz, D. Marpe, T. Wiegand (HHI)]

[JVET-O0442](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7047) [Non-CE8] IBC prediction improvement [A. Singh, A. Konda (Samsung)] [miss] [late]

[JVET-O0443](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7048) [Non-CE8] Flipping of reference blocks for Intra Block Copy (IBC) [A. Singh, A. Konda (Samsung)] [late]

[JVET-O0455](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7061) Non-CE8: On IBC merge mode [Y.-J. Chang, Y. Zhang, C.-C. Chen, W.-J. Chien, M. Karczewicz (Qualcomm)]

[JVET-O0463](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7070) Non-CE8: Intra prediction mode of BDPCM [D. Kim, G. Ko, J. Son, J. Kwak (WILUS), J. Jung (Gaudio Lab)]

[JVET-O0822](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7443) Crosscheck of JVET-O0463 (Non-CE8: Intra prediction mode of BDPCM) [S. Yoo, J. Lim (LGE)]

[JVET-O0480](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7087) Non-CE8: On signaling of IBC block vector predictor list size [X. Xu, X. Li, S. Liu (Tencent)]

[JVET-O0860](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7481) Crosscheck of JVET-O0480: Non-CE8: On signaling of IBC block vector predictor list size [X. Xiu (Kwai Inc.)]

[JVET-O0482](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7089) CE8-related: Modulo operations for IBC block vectors [X. Xu, X. Li, S. Liu (Tencent)]

[JVET-O0542](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7150) Non-CE8: Modified intra mode associated with BPDCM [X. Zhao, X. Li, X. Xu, S. Liu (Tencent)]

[JVET-O0563](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7177) Non-CE8: disabling TPM blending [W. Zhu, L. Zhang, J. Xu, K. Zhang (Bytedance)]

[JVET-O0922](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7547) Crosscheck of JVET-O0563 (Non-CE8: disabling TPM blending) [S. Ye (Hikvision)]

[JVET-O0564](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7178) CE8-related: Signalling of chroma IBC prediction modes [W. Zhu, L. Zhang, J. Xu, K. Zhang (Bytedance)]

[JVET-O0565](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7179) CE8-related: Default chroma IBC mode with vertical/ horizontal copying [W. Zhu, J. Xu, L. Zhang (Bytedance)]

[JVET-O0568](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7183) Non-CE8: An Alternative VPDU Memory Reuse for IBC [J. Xu, L. Zhang, K. Zhang, H. Liu, Y. Wang (Bytedance)]

[JVET-O0574](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7189) Non-CE8: Fixes of IBC BV candidate list [L. Zhang, J. Xu, K. Zhang, H. Liu, W. Zhu (Bytedance)]

[JVET-O0597](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7212) CE3/8-related: On signaling of PDPC enabling/disabling flag in SPS [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0929](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7554) Crosscheck of JVET-O0597 (CE3/8-related: On signaling of PDPC enabling/disabling flag in SPS) [Y.-H. Chao (Qualcomm)]

[JVET-O0600](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7215) CE8-related: Palette mode excluding small blocks [H.-J. Jhu, X. Xiu, Y.-W. Chen, T.-C. Ma, X. Wang (Kwai Inc.)]

[JVET-O0821](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7442) Crosscheck of JVET-O0600 (CE8-related: Palette mode excluding small blocks) [T.-S Chang (Alibaba)]

[JVET-O0602](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7217) Non-CE3/Non-CE8: Remove MIP/IBC mode storage from line buffer [H.-J. Jhu, Y.-W. Chen, X. Xiu, T.-C. Ma, X. Wang (Kwai Inc.)]

[JVET-O0813](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7434) Crosscheck of JVET-O0602 (Non-CE3/Non-CE8: Remove MIP/IBC mode storage from line buffer) [[T.-H. Li](mailto:thli@fginnov.com), J.-Y. Deng (Foxconn)]

[JVET-O0614](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7229) CE8-related: Combination of a simplified IBC mode and the palette mode in CE8-2.5 [Y.-C. Sun, X. Zuo, T.-S. Chang, J. Lou (Alibaba)]

[JVET-O0769](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7390) Crosscheck of JVET-O0614 (CE8-related: Combination of a simplified IBC mode and the palette mode in CE8-2.5) [Y.-W. Chen (Kwai Inc.)]

[JVET-O0626](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7241) Non-CE8: Simplified IBC BV candidate list construction process [L. Zhang, K. Zhang, H. Liu, J. Xu, Z. Deng, N. Zhang (Bytedance)]

[JVET-O0645](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7261) Non-CE4/8: Blending-off Switch for TPM Mode [C.-C. Chen, Y. Zhang, K. Reuze, Y.-J. Chang, W.-J. Chien, M. Karczewicz (Qualcomm)]

[JVET-O0912](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7534) Crosscheck of JVET-O0645: Non-CE4/8: Blending-off Switch for TPM Mode [W. Zhu (Bytedance)]

[JVET-O0646](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7262) Non-CE4/8: Combination of Blending-off Switch (JVET-O0645) and Simplified Motion Storage (JVET-O0411) for TPM Mode [C.-C. Chen, K. Reuze, Y. Zhang, W.-J. Chien, M. Karczewicz (Qualcomm)]

[JVET-O0763](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7384) Crosscheck of JVET-O0646 (Non-CE4/8: Combination of Blending-off Switch (JVET-O0645) and Simplified Motion Storage (JVET-O0411) for TPM Mode) [H. Yang (InterDigital)]

[JVET-O0651](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7267) Non-CE8: Intra prediction mode derivation for DM chroma block with corresponding IBC/PCM luma [L. Pham Van, G. Van der Auwera, A. K. Ramasubramonian, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0694](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7314) Crosscheck of JVET-O0651 (Non-CE8: Intra prediction mode derivation for DM chroma block with corresponding IBC/PCM luma) [L. Zhao (Tencent)]

[JVET-O0661](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7278) Non-CE4/8: Simplification of CIIP on IBC mode [Y. Zhang, C.-C. Chen, Y.-J. Chang, W.-J. Chien, M. Karczewicz (Qualcomm)]

[JVET-O0682](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7301) CE8-related: An Intra-Affine mode for screen content coding [J. Cao, Z.-R. Li, J. Wang, F. Liang (SYSU), Y.-F. Yu, Y. Liu (OPPO)] [late] [miss]

[JVET-O0706](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7326) CE8-related: 4x4 sub-block based dual-tree chroma IBC block-vector derivation [H. Yang, Y. He (InterDigital)] [late]

[JVET-O0740](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7361) Crosscheck of JVET-O0706 (CE8-related: 4x4 sub-block based dual-tree chroma IBC block-vector derivation) [J. Xu (Bytedance)]

[JVET-O0783](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7404) AHG17: Control of transform skip residual coding [L Chen, Y.-W. Huang, S.-M. Lei (MediaTek)] [late]

[JVET-O0914](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7536) CE8-related: Improved color generation method for palette mode [C.-H. Hung, Y.-H. Chao, V. Seregin, M. Karczewicz (Qualcomm)] [late] [miss]

[JVET-O0919](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7541) Non-CE8: Minimum QP for Transform Skip Mode [M. Karczewicz, Y.-H. Chao, H. Wang, M. Coban (Qualcomm)] [late]

## CE9 related – Decoder motion vector derivation (26)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0207](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6812) Non-CE9: A bug fix of BDOF specification [T. Chujoh, E. Sasaki, T. Ikai (Sharp)]

[JVET-O0208](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6813) Non-CE9: An improvement of early termination for BDOF [T. Chujoh, E. Sasaki, T. Ikai (Sharp)]

[JVET-O0210](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6815) Non-CE9: BDOF for chroma components [T. Chujoh, E. Sasaki, T. Ikai (Sharp)]

[JVET-O0679](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7298) Crosscheck of JVET-O0210 on BDOF for chroma components [[S. Iwamura (NHK)](mailto:iwamura.s-gc@nhk.or.jp)]

[JVET-O0702](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7322) Crosscheck of JVET-O0210 (Non-CE9: BDOF for chroma components) [N. Park (LGE)]

[JVET-O0211](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6816) Non-CE9: A speed up method of BDOF [T. Chujoh, T. Ikai (Sharp)]

[JVET-O0250](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6855) DMVR and BDOF on/off control [H. Huang, W.-J. Chien, V. Seregin, M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0684](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7303) Crosscheck of JVET-O0250 (DMVR and BDOF on/off control) [T. Chujoh (Sharp)]

[JVET-O0297](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6902) CE9-related: Simplification for DMVR padding process [Y.-C. Lin, C.-C. Chen, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0902](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7524) Crosscheck of JVET-O0297 (CE9-related: Simplification for DMVR padding process) [H. Huang (Qualcomm)]

[JVET-O0304](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6909) Non-CE9: Simplification of BDOF [Y. Kato, T. Toma, K. Abe (Panasonic)]

[JVET-O0861](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7482) Crosscheck of JVET-O0304 (Non-CE9: Simplification of BDOF) [S. Sethuraman (Ittiam)]

[JVET-O0307](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6912) Non-CE9/3: Unification of division processing [K. Abe, Y. Kato, T. Toma (Panasonic)]

[JVET-O0385](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6990) Non-CE9: An applying condition considering temporal correlation for DMVR [K. Unno, K. Kawamura, S. Naito (KDDI)]

[JVET-O0659](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7276) Crosscheck of JVET-O0385 on DMVR applying condition [S. Iwamura (NHK)]

[JVET-O0503](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7110) Non-CE9: Modifications to MMVD for CUs eligible for DMVR [S. Sethuraman (Ittiam)]

[JVET-O0810](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7431) Crosscheck of JVET-O0503 (Non-CE9: Modifications to MMVD for CUs eligible for DMVR test3) [E. Sasaki (Sharp)]

[JVET-O0504](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7111) Non-CE9: Header flags to disable DMVR and BDOF at finer granularities [S. Sethuraman (Ittiam)]

[JVET-O0505](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7112) Non-CE9: Simplifying BDOF early termination related cost calculations [S. Sethuraman (Ittiam)]

[JVET-O0871](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7492) Crosscheck of JVET-O0506 (Non-CE9: An improvement to extended predicted sample value derivation in BDOF) [C.-C. Chen, W.-J. Chien (Qualcomm)]

[JVET-O0506](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7113) Non-CE9: An improvement to extended predicted sample value derivation in BDOF [S. Sethuraman (Ittiam)]

[JVET-O0508](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7115) Non-CE9: Refinement of optical flow estimates in BDOF [S. Sethuraman (Ittiam)]

[JVET-O0782](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7403) Crosscheck of JVET-O0508: Non-CE9: Refinement of optical flow estimates in BDOF [X. Xiu (Kwai Inc.)]

[JVET-O0570](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7185) Non-CE9: Unified gradient calculations in BDOF [H. Liu, L. Zhang, K. Zhang, Z. Deng, N. Zhang (Bytedance)]

[JVET-O0731](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7352) Crosscheck of JVET-O0570: Non-CE9: Unified gradient calculations in BDOF [[Y. He (InterDigital)](mailto:yuwen.he@interdigital.com)]

[JVET-O0573](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7188) Non-CE9: Restriction on motion vector refinement in BDOF [H. Liu, L. Zhang, K. Zhang, H.-C. Chuang, J. Xu (Bytedance)]

[JVET-O0819](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7440) Crosscheck of JVET-O0573 (Non-CE9: Restriction on motion vector refinement in BDOF) [T. Ikai (Sharp)]

[JVET-O0577](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7192) Non-CE9: Bi-directional optical flow (BDOF) for chroma components [W. Chen, Y. He (InterDigital)]

[JVET-O0857](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7478) Crosscheck of JVET-O0577 (Non-CE9: Bi-directional optical flow (BDOF) for chroma components) [H. Liu (Bytedance)]

[JVET-O0581](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7196) Non-CE9: Block Boundary Prediction Refinement with Optical Flow for DMVR [W. Chen, Y. He (InterDigital)]

[JVET-O0729](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7350) Crosscheck of JVET-O0581: Non-CE9: Block Boundary Prediction Refinement with Optical Flow for DMVR [[X. Xiu (Kwai Inc.)](mailto:xiaoyuxiu@kwai.com)]

[JVET-O0590](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7205) Non-CE9: Improvement of DMVR [Y.-W. Chen, X. Xiu, X. Wang (Kwai Inc.)]

[JVET-O0799](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7420) Crosscheck of JVET-O0590 on Non-CE9: Improvement of DMVR [X. Li (Tencent)]

[JVET-O0609](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7224) CE9-related: On BDOF precision alignment for high internal bit-depth [X. Xiu, Y.-W. Chen, T.-C. Ma, H.-J. Jhu, X. Wang (Kwai Inc.)]

[JVET-O0612](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7227) CE9-related: Complexity reduction for BDOF parameters calculation [K. Panusopone (Nokia)]

[JVET-O0615](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7230) Non-CE9: Derivation of extended prediction samples for BDOF gradient calculation [Y. He, W. Chen (InterDigital)]

[JVET-O0628](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7243) Non-CE9: SATD based DMVR [J. Chen, R.-L. Liao, Y. Ye, J. Luo (Alibaba)]

[JVET-O0703](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7323) Crosscheck of JVET-O0628: Non-CE9: SATD based DMVR [X. Xiu (Kwai Inc.)]

[JVET-O0634](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7249) Non-CE9: BDOF simplifications [X. Chen, H. Yang (Huawei)]

[JVET-O0940](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7566) Crosscheck of JVET-O0634: Non-CE9: BDOF simplifications [X. Xiu (Kwai Inc.)]

[JVET-O0644](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7260) Non-CE9: Evaluation of Threshold Value Applied to SAD-based Early Termination in DMVR [C.-C. Chen, W.-J. Chien, M. Karczewicz (Qualcomm)]

[JVET-O0866](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7487) Crosscheck of JVET-O0644 (Non-CE9: Evaluation of Threshold Value Applied to SAD-based Early Termination in DMVR) [S. Sethuraman (Ittiam)]

[JVET-O0681](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7300) CE9-related: Disabled DMVR, BDOF and BCW for CIIP [H. Huang, W.-J. Chien, M. Karczewicz (Qualcomm)] [late]

## CE10 related – Neural network based loop filtering (2)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0157](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6761) Non-CE10: A CNN based in-loop filter for intra frame [L. Xu, J. Zhu, K. Kazui (Fujitsu)]

[JVET-O0348](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6953) CE10-Related: Classifier Based Adaptive Convolutional Neural Network Loop Filter [H. Yin, R. Yang, X. Fang, S. Ma (Intel)] [miss] [late]

## CE11 related – Gradual random access (0)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

## Quantization parameter signalling (8)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0046](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6650) AHG16: Changing cu\_qp\_delta signalling to facilitate VDPU-level processing [B. Heng, M. Zhou, T. Hellman (Broadcom)]

[JVET-O0838](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7459) Crosscheck of JVET-O0046: AHG16: Changing cu\_qp\_delta signalling to facilitate VDPU-level processing [L. Zhang (Bytedance)]

[JVET-O0186](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6790) AHG15: Signalling of chroma Qp mapping table [S. Ikonin, R. Chernyak, E. Alshina (Huawei)]

[JVET-O0267](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6872) AHG15: Cleanups for scaling matrices [O. Chubach, C.-Y. Lai, C.-Y. Chen, T.-D. Chuang, C.-W. Hsu, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0298](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6903) AHG15: Chroma Quantization Qpc Parameter Signalling [S. Paluri, J. Zhao, J. Lim, S. Kim (LGE)]

[JVET-O0433](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7038) AHG15: chroma quantization parameters Qpc table [F. Pu, T. Lu, P. Yin, S. McCarthy, W. Husak, T. Chen (Dolby)]

[JVET-O0678](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7297) Crosscheck of JVET-O0433 (AHG15: chroma quantization parameters QpC table) [Louis Kerofsky (InterDigital)]

[JVET-O0562](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7176) AHG15: Flexible luma-to-chroma quantization parameter tables [F. Bossen, A. Segall (Sharp)]

[JVET-O0650](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7266) AHG15: On signalling of chroma QP tables [A. K. Ramasubramonian, G. Van der Auwera, D. Rusanovskyy, M. Karczewicz (Qualcomm)]

[JVET-O0737](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7358) Chroma Delta QP for Separate Tree [Y. Han, G. Van Der Auwera, M. Coban, W.-J. Chien, Y.-H. Chao, A. Nalci, M. Karczewicz (Qualcomm)] [late]

## Entropy coding (6)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0308](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6913) CABAC skip mode [K. Abe, T. Toma (Panasonic), D.-Y. Kim (Chips&Media)]

[JVET-O0450](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7056) Separating context coded and bypass coded data [A. Hallapuro, J. Lainema (Nokia)]

[JVET-O0517](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7124) Slice/tile level CABAC zero-word constraints [A. Browne, S. Keating, K. Sharman (Sony)]

[JVET-O0519](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7126) Switching between CABAC context coded bins and EP coded bins [A. Browne, S. Keating, K. Sharman (Sony)]

[JVET-O0520](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7127) GT1 flag coding [A. Browne, S. Keating, K. Sharman (Sony)]

[JVET-O0554](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7168) Removal of infrequently used context models [A. Alshin, J. Boyce, Pavel Frolov, Vasily Aristarkhov (Intel)]

[JVET-O0854](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7475) Crosscheck of JVET-O0554 (Removal of infrequently used context models) [Z. Deng (Bytedance)]

## Chroma sampling and chroma formats (2)

[JVET-O0616](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7231) Non-CE: Various chroma format support in VVC [N. Zhang, L. Zhang, H. Liu, K. Zhang, Y. Wang (Bytedance)]

[JVET-O0622](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7237) AHG2: Draft text to update BT.2100 references and ICTCP HLG coding equations [S. McCarthy, F. Pu, T. Lu, P. Yin, W. Husak, T. Chen (Dolby)]

## Other coding tools (13)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0194](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6799) On chroma-CU-related flags and coding information [Y. Zhao, H. Yang (Huawei)]

[JVET-O0824](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7445) Crosscheck of JVET-O0194 (On chroma-CU-related flags and coding information) [X. Cao (Hikvision)]

[JVET-O0389](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6994) On Supporting 64x64 Chroma Transform Unit [W. Cai, J. Zhu, J. Yao, K. Kazui (Fujitsu)]

[JVET-O0704](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7324) Crosscheck of JVET-O0389: On Supporting 64x64 Chroma Transform Unit [X. Zhao (Tencent)]

[JVET-O0398](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7003) Chroma block size restriction in dual tree intra coding [J. Choi, J. Heo, J. Lim, S. Kim (LGE)]

[JVET-O0447](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7053) Enabling tools for small blocks in small pictures [P. Bordes, F. Le Léannec, T. Poirier, F. Galpin (InterDigital)]

[JVET-O0460](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7067) On lossless coding for VVC [T. Poirier, F. Le Léannec, F. Galpin (InterDigital)]

[JVET-O0945](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7572) Crosscheck of JVET-O0460 (On lossless coding for VVC) [T.-C. Ma (Kwai Inc.)]

[JVET-O0461](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7068) AHG13: On chroma tool evaluation in Dual Tree [T. Poirier, F. Le Léannec, F. Galpin, E. François (InterDigital)] [miss] [late]

[JVET-O0525](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7132) AHG16: Removal of PCM mode [T. Hellman (Broadcom)]

[JVET-O0526](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7133) AHG16: Setting the minimum CTU size to 32x32 [T. Hellman, M. Zhou (Broadcom)]

[JVET-O0537](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7144) Non-CE: Weighted intra and inter prediction mode [L. Zhao, X. Zhao, X. Li, S. Liu (Tencent)]

[JVET-O0778](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7399) Crosscheck of JVET-O0537 (Non-CE: Weighted intra and inter prediction mode) [Y.-W. Chen (Kwai Inc.)]

[JVET-O0548](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7160) Combined bilateral/SAO loop filter [J. Ström, P. Wennersten, J. Enhorn, D. Liu, K. Andersson, R. Sjöberg (Ericsson)]

[JVET-O0549](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7162) AHG10: Encoder-only GOP-based temporal filter [P. Wennersten, R. Sjöberg, J. Enhorn (Ericsson)]

[JVET-O0584](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7199) Comments on transform quantization bypassed mode [H. Jang, J. Nam, N. Park, J. Choi, S. Kim, J. Lim (LGE)]

[JVET-O0591](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7206) Modifications to support the lossless coding [T.-C. Ma, Y.-W. Chen, X. Xiu, X. Wang (Kwai Inc.)]

## 360 degree video (5)

[JVET-O0344](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6949) AHG6-Related: Spherical domain rate control for 360-degree video coding in VVC [N. Yan, L. Li, D. Liu, H. Li, Z. Li, F. Wu (USTC)] [miss] [late]

[JVET-O0419](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7024) AHG6: Wrap-around spatial merge candidate for inter prediction [M. Lee, J. Lee, J. Park, D. Sim, S. Oh (KWU), W. Lim, G. Bang, H. Kim (ETRI)]

[JVET-O0487](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7094) Geometry padding for cube based 360 degree video using uncoded areas [J. Sauer, M. Bläser (RWTH Aachen)]

[JVET-O0488](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7095) Parameterizable cubemap for 360 degree video coding [J. Sauer, M. Bläser (RWTH Aachen)]

[JVET-O0891](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7513) AHG6/AHG17: Cubemap-based projection syntax for 360-degree videos [Y.-H. Lee, J.-L. Lin, Y.-J. Chen, C.-C. Ju (MediaTek)] [late]

## AHG17: General high-level syntax (60)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

### NAL unit header and reference picture indication (5)

[JVET-O0146](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6750) AHG17: On unspecified NAL unit types [Y.-K. Wang (Futurewei)]

[JVET-O0179](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6783) On NAL Unit Header Design [S. Deshpande (Sharp)]

[JVET-O0237](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6842) AHG17: Compact NAL unit header [M. Pettersson, R. Sjöberg, M. Damghanian (Ericsson)]

[JVET-O0359](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6964) AHG17: On identification of reference and non-reference picture [T. Suzuki (Sony)]

[JVET-O0181](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6785) Comments on High-Level Syntax of VVC [S. Deshpande (Sharp)]

Only the proposal 2 in the contribution belongs to this agenda item.

### Parameter set mechanism and parsing dependency (6)

[JVET-O0113](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6717) AHG17: On parsing dependency between parameter sets [Y.-K. Wang (Futurewei), J. Boyce (Intel)]

[JVET-O0155](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6759) AHG17: Resolving PPS parsing dependency on SPS [Hendry, Y.-K. Wang (Futurewei)]

[JVET-O0236](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6841) AHG17: Making PPS parsing independent of SPS [R. Sjöberg, M. Pettersson, M. Damghanian (Ericsson)]

[JVET-O0490](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7097) AHG17: Independent parsing of parameter sets [K. Sühring, R. Skupin, Y. Sanchez, T. Schierl (HHI)]

[JVET-O0181](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6785) Comments on High-Level Syntax of VVC [S. Deshpande (Sharp)]

Only the proposal 1 (on parameter set activation) and proposal 4 (on DPS referencing by SPS) in the contribution belong to this agenda item.

[JVET-O0268](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6873) AHG17: Signalling SPS base and patch [L. Chen, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

### IOP point definition and signalling (2)

[JVET-O0044](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6648) AHG17: Signalling zero or more Sub-Profiles [S. Wenger, B. D. Choi, S. Liu (Tencent)]

[JVET-O0417](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7022) AHG17: On general constraint information [P. Bordes, M. Kerdranvat (InterDigital)]

### Picture types, random access, and sub-layer switching (5)

[JVET-O0147](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6751) AHG17: Constraints on IRAP pictures and leading pictures [Hendry, Y.-K. Wang (Futurewei)]

[JVET-O0226](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6831) AHG17: On constraint about position of leading pictures and trailing pictures associated to an IRAP [V. Drugeon, K. Abe (Panasonic)]

[JVET-O0149](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6753) AHG17: On external decoding refresh (EDR) [Y.-K. Wang, J. Chen (Futurewei), H. Yu, L. Yu (ZJU)]

[JVET-O0393](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6998) AHG17: On cross-RAP referencing [M. M. Hannuksela (Nokia)]

[JVET-O0235](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6840) AHG17: Ensuring temporal switching with STSA pictures [R. Sjöberg, M. Damghanian, M. Pettersson (Ericsson)]

### Reference picture list signalling (2)

[JVET-O0148](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6752) AHG17: On reference picture list signalling [Y.-K. Wang, Hendry (Futurewei)]

[JVET-O0244](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6849) AHG17: On zero delta POC in reference picture structure [V. Seregin, M. Coban, A. K. Ramasubramonian, M. Karczewicz (Qualcomm)]

### Miscellaneous HLS topics (12)

[JVET-O0151](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6755) AHG17: On no\_output\_of\_prior\_pics\_flag [Y.-K. Wang (Futurewei)]

[JVET-O0154](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6758) AHG17: Miscellaneous AHG17 topics [Y.-K. Wang (Futurewei)]

[JVET-O0176](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6780) On Tiles, Bricks and Slices [S. Deshpande (Sharp)]

Only the proposal 5 (also covered in JVET-O0152) in the contribution belongs to this agenda item.

[JVET-O0178](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6782) On DPB Parameters [S. Deshpande (Sharp)]

[JVET-O0201](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6806) AHG17: On Gradual Random Access (GRA) [M. Coban, A. Ramasubramonian, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0234](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6839) AHG17: Slice header extension [R. Sjöberg, M. Damghanian, M. Pettersson (Ericsson)]

[JVET-O0238](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6843) AHG17: Parameters in SPS or slice headers [M. Pettersson, P. Wennersten, R. Sjöberg, M. Damghanian (Ericsson)]

[JVET-O0241](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6846) AHG17: On Decoding Process Steps [S. Deshpande (Sharp)]

[JVET-O0270](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6875) AHG17: Signalling refreshed regions associated with a GRA picture [L. Chen, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0391](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6996) AHG17: Avoiding dependencies on externally controlled variables in the decoding process [M. M. Hannuksela, K. Kammachi-Sreedhar (Nokia)]

[JVET-O0392](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6997) AHG17: Output of SEI messages [M. M. Hannuksela (Nokia)]

[JVET-O0497](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7104) AHG17: On layer grouping for independent regions and ARC [Y. Sanchez, R. Skupin, K. Sühring, T. Schierl (HHI)]

### APS and ALF parameters signalling (8)

[JVET-O0245](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6850) AHG17: On TemporalId and NuhLayerId in APS [V. Seregin, M. Coban, A. K. Ramasubramonian, N. Hu, M. Karczewicz (Qualcomm)]

[JVET-O0246](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6851) AHG17: Separate luma and chroma ALF APS types [V. Seregin, N. Hu, M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0299](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6904) AHG17: APS support for default and user defined scaling matrices [S. Paluri, J. Zhao, J. Lim, S. Kim (LGE)]

[JVET-O0300](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6905) AHG17: Clean-up of ALF Syntax Parameters [S. Paluri, J. Lim, S. Kim (LGE)]

[JVET-O0301](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6906) AHG17: Simplification of ALF Clipping Coefficients in the APS [S. Paluri, J. Lim, S. Kim (LGE)]

[JVET-O0302](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6907) AHG17: Simplification of ALF Coefficients in the APS [S. Paluri, J. Lim, S. Kim (LGE)]

[JVET-O0893](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7515) Crosscheck of JVET-O0302 (Non-CE5: Simplification of ALF Coefficients in the APS) [N. Hu (Qualcomm)]

[JVET-O0491](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7098) AHG17: High-level syntax cleanup [K. Sühring, R. Skupin, Y. Sanchez, T. Schierl (HHI)]

[JVET-O0561](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7175) AHG17: APS ID Control Mechanism [S. Paluri, J. Lim, S. Kim (LGE)]

### Virtual boundary signaling (3)

[JVET-O0339](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6944) AHG17: On virtual bounary signaling [B. Choi, S. Wenger, S. Liu (Tencent)]

[JVET-O0340](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6945) AHG17: On modifiaction of virtual boundaries signaling [G. Bang, W. Lim, H.Y. Kim (ETRI)]

[JVET-O0445](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7050) [AHG17] Signaling of uniform virtual boundaries [A. DSouza, C. Pujara, R. Gadde, W. Choi, K. P. Choi (Samsung)]

### HRD (11)

[JVET-O0043](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6647) AHG17: Clarification on role of pic\_struct to HRD and the output picture process [C. Fogg (MovieLabs)]

[JVET-O0152](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6756) AHG17: HRD support for GRA and changing the term GRA to GDR [Y.-K. Wang (Futurewei)]

[JVET-O0153](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6757) AHG17: HRD clean-ups [Y.-K. Wang (Futurewei)]

[JVET-O0177](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6781) On HRD Signalling [S. Deshpande (Sharp)]

[JVET-O0180](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6784) On Buffering Period [S. Deshpande (Sharp)]

[JVET-O0189](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6793) AHG17: Support of decoding unit in HRD [K. Kazui (Fujitsu)]

[JVET-O0227](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6832) AHG17: Parsing dependency of picture timing SEI message on SPS [V. Drugeon (Panasonic)]

[JVET-O0228](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6833) AHG17: Harmonized HRD parameters signaling [V. Drugeon (Panasonic)]

[JVET-O0495](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7102) AHG17: On HRD for Open GOP and DRAP [Y. Sanchez, R. Skupin, K. Sühring, T. Schierl (HHI)]

[JVET-O0496](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7103) AHG17: On HRD for splicing [Y. Sanchez, R. Skupin, K. Sühring, T. Schierl (HHI)

[JVET-O0667](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7284) DPB size analysis of VTM5.0 using RA configuration in current CTC [X. Ma, H. Yang (Huawei)]

### VUI and SEI (6)

[JVET-O0041](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6645) AHG17: On signalling of frame field information [Y.-K. Wang (Futurewei), K. Sühring (HHI), C. Fogg (MovieLabs)]

[JVET-O0042](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6646) AHG17: Replicating pic\_struct into separate elements for future extensibility [C. Fogg (MovieLabs)]

[JVET-O0435](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7040) AHG17: on pic\_struct [S. McCarthy, F. Pu, T. Lu, P. Yin, W. Husak, T. Chen (Dolby)]

[JVET-O0360](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6965) AHG17: On sRGB and sYCC [T. Suzuki (Sony)]

[JVET-O0622](http://phenix.int-evry.fr/jvet/doc_end_user/current_document.php?id=7237) AHG2/AHG17: Draft text to update BT.2100 references and ICTCP HLG coding equations [S. McCarthy, F. Pu, T. Lu, P. Yin, W. Husak, T. Chen]

[JVET-O0492](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7099) AHG17: On merging of bitstreams and conformance [R. Skupin, Y. Sanchez, K. Sühring, T. Schierl (HHI)]

## AHG12: high-level parallelism and coded picture regions (41)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0800](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7421) A summary of HLS proposals on coded picture regions [Y.-K. Wang (Futurewei)] [late]

### Sequence-level independent regions (10)

[JVET-O0138](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6742) AHG12: Treating region boundaries as picture boundaries [Hendry, S. Hong, J. Chen, Y.-K. Wang (Futurewei)]

[JVET-O0141](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6745) AHG12: Sub-picture based motion-constrained independent regions [Y.-K. Wang, Hendry, J. Chen (Futurewei), R. Skupin, Y. Sanchez, K. Suehring (HHI)]

[JVET-O0176](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6780) On Tiles, Bricks and Slices [S. Deshpande (Sharp)]

Only the proposal 6 in the contribution belongs to this agenda item.

[JVET-O0182](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6786) AHG12: On picture and sub-picture signaling [Y. He, A. Hamza (InterDigital)]

[JVET-O0183](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6787) AHG12: On layer-based sub-picture extraction and reposition [Y. He, A. Hamza (InterDigital)]

[JVET-O0334](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6939) AHG8/AHG12: On sub-picture partitioning support with layers [B. Choi, S. Wenger, S. Liu (Tencent)] [late]

[JVET-O0394](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6999) AHG12: On independently coded picture regions [M. M. Hannuksela (Nokia)]

[JVET-O0493](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7100) AHG12: On independent regions and output sub-picture sets [R. Skupin, Y. Sanchez, K. Sühring, T. Schierl (HHI)]

[JVET-O0555](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7169) AHG12: Sub-pictures and sub-picture sets with level derivation [J. Boyce, L. Xu (Intel)]

[JVET-O0700](http://phenix.int-evry.fr/jvet/doc_end_user/current_document.php?id=7320) AHG12: On sub-picture info SEI message [Y. He, A. Hamza (InterDigital)] [late]

### Mixed NAL unit types within a picture (2)

[JVET-O0140](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6744) AHG12: On mixed NAL unit types within a picture [Y.-K. Wang, Hendry (Futurewei)]

[JVET-O0498](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7105) AHG17: On marking process and unequal NALU types per AU [Y. Sanchez, R. Skupin, K. Sühring, T. Schierl (HHI)]

### Slice, tile, and brick signalling in PPS and slice header (15)

[JVET-O0143](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6747) AHG12: Signalling of addresses for rectangular slices [R. Sjöberg, M. Damghanian, M. Pettersson, J. Ström (Ericsson), Hendry, Y.-K. Wang (Futurewei)]

[JVET-O0156](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6760) AHG12: On brick signaling [W. Lim, G. Bang, H. Y. Kim (ETRI)]

[JVET-O0172](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6776) On rectangular slice address signaling [B.-K. Lee (Xris)]

[JVET-O0173](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6777) On brick information signaling [B.-K. Lee (Xris)]

[JVET-O0176](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6780) On Tiles, Bricks and Slices [S. Deshpande (Sharp)]

Only the proposals 1 to 4 in the contribution belongs to this agenda item.

[JVET-O0181](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6785) Comments on High-Level Syntax of VVC [S. Deshpande (Sharp)]

Only the proposal 3 in the contribution belongs to this agenda item.

[JVET-O0199](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6804) AHG12: On rectangular slices [M. Coban, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0225](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6830) AHG17: On signalling of bottom right brick for rectangular slices [V. Drugeon, T. Nishi (Panasonic)]

[JVET-O0269](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6874) AHG17: Suggested syntax improvement on PPS [L. Chen, C.-Y. Chen, Y.-W. Huang, S.-M. Lei (MediaTek)]

[JVET-O0338](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6943) AHG12: On signaling for slice, tile and brick [B. Choi, S. Wenger, S. Liu (Tencent)]

[JVET-O0403](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7008) AHG17: On brick index signalling [P. Bordes, M. Kerdranvat (InterDigital)]

[JVET-O0452](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7058) AHG12: On tile and brick partition [Y.-J. Chang, M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0453](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7059) AHG12: On rectangular slice signaling [Y.-J. Chang, M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0454](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7060) AHG12: On sub-bitstream extraction [Y.-J. Chang, M. Coban, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0610](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7225) AHG17: Association of VCL NAL units to coded pictures [B. Heng, W. Wan (Broadcom)]

### Signalling of brick/WPP entries (4)

[JVET-O0144](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6748) AHG12: On end\_of\_subset\_one\_bit, end\_of\_brick\_one\_bit, and byte alignment in the slice data syntax [Hendry, Y.-K. Wang (Futurewei)]

[JVET-O0145](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6749) AHG12: Miscellaneous AHG12 topics [Y.-K. Wang, Hendry (Futurewei)]

[JVET-O0215](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6820) AHG12: Clean up on wavefront parallel processing signaling [T. Ikai, E. Sasaki, T. Chujoh (Sharp)]

[JVET-O0306](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6911) AHG17: Entry point offsets without start code emulation [K. Abe, T. Toma, V. Drugeon (Panasonic)]

### New tile/brick/WPP partitioning schemes (5)

[JVET-O0198](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6803) AHG12: On brick partitioning [M. Coban, A. Ramasubramonian, Y.-J. Chan, V. Seregin, M. Karczewicz (Qualcomm)]

[JVET-O0358](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6963) AHG12: On uniform tile spacing [Y. Fujimoto, M. Ikeda, T. Suzuki (Sony)]

[JVET-O0390](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6995) AHG17: On tile and brick signalling [M. M. Hannuksela (Nokia)]

[JVET-O0511](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7118) AHG12: On tiles and bricks partitioning [N. Ouedraogo, E. Nassor (Canon)]

[JVET-O0462](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7069) AHG12: Separated Luma and Chroma wavefront for dual tree [T. Poirier, F. Le Léannec, F. Galpin (InterDigital)]

### Loop filtering across boundaries of independently coded regions (4)

[JVET-O0142](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6746) AHG12: On turning off ALF filtering at brick and slice boundaries [Y.-K. Wang, J. Chen, Hendry (Futurewei)]

Also included in the agenda item 6.5.

[JVET-O0494](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7101) AHG12: On filtering of independently coded region [R. Skupin, Y. Sanchez, K. Sühring, T. Schierl (HHI)]

Also included in the agenda item 6.5.

[JVET-O0654](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7270) AHG12/Non-CE5: Unification of boundary handling for adaptive loop filter [N. Hu, V. Seregin, M. Karczewicz (Qualcomm)]

Also included in the agenda item 6.5.

[JVET-O0858](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7479) Crosscheck of JVET-O0654 (AHG12/Non-CE5: Unification of boundary handling for adaptive loop filter) [H. Liu (Bytedance)]

[JVET-O0662](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7279) Non-CE5: Modified ALF filtering for Slice, Brick and Virtual boundaries [A. M. Kotra, S. Esenlik, B. Wang, H. Gao, E. Alshina (Huawei)]

Also included in the agenda item 6.5.

## AHG8: layered coding and resolution adaptivity (24)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

### Reference picture resampling (RPR) (11)

[JVET-O0133](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6737) AHG8: Support for reference picture resampling - handling of picture size signalling, conformance windows, and DPB management [Hendry, S. Hong, Y.-K. Wang, J. Chen (Futurewei), Y.-C Sun, T.-S Chang, J. Lou (Alibaba)]

[JVET-O0134](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6738) AHG8: Support for reference picture resampling - handling of resampling, TMVP, DMVR, and BDOF [Y.-C Sun, T.-S Chang, J. Lou (Alibaba), Hendry, S. Hong, Y.-K. Wang, J. Chen (Futurewei)]

[JVET-O0757](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7378) Crosscheck of JVET-O0134 (AHG8: Support for reference picture resampling - handling of resampling, TMVP, DMVR, and BDOF) [H.-J. Jhu (Kwai Inc.)]

[JVET-O0184](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6788) AHG8: On ARC indicator [Y. He, Y. He, A. Hamza (InterDigital)]

[JVET-O0204](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6809) AHG8: On Adaptive Resolution Change (ARC) High-Level Syntax (HLS) [J. Samuelsson, S. Deshpande, A. Segall (Sharp)]

[JVET-O0240](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6845) AHG8: Adaptive Resolution Change (ARC) with downsampling [J. Samuelsson, S. Deshpande, A. Segall (Sharp)]

[JVET-O0242](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6847) AHG8: Enabling BDOF and DMVR for reference picture resampling [V. Seregin, M. Coban, M. Karczewicz (Qualcomm)]

[JVET-O0303](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6908) AHG8: Adaptive Resolution Change [P. Chen, T. Hellman, B. Heng, W. Wan, M. Zhou (Broadcom)]

[JVET-O0701](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7321) Crosscheck of JVET-O0303 (AHG8: Adaptive Resolution Change) [Y. He (InterDigital)]

[JVET-O0319](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6924) AHG8: On Adaptive Resolution Change [P. Topiwala, M- Krishnan, W. Dai (FastVDO)]

[JVET-O0332](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6937) AHG8: Signaling and filtering for Reference Picture Resampling (RPR) [B. Choi, S. Wenger, S. Liu (Tencent)]

[JVET-O0395](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7000) AHG8: On adaptive resolution changing and scalable coding [M. M. Hannuksela, A. Aminlou (Nokia)]

Only the RPR part in the contribution belongs to this agenda item.

[JVET-O0641](file:///C:\Users\y00441455\Downloads\current_document.php?id=7257) AHG8: AHG 8: JVET Common test conditions for adaptive resolution change (ARC) [M. G. Sarwer, R. -L Liao, J. Chen, J. Luo, Y. Ye (Alibaba)] [late]

### Scalabitliy (13)

[JVET-O0045](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6649) AHG8: Spatial Scalability using Reference Picture Resampling [S. Wenger, B. D. Choi, S. C. Han, X. Li, S. Liu (Tencent)]

[JVET-O0135](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6739) AHG8: Scalability for VVC – general [Y.-K. Wang, Hendry, J. Chen (Futurewei)]

[JVET-O0136](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6740) AHG8: Scalability for VVC - handling of POC, RPL, and RPM [Y.-K. Wang, Hendry, J. Chen (Futurewei)]

[JVET-O0185](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6789) AHG8/AHG17: On layer dependency signaling [Y. He, A. Hamza (InterDigital)]

[JVET-O0243](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6848) AHG8: On inter-layer reference for scalability support [V. Seregin, M. Coban, A. K. Ramasubramonian, M. Karczewicz (Qualcomm)]

[JVET-O0333](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6938) AHG8: On spatial scalability support with reference picture resampling [B. Choi, S. Wenger, S. Liu (Tencent)]

[JVET-O0335](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6940) AHG8: E-Sport Use cases of VVC region-wise scalability and its system support [B. Choi, S. Zhao, I. Sodagar, S. Wenger, S. Liu (Tencent)]

[JVET-O0336](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6941) AHG8: Region-wise scalability support with reference picture resampling [B. Choi, S. Wenger, S. Liu (Tencent)]

[JVET-O0337](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6942) AHG8/AHG17: Layered Random Access point (LRA) [B. Choi, S. Wenger, S. Liu (Tencent)] [miss] [late]

[JVET-O0395](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7000) AHG8: On adaptive resolution changing and scalable coding [M. M. Hannuksela, A. Aminlou (Nokia)]

Only the scalability part in the contribution belongs to this agenda item.

[JVET-O0436](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7041) AHG8/AHG17: indication of shutter angle for variable frame rate application [S. McCarthy, T. Lu, F. Pu, P. Yin, W. Husak, T. Chen (Dolby)]

[JVET-O0471](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7078) AHG8/AHG12: Layer concepts clarifications and improvements for immersive media use cases [E. Thomas, A. Gabriel (TNO)]

[JVET-O0473](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7080) AHG8: On HRD operations for layers [E. Thomas, A. Gabriel (TNO)] [miss] [late]

# Complexity analysis and reduction (X)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

# Encoder optimization (4)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0256](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6861) Non-CE: Fast encoder with adjusted threshold in dependent quantization [M. Wang, J. Li, L. Zhang, K. Zhang, H. Liu (Bytedance), S. Wang, S. Ma (Peking University)]

[JVET-O0432](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7037) AHG10: LMCS encoder improvement [F. Pu, S. McCarthy, T. Lu, P. Yin, W. Husak, T. Chen (Dolby)]

[JVET-O0658](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7275) Crosscheck of JVET-O0432 on LMCS encoder improvements [S. Iwamura (NHK)]

[JVET-O0675](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7293) Crosscheck of JVET-O0432: LMCS encoder improvement [J. Zhao (LGE)]

[JVET-O0635](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7251) AHG10-Related: Optimized CTU level bit allocation in inter frames [Y. Li, D. Liu, Z. Chen (USTC), L. Li, Z. Li (UMKC)] [miss] [late]

[JVET-O0888](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=7510) AHG10: Adaptive Coding Sub-set for encoder optimization [M. Bhat, D Gommelet, J.-M. Thiesse, D. Nicholson (VITEC)] [late] [miss]

# Metrics and evaluation criteria (1)

Contributions in this category were discussed XXday X July XXXX–XXXX in Track X (chaired by XXX).

[JVET-O0310](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6915) On video quality analysis [P. Topiwala, M. Krishnan, W. Dai (FastVDO)]

# Withdrawn (X)

JVET-O0137 (withdrawn), JVET-O0150 (withdrawn), JVET-O0218 (withdrawn), JVET-O0239 (withdrawn), JVET-O0251 (withdrawn), JVET-O0458 (withdrawn), JVET-O0459 (withdrawn), JVET-O0478 (withdrawn), JVET-O0479 (withdrawn), JVET-O0784 (withdrawn) , JVET-O0920 (withdrawn) , JVET-O0948 (withdrawn)

# Plenary meetings, joint meetings, BoG reports, and summary of actions taken

## Plenary meeting XXday X July XXXX-XXXX

Reports of the tracks were presented as follows:

The general status of track A was presented and discussed.

The general status of track B was then presented and discussed, which particularly included the following aspects:

* ..

## Closing Plenary meeting Friday 12 July

… .

## High-level syntax / systems relation meeting Saturday 6 July 0900-XXXX

This session, held on 23 March at 0900, was co-chaired by GJS and Youngkown Lim.

…

## Joint meeting XXday XX March XXXX-

JVET with … .

## BoGs (X)

## List of actions taken affecting the draft text of VVC, the VTM, and 360Lib

The following is a summary, in the form of a brief list, of the actions taken at the meeting that affect the text of the VVC draft text, VTM or 360Lib description. Both technical and editorial issues are included. This list is provided only as a summary – details of specific actions are noted elsewhere in this report and the list provided here may not be complete and correct. The listing of a document number only indicates that the document is related, not that it was adopted in whole or in part. The description given in the “Tool” column is a best effort for the sake of understanding but may not precisely reflect the functionality of the tool.

Note that various other aspects that were just marked as decisions after some discussion, without any particular proposal behind them, are not listed in the table. [Ed. Really? Shouldn’t everything be listed?]

(add Ben’s table)

# Project planning

## Core experiment planning

…

## Drafting of specification text, encoder algorithm descriptions, and software

The following agreement has been established: the editorial team has the discretion to not integrate recorded adoptions for which the available text is grossly inadequate (and cannot be fixed with a reasonable degree of effort), if such a situation hypothetically arises. In such an event, the text would record the intent expressed by the committee without including a full integration of the available inadequate text.

## Plans for improved efficiency and contribution consideration

The group considered it important to have the full design of proposals documented to enable proper study.

Adoptions need to be based on properly drafted working draft text (on normative elements) and HM encoder algorithm descriptions – relative to the existing drafts. Proposal contributions should also provide a software implementation (or at least such software should be made available for study and testing by other participants at the meeting, and software must be made available to cross-checkers in EEs).

Suggestions for future meetings included the following generally-supported principles:

* No review of normative contributions without draft specification text
* VTM algorithm description text is strongly encouraged for non-normative contributions
* Early upload deadline to enable substantial study prior to the meeting
* Using a clock timer to ensure efficient proposal presentations (5 min) and discussions

The document upload deadline for the next meeting was planned to be XXday XX June 2019.

As general guidance, it was suggested to avoid usage of company names in document titles, software modules etc., and not to describe a technology by using a company name.

## General issues for experiments

It was emphasized during the opening plenary on January 9 that those rules which had been set up or refined during the 12th meeting should be observed. In particular, for some CEs, results were available late, and some changes in the experimental setup (particularly in CE4) were not discussed on the JVET reflector.

Group coordinated experiments have been planned as follows:

* “Core experiments” (CEs) are the coordinated experiments on coding tools which are deemed to be interesting but require more investigation and could potentially become part of the draft standard by the next meeting.
* A CE is a test of a specific fully described technology in a specific agreed way. It is not a forum for thinking of new ideas (like an AHG). The CE coordinators are responsible for making sure tha the CE description is complete and correct and has adequate detail. Reflector discussions about CE description clarity and other aspects of CE plans are encouraged.
* A description of each experiment is to be approved at the meeting at which the experiment plan is established. This should include the issues that were raised by other experts when the tool was presented, e.g., interference with other tools, contribution of different elements that are part of a package, etc. The experiment description document should provide the names of individual people, not just company names.
* Software for tools investigated in a CE will be provided in one or more separate branches of the software repository. Each CE will have a “fork” of the software, and within the CE there may be multiple branches established by the CE coordinator. The software coordinator will help coordinate the creation of these forks and branches and their naming. All JVET members will have read access to the CE software branches (using shared read-only credentials; the method for members to obtain the credentials is TBA on the reflector).
* During the experiment, revisions of the experiment plans can be made, but not substantial changes to the proposed technology.
* The CE description must match the CE testing that is done. The CE description needs to be revised if there has been some change of plans.
* The CE summary report must describe any changes that were made in the process of finalizing the CE.
* By the next meeting it is expected that at least one independent cross-checker will report a detailed analysis of each proposed feature that has been tested and confirm that the implementation is correct. Commentary on the potential benefits and disadvantages of the proposed technology in cross-checking reports is highly encouraged. Having multiple cross-checking reports is also highly encouraged (especially if the cross-checking involves more than confirmation of correct test results). The reports of cross-checking activities may (and generally should) be integrated into the CE report rather than submitted as separate documents.

It is possible to define sub-experiments within particular CEs, for example designated as CEX.a, CEX.b, etc., where X is the basic CE number.

As a general rule, it was agreed that each CE should be run under the same testing conditions using one software codebase, which should be based on the group test model software codebase. An experiment is not to be established as a CE unless there is access given to the participants in (any part of) the CE to the software used to perform the experiments.

The general agreed common conditions for single-layer coding efficiency experiments are described in the output document JVET-M1010.

Experiment descriptions should be written in a way such that it is understood as a JVET output document (written from an objective “third party perspective”, not a proponent perspective – e.g. not referring to methods as “improved”, “optimized”, etc.). The experiment descriptions should generally not express opinions or suggest conclusions – rather, they should just describe what technology will be tested, how it will be tested, who will participate, etc. Responsibilities for contributions to CE work should identify individuals in addition to company names.

CE descriptions contain a basic description of the technology under test, but should not contain excessively verbose descriptions of a technology (at least not unless the technology is not adequately documented elsewhere). Instead, the CE descriptions should refer to the relevant proposal contributions for any necessary further detail. However, the complete detail of what technology will be tested must be available – either in the CE description itself or in documents that are referenced in the CE description that are also available in the JVET document archive.

Any technology must have at least one cross-check partner to establish a CE – a single proponent is not enough. It is highly desirable have more than just one proponent and one cross-checker.

Some agreements relating to CE activities were established as follows:

* Only qualified JVET members can participate in a CE.
* Participation in a CE is possible without a commitment of submitting an input document to the next meeting. Participation is requested by contacting the CE coordinator.
* All software, results, and documents produced in the CE should be announced and made available to JVET in a timely manner.
* All substantial communications about a CE, other than logistics arrangements, exchange of data, minor refinement of the test plans, and preparation of documents shall be conducted on the main JVET reflector. In the case that large amounts of data are to be distributed is recommended to send an announcement to the JVET reflector without attaching the materials, and send the materials to those who have requested it directly, or provide a link to it, or upload the data as an input contribution to the next meeting.

General timeline for CEs

T1= 3 weeks after the JVET meeting: To revise the CE description and refine questions to be answered. Questions should be discussed and agreed on JVET reflector. Any changes of planned tests after this time need to be announced and discussed on the JVET reflector. Initially assigned description numbers shall not be changed later. If a test is skipped, it is to merked as “withdrawn”.

T2 = Test model software release + 2 weeks or X XX, whichever is earlier: Integration of all tools into a separate CE branch of the VTM is completed and announced to JVET reflector.

* Initial study by cross-checkers can begin.
* Proponents may continue to modify the software in this branch until T3
* 3rd parties are encouraged to study and make contributions to the next meeting with proposed changes

T3: 3 weeks before the next JVET meeting or T2 + 1 week, whichever is later: Any changes to the CE test branches of the software must be frozen, so the cross-checkers can know exactly what they are cross-checking. A software version tag should be created at this time. The name of the cross-checkers and list of specific tests for each tool under study in the CE plan description shall be documented in an updated CE description by this time.

T4: Regular document deadline – 1 week: CE contribution documents including specification text and complete test results shall be uploaded to the JVET document repository (particularly for proposals targeting to be promoted to the draft standard at the next meeting).

The CE summary reports shall be available by the regular deadline. This shall include documentation about crosscheck of software, matching of CE description and confirmation of the appropriateness of the text change, as well as sufficient crosscheck results to create evidence about correctness (crosscheckers must send this information to the CE coordinator at least 3 days ahead of the document deadline). Furthermore, any deviations from the timelines above shall be documented. The numbers used in the summary report shall not be changed relative to the description document.

CE reports may contain additional information about tests of straightforwared combinations of the identified technologies. Such supplemental testing needs to be clearly identified in the report if it was not part of the CE plan.

New branches may be created which combine two or more tools included in the CE document or the VTM (as applicable).

It is not necessary to formally name cross-checkers in the initial version of the CE description document. To adopt a proposed feature at the next meeting, we would like see comprehensive cross-checking done, with analysis that the description matches the software, and recommendation of value of the tool given tradeoffs.

The establishment of a CE does not indicate that a proposed technology is mature for adoption or that the testing conducted in the CE is fully adequate for assessing the merits of the technology, and a favourable outcome of CE does not indicate a need for adoption of the technology.

Availability of spec text is important to have a detailed understanding of the technology and also to judge what its impact on the complexity of the spec will be. There must also be sufficient time to study it in detail. CE contributions without sufficiently mature draft spec text in the CE input document should not be considered for adoption.

Plans for the CEs to be conducted were established Thursday 18 January (GJS); CE plan documents were reviewed Friday 19 January (GJS & JRO).

Lists of participants in CE documents should be pruned to include only the active participants. Read access to software will be available to all members.

## Software development and anchor generation

The planned timeline for software releases was established as follows:

* VTM5.0 will be released by 2019-04-29 including all adoptions necessary for CTC (likely not yet including APS). VTM5.1 with non-CTC adoptions will be released later. Further versions of VTM may be released for additional bug fixing, as appropriate.
* Preparation of the VTM software will include immediate removal of macros that were added in the previous meeting cycle. The software coordinator has the discretion to retain some such macros.
* Timeline of 360lib9.1 (related to finalization of inclusions from last meeting): 2019-04-29.

# Establishment of ad hoc groups

The ad hoc groups established to progress work on particular subject areas until the next meeting are described in the table below. The discussion list for all of these ad hoc groups was agreed to be the main JVET reflector ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de)).

|  |  |  |
| --- | --- | --- |
| **Title and Email Reflector** | **Chairs** | **Mtg** |
| **Project Management (AHG1)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Coordinate overall JVET interim efforts. * Supervise CE and AHG studies. * Report on project status to JVET reflector. * Provide a report to next meeting on project coordination status. | J.-R. Ohm, G. J. Sullivan (co-chairs) | N |
| **Draft text and test model algorithm description editing (AHG2)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Produce and finalize JVET-N1001 VVC text specification draft 4. * Produce and finalize JVET-N1002 VVC Test Model 5 (VTM 5) Algorithm and Encoder Description. * Gather and address comments for refinement of these documents. * Coordinate with test model software development AhG to address issues relating to mismatches between software and text. | B. Bross, J. Chen (co-chairs), J. Boyce, S. Kim, S. Liu, Y. Ye (vice-chairs) | N |
| **Test model software development (AHG3)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Coordinate development of test model (VTM) software and associated configuration files. * Produce documentation of software usage for distribution with the software. * Discuss and make recommendations on the software development process. * Propose improvements to the guideline document for developments of the test model software. * Perform tests of VTM 5 behaviour relative to HEVC and VTM 4 using the VTM common test conditions and the multi-resolution streaming test conditions described in JVET-N0446. * Coordinate with AHG on Draft text and test model algorithm description editing (AHG2) to identify any mismatches between software and text, and make further updates and cleanups to the software as appropriate. * Coordinate with AHG6 for integration with 360lib software. | F. Bossen, X. Li, A. Norkin, K. Sühring (co-chairs) | N |
| **Test material and visual assessment (AHG4)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Maintain the video sequence test material database for development of the VVC standard. * Identify and recommend appropriate test materials for use in the development of the VVC standard. * Identify missing types of video material, solicit contributions, collect, and make available a variety of video sequence test material. * Evaluate new test sequences, particularly including the material recently submitted by the Blender Foundation / Blender Animation Studio and Twitch. * Propose a new structure for the test sequence repository. * Prepare availability of viewing equipment and facilities arrangements for the next meeting, and prepare testing upon consultation with CE coordinators. * Coordinate with AHG11 on test material for screen content coding | T. Suzuki, M. Wien (co-chairs), V. Baroncini, R. Chernyak, P. Hanhart, A. Norkin, J. Ye (vice-chairs) | N |
| **Memory bandwidth consumption of coding tools (AHG5)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Develop improved software tools for measuring both average and worst case of memory bandwidth, and provide information for usage of these tools. * Study cache configurations for measuring decoder memory bandwidth consumption. * Identify coding tools in CEs and VTM with significant memory bandwidth impact. * Study the impact of memory bandwidth on specific application cases. | R. Hashimoto (chair), T. Ikai, X. Li, D. Luo, H. Yang, M. Zhou (vice-chairs) | N |
| **360° video coding tools, software and test conditions (AHG6)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Study the effect on compression and subjective quality of different projections formats, resolutions, and packing layouts. * Discuss refinements of common test conditions, test sequences, and evaluation criteria. * Solicit additional test sequences, and evaluate suitability of test sequences on head-mounted displays and normal 2D displays. * Study coding tools dedicated to 360° video, their impact on compression, and implications to the core codec design. * Study the effect of viewport resolution, field of view, and viewport speed/direction on visual comfort. * Study complexity of GPU rendering of projection formats * Study syntax for signalling of projection formats, cubeface layouts, spherical rotations * Prepare and deliver the 360Lib-9.1 software version and common test condition configuration files according to JVET-M1012. * Generate CTC (PHEC) anchors and PERP results for VTM according to JVET-M1012, and finalize the reporting template for the common test conditions. * Produce documentation of software usage for distribution with the software. | J. Boyce and Y. He (co-chairs), K. Choi, P. Hanhart, J.-L. Lin (vice-chairs) | N |
| **Coding of HDR/WCG material (AHG7)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Study and evaluate available HDR/WCG test content. * Study objective metrics for quality assessment of HDR/WCG material, including investigation of the correlation between subjective and objective results. * Compare the performance of the VTM and HM for HDR/WCG content. * Prepare for expert viewing of HDR content at the 15th JVET meeting if feasible. * Coordinate implementation of HDR anchor aspects in the test model software with AHG3. * Study additional aspects of coding HDR/WCG content. | A. Segall (chair), E. François, W. Husak, D. Rusanovskyy (vice-chairs) | N |
| **Layered coding and resolution adaptivity (AHG8)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Study adaptive-resolution coding approaches for real-time communication, adaptive streaming, and 360-degree viewport-dependent streaming, including filters for resampling, reference picture management, and related scope and signalling * Study approaches for temporal scalability to avoid temporal judder when temporal scalability sub-bitstream extraction is used for achieving lower frame rate, and consider whether this should have a normative impact. * Identify related test conditions for an upcoming core experiment, test sequences, and evaluation techniques (including subjective assessment techniques), including a base configuration to test other ARC modalities against. * Develop a software framework which allows testing various modalities of ARC in the context of the VTM software * Study potential approaches for support of layered coding scalability including spatial, temporal, quality, and view scalability | S. Wenger and A. Segall (co-chairs), M. M. Hannuksela, Hendry, S. McCarthy, Y.-C. Sun, P. Topwala, M. Zhou (vice-chairs) | N |
| **Neural networks in video coding (AHG9)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Investigate the benefit of using neural networks in video compression such as CNN loop filter, intra prediction, re-sampling in adaptive resolution coding, and encoder side partition mode decisions. * Investigate the complexity impact of using neural networks in video compression. * Investigate the complexity measurement of neural network coding tools. * Investigate benefit of universal versus selectable networks, both in terms of compression benefit and complexity. * Investigate how CNN parameters can be established for operation of the decoding process. * Investigate the impact of training materials on the performance of neural network coding tools. * Investigate the impact of the training process on performance and complexity. | S. Liu (chair), B. Choi, K. Kawamura, Y. Li, L. Wang, P. Wu, H. Yang (vice-chairs) | N |
| **Encoding algorithm optimization (AHG10)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Study the impact of using techniques such as GOP structures and perceptually optimized adaptive quantization for encoder optimization. * Study the impact of adaptive quantization on individual tools in the test model. * Study the quantization adaptation tool in the test model. * Investigate the feasibility of adding a CTC test category in which adaptive quantization is turned on. * Study quality metrics for measuring subjective quality using e.g. the CfP response MOS scores. * Investigate other methods of improving objective and/or subjective quality, including adaptive coding structures, adaptive quantization without signalling, and multi-pass encoding. * Study methods of rate control and their impact on performance, subjective and objective quality. | A. Duenas, A. Tourapis (co-chairs), C. Helmrich, S. Ikonin, A. Norkin, R. Sjöberg (vice-chairs) | N |
| **Screen content coding (AHG11)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Investigate coding tools targeted at screen content in terms of compression benefit and implementation complexity. * Identify test materials, discuss testing conditions for screen content coding, and propose associated updated common test conditions. * Study the impact of loop filters on screen content coding. | S. Liu (chair), J. Boyce, A. Filippov, Y.-C. Sun, J. Xu, H. Yang (vice-chairs) | N |
| **High-level parallelism and coded picture regions (AHG12)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Study wavefront processing including the relationship with tiles and low delay characteristics. * Study flexible loop filter control and tile size restriction, including identifying implications on coding tools and implementation. * Study flexible tile partitioning (e.g. more flexible than HEVC and tile boundaries not spanning a full picture). * Study support of independently coded picture regions, including easy rewriting of such regions into a conforming sub-bitstream. * Prepare software and configurations for the test model to facilitate parallel processing tests. * Study the coding efficiency impact of parallel processing and coded picture regions. | S. Deshpande (chair), M. M. Hannuksela, R. Sjöberg, R. Skupin, W. Wan, Y.-K. Wang S. Wenger (vice-chairs) | N |
| **Tool reporting procedure (AHG13)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Prepare output document JVET-N1005, which describes the methodology of tool-off testing and a list of tools to be tested by identified testers. * Provide configurations files, bitstreams, and results of tool-on/tool-off testing. * Use the tool usage counts and memory bandwidth usage to study the decoder complexity of features in on/off testing. * Prepare a report with results of the tests. | W.-J. Chien, J. Boyce (co-chairs), Y.-W. Chen, R. Chernyak, K. Choi, R. Hashimoto, Y.**-**W. Huang, H. Jang, R.-L. Liao, S. Liu, D. Luo (vice-chairs) | N |
| **Operation modes for low latency support (AHG14)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Define relevant test conditions for the study of low latency modes * Study and propose low-latency performance assessment criteria/metrics * Update the implementation in the VTM model for supporting GRA as in JVET-N0865. * Study a parallel framework for GRA assessment | J.-M. Thiesse (chair), S. Deshpande, A. Duenas, Hendry, K. Kazui, R. Sjöberg, A. Tourapis (vice-chairs) | N |
| **Quantization control (AHG15)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Identify methods for quantization step size control for luma and chroma, including spatially and frequency-adaptive approaches * Develop methods for evaluating quantization step size control operation * Study the impact of MTS transforms on quantization matrices and the need for default matrices * Study the interaction between LMCS and quantization step size control * Develop testing conditions for evaluating QP signalling improvements including rate control and perceptual optimization strategies as appropriate   Evaluate the performance of the current VVC QP design using the two adaptive quantization control techniques currently available in the VTM | R. Chernyak (chair), E. François, C. Helmrich, S. McCarthy, A. Segall (vice-chairs) | N |
| **Implementation studies (AHG16)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Study draft and proposed coding tools to identify implementation issues relating to decoder pipelines, decoder throughput, and other aspects of implementation difficulty. * Solicit hardware analysis of complex tools. * Provide feedback on potential solutions to address identified issues. | M. Zhou (chair), J. An, E. Chai, K. Choi, S. Sethuraman, T. Hsieh, X. Xiu (vice-chairs) | N |
| **High-level syntax (AHG17)**  ([jvet@lists.rwth-aachen.de](mailto:jvet@lists.rwth-aachen.de))   * Study NAL unit header, sequence parameter set, picture parameter set, adaptation parameter set, and tile group header syntax designs * Study the proposed picture header designs and alternatives * Study reference picture buffering and list construction * Study random access signalling and random access approaches, including approaches with reference pictures provided by external means * Assist in software development and text drafting for the high-level syntax in the VVC design. * Study syntax alternatives for interoperability point signalling * Study selection of constraint flags to be included in the VTM and their impact on syntax, semantics, and decoding process | R. Sjöberg, J. Boyce (co-chairs), S. Deshpande, M. M. Hannuksela, R. Skupin, A. Tourapis, Y.-K. Wang, W. Wan, S. Wenger (vice-chairs) | N |

# Output documents

The following documents were agreed to be produced or endorsed as outputs of the meeting. Names recorded below indicate the editors responsible for the document production. Where applicable, dates of planned finalization and corresponding parent-body document numbers are also noted.

It was reminded that in cases where the JVET document is also made available as MPEG output document, a separate version under the MPEG document header should be generated. This version should be sent to GJS and JRO for upload.

JVET-N1000 Meeting Report of the 14th JVET Meeting [G. J. Sullivan, J.-R. Ohm] (2019-07-02, near next meeting)

Initial versions of the meeting notes (d0 … d9) were made available on a daily basis during the meeting.

JVET-N1001 Versatile Video Coding (Draft 5) [B. Bross, J. Chen, S. Liu] [WG 11 N 18370] (2019-06-15)

(Initial version planned to be made available by 2019-04-18.)

See the list of elements under section 11.8, as agreed by the Wed. 27 March plenary.

JVET-N1002 Algorithm description for Versatile Video Coding and Test Model 5 (VTM 5) [J. Chen, Y. Ye, S. Kim] [WG 11 N 18371] (2019-06-15)

(Initial version planned to be made available by 2019-04-18.)

See the list of elements under section 11.8, as agreed by the Wed. 27 March plenary.

[JVET-N1003](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6638) Guidelines for VVC reference software development [K. Sühring] (2019-04-01)

Remains valid – not updated: [JVET-M1004](http://phenix.int-evry.fr/jvet/doc_end_user/current_document.php?id=5757) Algorithm descriptions of projection format conversion and video quality metrics in 360Lib (Version 9) [Y. Ye, J. Boyce] (2019-02-15)

JVET-N1005 Methodology and reporting template for coding tool testing [W.-J. Chien and J. Boyce] (2019-04-12)

Remains valid – not updated: [JVET-M1006](http://phenix.int-evry.fr/jvet/doc_end_user/current_document.php?id=5758) Methodology and reporting template for neural network coding tool testing [Y. Li, S. Liu, K. Kawamura] (2019-02-01)

This output was produced to capture aspects specific to enable study of neural network techniques.

JVET-N1010 JVET common test conditions and software reference configurations for SDR video [F. Bossen, J. Boyce, X. Li, V. Seregin, K. Sühring] (2019-04-12)

Decided in closing plenary: Keep class F unchanged in CTC. AHG shall further work on defining better test sets for SC. For the current CE, classes F and TGM to be used for 4:2:0 (unchanged), investigations on testing 4:2:0 vs. 4:4:4 to be done with a set TGM + AOV + TBD . A more reasonable set of sequences needs to be defined by next meeting, otherwise CE8 cannot be continued.

JVET-N1011 JVET common test conditions and evaluation procedures for HDR/WCG video [A. Segall, E. François, W. Husak, S. Iwamura, D. Rusanovskyy] (2019-04-05)

See notes for N0828 regarding action items.

Remains valid – not updated: [JVET-L1012](http://phenix.int-evry.fr/jvet/doc_end_user/current_document.php?id=4840) JVET common test conditions and evaluation procedures for 360° video [P. Hanhart, J. Boyce, K. Choi, J.-L. Lin] (2018-10-26)

[JVET-N1021](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6635) Description of Core Experiment 1 (CE 1): CABAC initialization [F. Bossen, J. Dong, H. Kirchhoffer]

New – previous CE1 was closed, and aspects on LIC subsumed under CE4

[JVET-N1022](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6637) Description of Core Experiment 2 (CE2): Luma/chroma dependency reduction [J. Chen, E. François, P. Yin]

New – previous CE2 was subsumed under CE4

[JVET-N1023](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6630) Description of Core Experiment 3 (CE3): Intra prediction and mode coding [G. Van der Auwera, L. Li, A. Filippov]

Continue

It is emphasized that CE3-1 shall concentrate on simplifying the implementation of 1xN and and 2xN blocks, and not making the design more complicated for the benefit of compression. If there are elements in a proposal that try to compensate a loss due to simplification by adding other elements, it shall be possible to judge the benefit or disadvantage separately. This remark would equivalently apply to other CEs that target simplification of design.

[JVET-N1024](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6633) Description of Core Experiment 4 (CE4): Inter prediction [H. Yang, C.-C. Chen]

CE4-2.x should also conduct subjective tests, as it is claimed to have subjective benefit.

It is noted that for CE4-2.1, test case a would be preferable, as it is best aligned with the new affine mode design of using 4x4 block sizes for uni and bi pred, and same 6-tap filters.

A detailed complexity analysis of CE4-2 and CE4-3 is mandatory.

CE4-3 only two tests

[JVET-N1025](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6636) Description of Core Experiment 5 (CE5): Loop filtering [C.-Y. Chen, A. Norkin]

Includes ALF, and deblocking

[JVET-N1026](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6628) Description of Core Experiment 6 (CE6): Transforms and transform signalling [X. Zhao, H. E. Egilmez]

Joint chroma coding shall also test HDR

[JVET-N1027](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6626) Description of Core Experiment 7 (CE 7): Quantization and coefficient coding [H. Schwarz, M. Coban, C. Auyeung]

includes the coding part of TS residual which was previously in CE8.

[JVET-N1028](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6629) Description of Core Experiment 8 (CE8): Screen Content Coding Tools [X. Xu, Y.-H. Chao, Y.-C. Sun, J. Xu]

Coordination between CE7 and CE8 is desired for TS coefficient coding evaluation, in particular regarding the planned 4:4:4 tests.

[JVET-N1029](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6627) Description of Core Experiment 9 (CE9): Decoder Motion Vector Derivation [S. Esenlik, X. Xiu]

[JVET-N1030](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6631) Description of Core Experiment 10 (CE10): Neural-network based loop filtering [Y. Li, S. Liu, K. Kawamura]

Previous CE10 was closed, and previous CE13 re-numbered

[JVET-N1031](http://phenix.it-sudparis.eu/jvet/doc_end_user/current_document.php?id=6634) Description of Core Experiment 11 (CE11): Gradual random access [K. Kazui, J.-M. Thiesse, Hendry, K. Kawamura]

New - previous CE11 was subsumed under CE5

Previous CE12 (Tile set boundary MC handling) is discontinued as per discussion in HLS topics.

It is emphasized that those CEs which plan subjective testing to study JVET-N0835 which points to some issues of selecting the test conditions for that.

# Future meeting plans, expressions of thanks, and closing of the meeting

Future meeting plans were established according to the following guidelines:

* Meeting under ITU-T SG 16 auspices when it meets (starting meetings on the Tuesday of the first week and closing it on the Wednesday of the second week of the SG 16 meeting – a total of 9 meeting days), and
* Otherwise meeting under ISO/IEC JTC 1/SC 29/WG 11 auspices when it meets (starting meetings on the Wednesday prior to such meetings and closing it at lunchtime on the last day of the WG 11 meeting – a total of 9.5 meeting days).

In cases where an exceptionally high workload is expected for a meeting, an earlier starting date may be defined.

Some specific future meeting plans (to be confirmed) were established as follows:

* Tue. 1 – Wed. 9 October 2019, 16th meeting under ITU-T auspices in Geneva, CH.
* Wed. 8 – Fri. 17 January 2020, 17th meeting under WG 11 auspices in Brussels, BE.
* Wed. 15 – Fri. 24 April 2020, 18th meeting under WG 11 auspices in Alpbach, AT.
* Tue. 23 June – Wed. 1 July 2020, 19th meeting under ITU-T auspices in Geneva, CH.

The agreed document deadline for the 15th JVET meeting was planned to be Tuesday XX September 2019. CE proposal documents are due one week ahead of that date. Plans for scheduling of agenda items within that meeting remained TBA.

XXX was thanked for the excellent hosting of the 15th meeting of the JVET.

EBU, HHI, ITU-T and Sharp Labs of America were thanked for providing equipment used for subjective viewing during the 14th JVET meeting. Philippe Hanhart, Andrew Segall and Mathias Wien were thanked for preparing and conducting the subjective test efforts related to CE1, CE5 and CE11, and Vittorio Baroncini was thanked for his advice. Roger Miles was thanked for support in providing the displays from EBU; Kenneth Andersson, Johannes Sauer and Vadim Seregin were thanked for carefully transporting these. The experts who participated in the role as test subjects were also thanked.

The 15th JVET meeting was closed at approximately XXXX hours on Friday 12 July 2019.

# Annex A to JVET report: List of documents

# Annex B to JVET report: List of meeting participants

The participants of the fifteenth meeting of the JVET, according to an attendance sheet circulated during the meeting sessions (approximately XXX people in total), were as follows: