

J. MAU

**DESCRIPTION OF THE VADIS-A3
COMMON SCHEME**

MPEG2 PROPOSAL n°9

Coding scheme proposed by the algorithm group 3 of the
european VADIS project and supported by:

Proposers:

CCETT

EPFL

HHI

LER

SIEMENS

UCL

F

CH

D

F

D

B

Simulations and papers done by CCETT.

Source: J.Mau / CCETT-FRANCE

Introduction

This paper describes briefly the common scheme proposed by the Algorithm Group 3 of the european project VADIS (referenced as proposal n°9) and gives in annexes the useful information (VLC tables, statistics ...).

The block diagrams of the encoder and decoder are shown next page.

This 4.2.2 coding scheme is based on a two dimensionnal PRMF frequency splitting in 8*8 subbands.

PRMF means Perfect Reconstruction Modulated Filters, a newly developped subband technique by CCETT, and which will be presented in the early 1992 at the ICASSP conference in San-Francisco.

This PRMF subband scheme combines the advantages of subband techniques and can be implemented with a fast algorithm leading to a very efficient hardware implementation, with a total number of multiplications and additions comparable to the best 8*8 DCT solutions. (#A =7.5 and #M=2.5 per pixel for a 2D 8*8 splitting for PRMF to be compared with B.G. LEE algorithm for a 2D 8*8 DCT: #A=7.25 and #M=3).

These PRMF permit also local field/frame decisions which seems not to be possible in any other type of subband techniques.

Nevertheless a lot of particular points are still open to discussion and certainly need some more experiments. Many feedbacks are expected from, for example, hardware implementation people, but the general framework of this codec is now quite stable and presented in this paper.

A simple hardware cost evaluation is also given in this document.

All the numbers will be related to luminance signals (except when specified). Chrominance signals are treated rather similarly except that some horizontal dimensions are to be divided by 2 due to the horizontal sub-sampling. This has to be known to estimate the hardware cost of a complete Y/DB/DR codec. At first approximation except for boxes ME and SM the cost for DB+DR is the same as for Y

On paths will be indicated the numbers of bits transferred per clock cycle except when followed by the '/' signifying that this number of bits is transferred at lower frequency (example 12/256 is equivalent to 12 bits each 256 cycles).

#A and #M mean respectively number of additions and multiplications per cycle (per pixel), #I and #O mean number of input bits and output bits per cycle. #MEM means size of the memory in bits.

LEGEND

Separable two dimensional 8*8 subbands decomposition

Separable two dimensional 8*8 subbands reconstruction

Selection of coding modes and execution

Receives the coding modes and executes the inverse processes

Quantization of the 64 subbands

Inverse Quantization of the 64 subbands

Frame memory

Motion compensation process in the image domain

Motion estimation process in the image domain

Variable length encoder

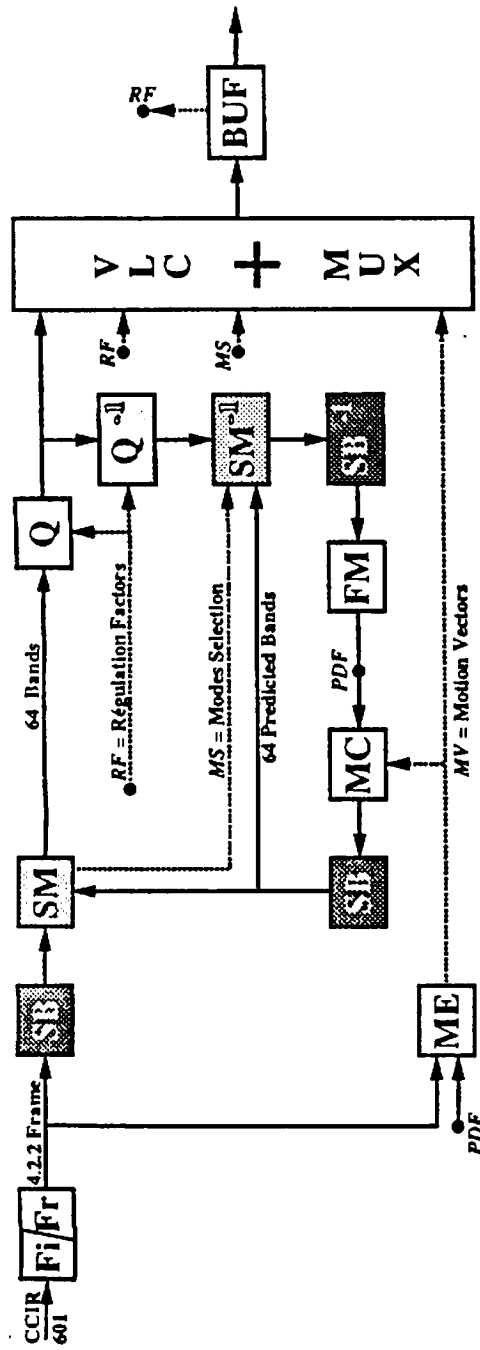
Variable length decoder

Buffer needed for bit-rate regulation

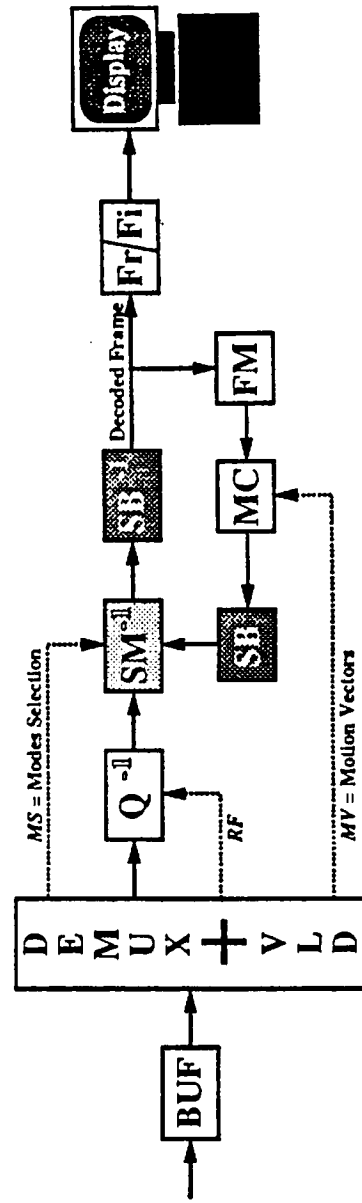
Fields to frame and lines to blocks conversion

Frame to fields and blocks to lines conversion

Previously decoded Frame



SUBBAND ENCODER

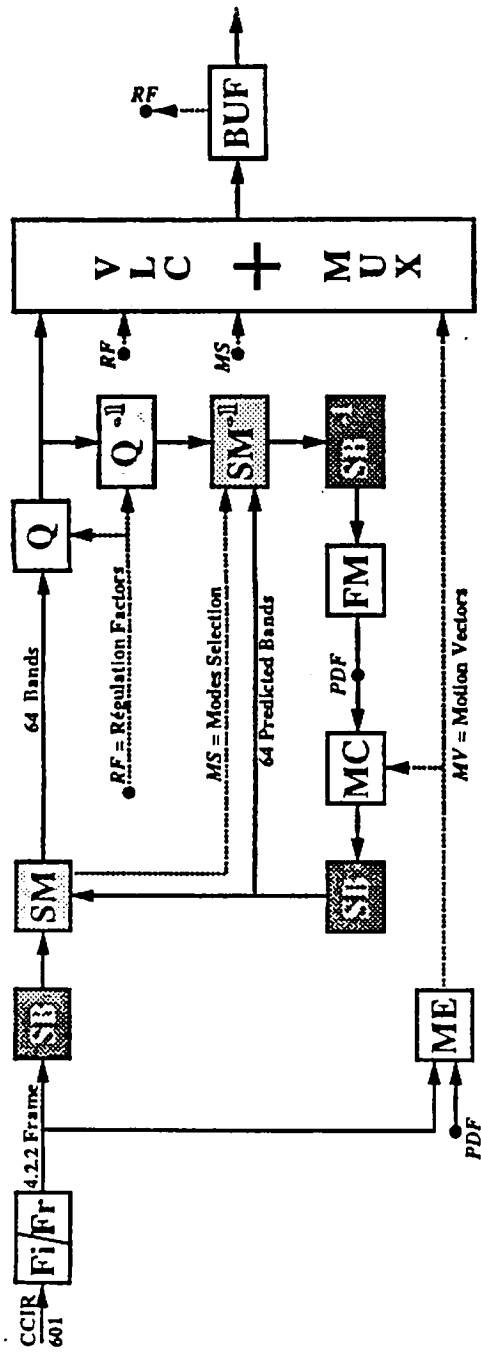


SUBBAND DECODER

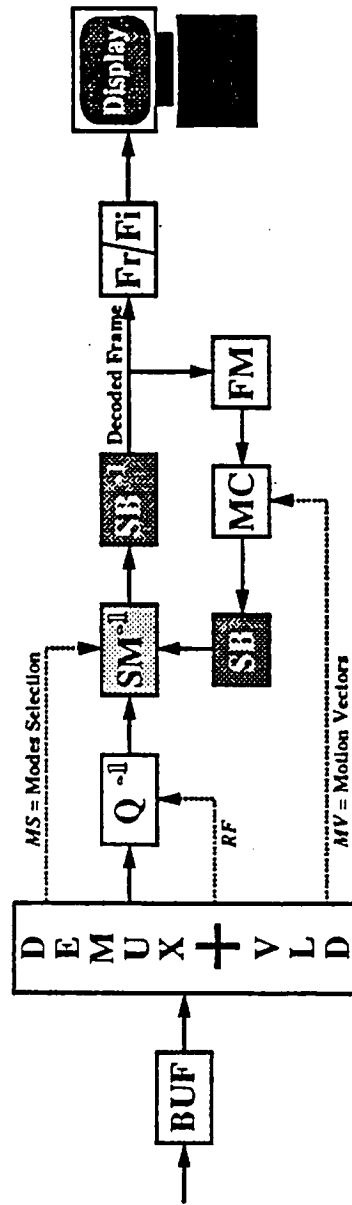
PDF

LEGEND

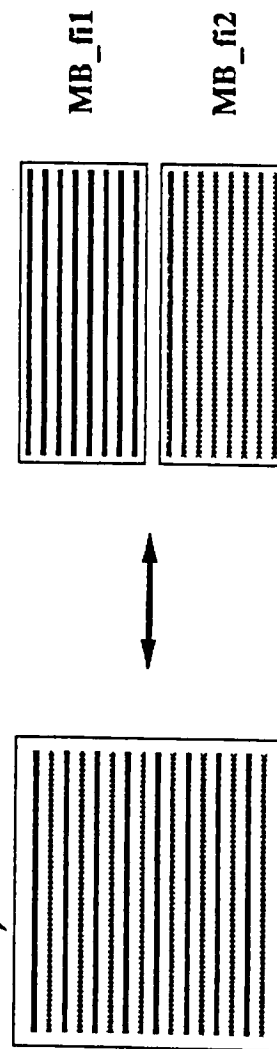
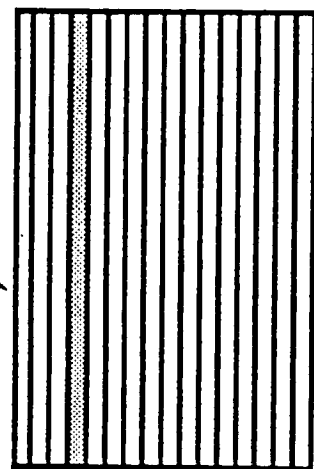
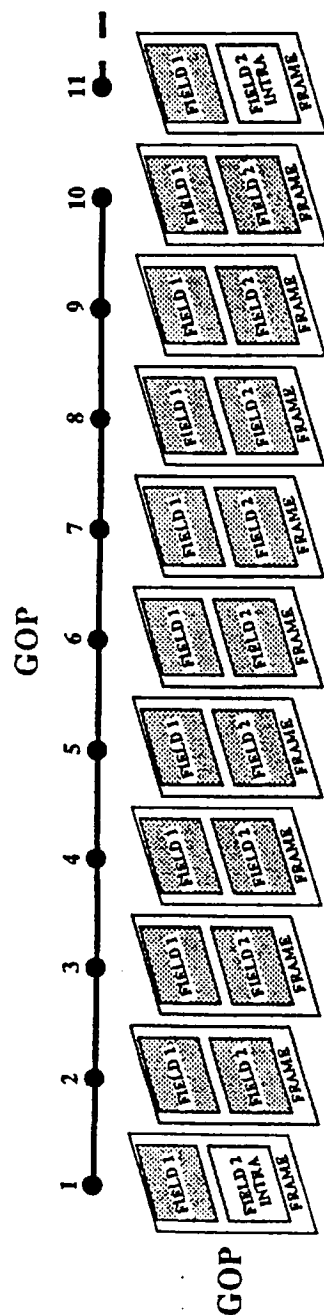
- SB** Separable two dimensionnal 8*8 subbands decomposition
- SB⁻¹** Separable two dimensionnal 8*8 subbands reconstruction
- SM** Selection of coding modes and execution
- SM⁻¹** Receives the coding modes and executes the inverse processes
- Q** Quantization of the 64 subbands
- Q⁻¹** Inverse Quantization of the 64 subbands
- FM** Frame memory
- MC** Motion compensation process in the image domain
- ME** Motion estimation process in the image domain
- VLC** Variable length encoder
- VLD** Variable length decoder
- BUF** Buffer needed for bit-rate regulation
- Fi/Fr** Fields to frame and lines to blocks conversion
- Fr/Fi** Frame to fields and blocks to lines conversion
- PDF** Previously decoded Frame



SUBBAND ENCODER



SUBBAND DECODER



FRAME

A frame is divided into 36 stripes of 16 lines each. (case of 4:2:2 pictures)

STRIPE

A stripe is a 16 lines part of a frame, and contains 45 macro-blocks.

MACRO_BLOCK

A macro-block (MB_fr) is a 16*16 frame based window, created by the interlacing of two 16*8 fields based macro-blocks (MB_fr1). A MB_fr contains then 16*16 Luminance pixels, and two 16*8 Chrominance pixels. A MB_fr1 contains 16*8 Luminance pixels, and two 8*8 Chrominance pixels.

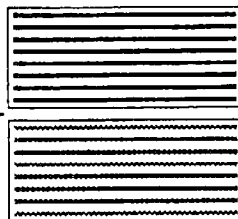
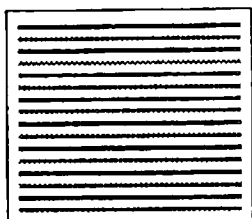
I
M
A
G
E

D
O
M
A
I
N

FRAME BASED LAYERS

FIELD BASED LAYERS

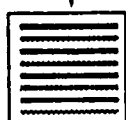
MB_fr
16*16 Y
8*16 DB
8*16 DR
pixels



MB_f1

MB_f2

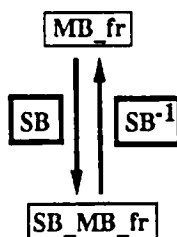
MB_fr
<=>
8 BL_fr
(4 Y + 2DB
+ 2DR)
of 8*8 pixels



a MB_fr

<=>
8 BL_fr
(4 Y + 2DB
+ 2DR)
of 8*8 pixels

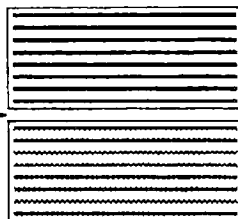
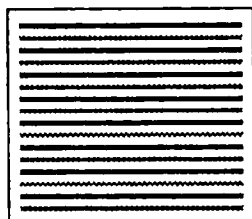
A MB_fr is converted
in a SB_MB_fr
by the subband
transform,
which internally
works on a
BL_fr basis



S
U
B
B
A
N
D

D
O
M
A
I
N

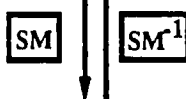
SB_MB_fr
16*16 Y
8*16 DB
8*16 DR
transformed
pixels



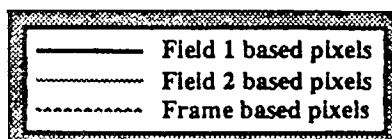
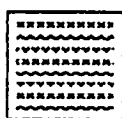
SB_MB_f1

SB_MB_f2

SB_MB
<=>
8 SB BL
(4 Y + 2DB + 2DR)
of 8*8 pixels
Field mode
representation



Modified
SB_BL
Frame mode
representation

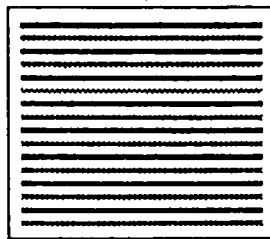


ST



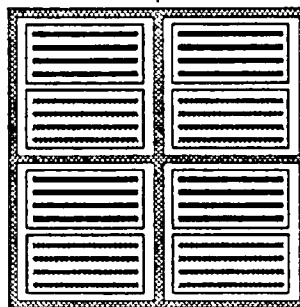
A stripe in the subband domain, as in the image domain, contains 45 SB_MB_fr

SB_MB_fr



(See previous page)

4 * SB_BL
for Y
(2 * SB_BL
for DB
and DR)



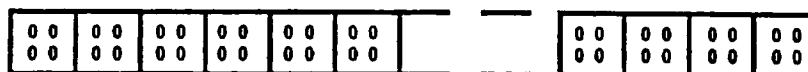
(See previous page)
Here the SB_BL are in a field mode representation but of course they could be in a frame mode representation

SB_BL

0	1	2	3	4	5	6	7
8	9	10	11	12	13	14	15
16	17	18	19	20	21	22	23
24	25	26	27	28	29	30	31
32	33	34	35	36	37	38	39
40	41	42	43	44	45	46	47
48	49	50	51	52	53	54	55
56	57	58	59	60	61	62	63

SB_BL with the subband number associated with each transformed pixel.

SB_ST 0

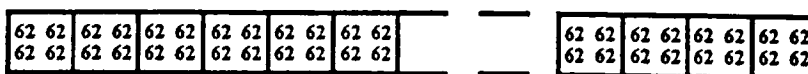


SB_ST 1

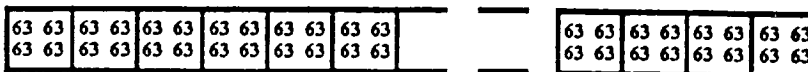


A SB_ST is created by putting together, inside a ST, all the transformed pixels with same subband number, with this scanning inside a SB_MB_fr

SB_ST 62



SB_ST 63



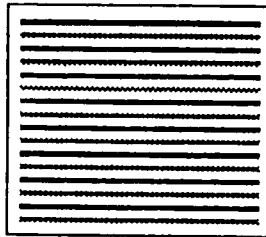
A ST is then converted into 64 SB_ST of size 2*2*45 for Y and 2*45 for DB and DR

ST



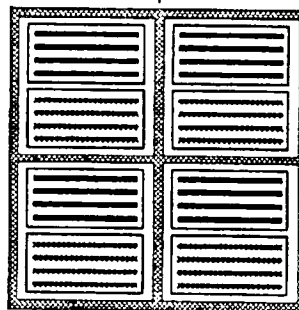
A stripe in the subband domain, as in the image domain, contains 45 SB_MB_fr

SB_MB_fr



(See previous page)

4 * SB_BL
for Y
(2 * SB BL
for DB
and DR)



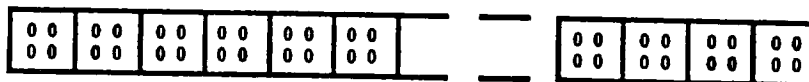
(See previous page)
Here the SB_BL are in a field mode representation but of course they could be in a frame mode representation

SB_BL

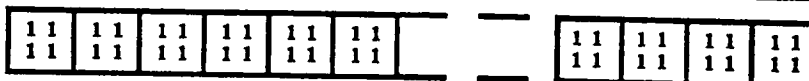
0	1	2	3	4	5	6	7
8	9	10	11	12	13	14	15
16	17	18	19	20	21	22	23
24	25	26	27	28	29	30	31
32	33	34	35	36	37	38	39
40	41	42	43	44	45	46	47
48	49	50	51	52	53	54	54
56	57	58	59	60	61	62	63

SB BL with the subband number associated with each transformed pixel.

SB_ST 0

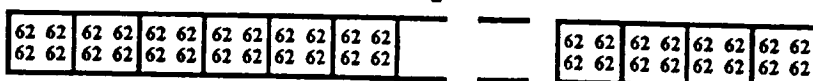


SB_ST 1

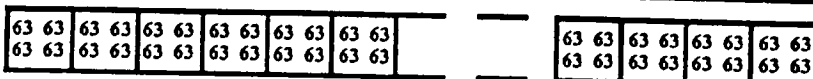


A SB_ST is created by putting together, inside a ST, all the transformed pixels with same subband number, with this scanning inside a SB_MB_fr

SB_ST 62



SB_ST 63



A ST is then converted into 64 SB_ST of size 2*2*45 for Y and 2*45 for DB and DR

Furthermore this fast algorithm needs 3 lines of internal memory, this is in the worst assumptions a $3 \times 720 \times 16 = 34560$ bits memory. (At first approximation these 3 lines are the overcost of this subband chip compared to a DCT chip)

Furthermore some extra memories are needed, like in a DCT chip to perform for example the transposition process ($8 \times 8 \times 16 = 1024$ bits).

The computational cost is the same for $[SB]$ and $[SB^{-1}]$, and in fact as for DCT, the same chip can perform both operations.

The inputs of the subband box is MB_fr based, the output is SB_MB_fr based (internally BL_fr (8×8 pixels) are converted into SB_BL (8×8 transformed pixels))

A frame FR is scanned in horizontal stripes ST of 45 MB_fr . Similarly in the subband domain a stripe ST contains 45 SB_MB_fr .

The chrominance signals are also split in 8×8 subbands by the same filter bank.

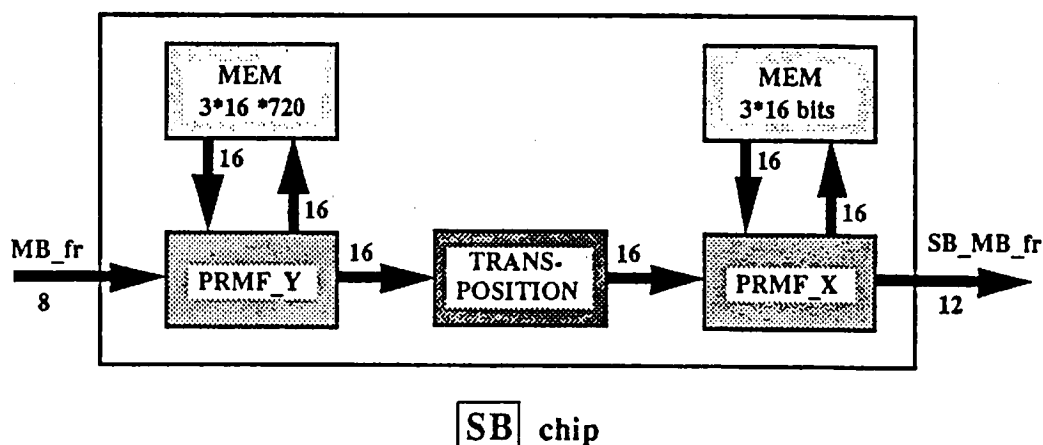
$[SB]$ or $[SB^{-1}]$

#A = 7.5, 16bits wide

#M = 2.5, 16bits wide, fixed coefficients

#MEM = 36,000

#I = 8 and #O = 12 for the bandwidth of the chip for coefficients



Selection of Modes: $[SM]$ and $[SM^{-1}]$

In fact this box has to be split in 2 parts: The decision part, and the action part.
The decision part is just needed at the encoder side and for luminance signal only.

The decision part: (just for the encoder and luminance signal)

Inputs: 2 paths on 12 bits each, one giving an original SB_MB_fr , the other one giving the predicted SB_MB_fr coming from the prediction loop.

For a SB_MB_fr , 4 energies are calculated (energies on a SB_MB_fi base, see "VARIOUS LAYERS"), leading to 2 decisions coded then on 2 bits for a whole SB_M_fr with a total cost of $\#A = 2 \times 2 \times 16 \times 8$, and also $\#M = 2 \times 2 \times 16 \times 8$ if L^2 norm is chosen, no multiplication if L^1 norm is preferred. An internal storage of 2 times 16×16 pixels on 12 bits is also needed $\#MEM = 2 \times 12 \times 16 \times 16 = 6144$ bits.

SM decision part (just at encoder side and for luminance signal)

$\#A = 2$

$\#M = 2$

$\#MEM = 6000$

$\#I = 12 \times 2$ $\#O = 12$ for the bandwidth of coefficients

The action part: needed at encoder and decoder side for both luminance and chrominance signals: same complexity in **SM** and **SM-1**

This part performs the appropriate action on the received SB_MB_fr , knowing the predicted SB_MB_fr and the decision taken in the decision part. According to the decision taken, the predicted SB_MB_fi are subtracted (or added) or not from the original SB_MB_fi , and a field to frame subband representation conversion is performed if needed. (This field/frame commutation is rather close to two 8×4 DCT on BL_fi basis commuted with one 8×8 DCT on a BL_fr basis, except that here the commutation is performed in the subband domain, with a different technique). This local field/frame conversion is possible with a subband PRMF bank, but seems not to be possible with any other subband technique.

NOTE: The SB_MB_fi2 for frame 1 in a GOP are forced to be INTRA coded.

SB MB fi1	SB MB fi2	MODE
INTER	INTER	FRAME
INTRA	INTER	FIELD
INTER	INTRA	FIELD
INTRA	INTRA	FRAME

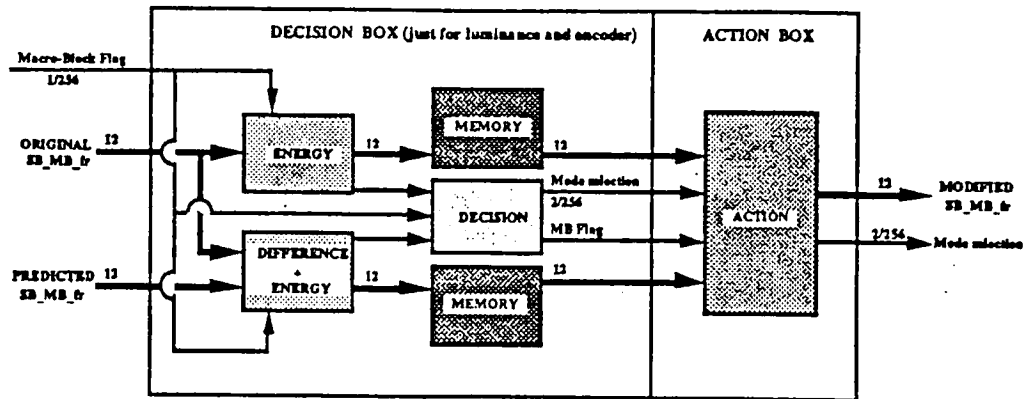
The global cost is $\#A = 2$, no multiplication. No memory is needed if the SB_MB_fr is scanned in a good manner.

SM or **SM-1** decision part

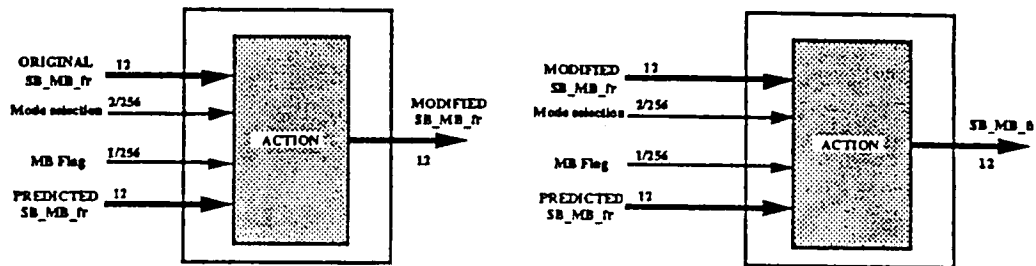
$\#A = 2$

$\#I = 12 \times 2$ for the input coefficients bandwidth

$\#O = 12$



SM for Luminance



SM for Chrominance signals

SM-1

Subband weighting and quantization: **Q** and **Q-1**

Block **Q**

After splitting each subband pixel (transformed coefficients) is multiplied by a weighing factor which depends on its frequency location (its location in a SB_{BL} of 8*8 pixels) and on the buffer regulation factor. This weighing factor is the same along a whole stripe for a subband (pixels with same location in a SB_{BL}). After that each subband is linearly quantized. The quantizer is the same for all subbands.

Without going into details we have: W(sb) = weighing factor for subband sb, F(sb) = frequency location factor of subband sb, RF = regulation factor, sb=0,63

$$\begin{aligned} P(sb) &= \text{Min}[F(sb)-48, RF] + RF \\ P(sb) &= \text{Max}[0, P(sb)] \\ P(sb) &= \text{Min}[175, P(sb)] \\ W(sb) &= 2 * \left[\frac{-P(sb)}{16} \right] \end{aligned}$$

if $P(sb)$ is to be calculated, $W(sb)$ can be calculated by means of binary shift by $P(sb)/16$ bits of $WM(i) = 2^{*}[\frac{i}{16}]$, $i = \text{mod}(P(sb), 16)$. These WM coefficients can be stored permanently and they need (accuracy is to be verified) $16*15 = 240$ bits. Nevertheless $P(sb)$ ($sb=0,63$) have to be calculated once for a whole frame. If RF is the incoming regulation factor, internally a set of up to three different RF factors can be needed:

-For frames 2-10 in a GOP

* RF is used for all SB_MB_fr not coded in (INTRA, INTRA) mode
(see Selection of Modes: SM and $SM-1$)

* $RF-8$ is used for all SB_MB_fr coded in (INTRA, INTRA) mode

-For frame 1 in a GOP

* $RF-8$ is used for all SB_MB_fr coded in (INTRA, INTRA) mode

* Otherwise, RF is used for SB_MB_fi2 , and the SB_MB_fi1 blocks need the RF factor sent for the previous frame (8bits memory)

Each subband has a $F(sb)$ factor (7 bits max) and that for intra and inter modes. Presently the intra mode is used only for field2 of frame 1 (Intra refresh) in a GOP, then for sb numbers from 32 to 63 in a SB_BL of frame 1. For all others fields the inter mode is used.

These tables can be found in Annexe 1. They show a vertical symmetry, because they are presently field based: The upper $8*4$ coefficients represent a $8*4$ subband block field1 based with the DC subband at upper left. The lower $8*4$ coefficients represent a $8*4$ subband block field2 based with the DC subband at lower left. When Frame mode is chosen, SB_BL (containing a $8*4$ field1 and a $8*4$ field2 subband block) are converted into a SB_BL of $8*8$ coefficients frame based. The same $8*8$ tables are taken with DC at upper left. (See Various layers part of this paper).

These tables can be externally loaded and cost $\#MEM = 2*7*8*4 = 448$ bits.

For the Quantization process, if $c(sb)$ is a subband pixel, $q(sb)$ the quantized subband pixel and $Param$ a fixed parameter ($=.2$) the formula is as follow :

$c(sb) = c(sb) * W(sb)$! multiplication by the weighing factor.

If $(c(sb) \geq 0)$ then

$q(sb) = \text{int}(c(sb) + Param)$

else

$q(sb) = \text{int}(c(sb) - Param)$

endif

The multiplication by the weighing factor costs just 1 multiplication per pixel. The quantization process cost is then a bit test, an addition, and a truncation. Total cost ($P(sb)$ counted for 1 add., $W(sb)$ calculation neglected).

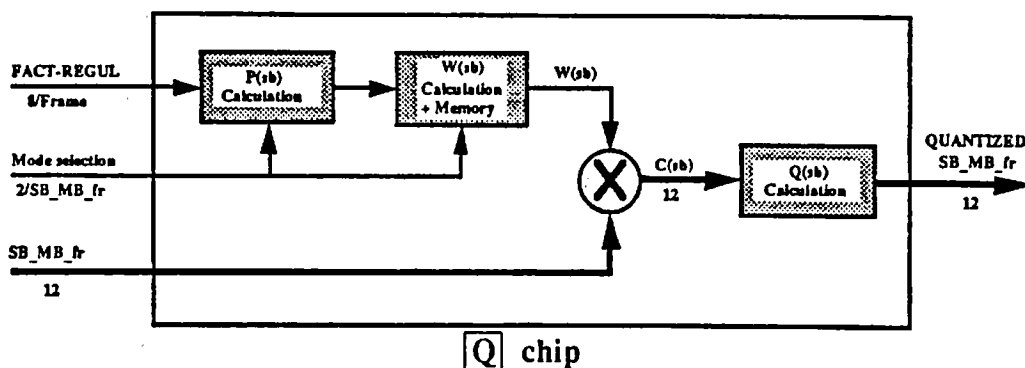
Q chip

#A = 2

#M = 1

#I = #O = 12 bits for coefficients

#MEM = 700



Block **Q-1**

Formula:

$$P(sb) = \text{Min}[F(sb) - 48, RF] + RF \quad ! \text{ see } \mathbf{Q} \text{ cost for } F(sb)$$

$$P(sb) = \text{Max}[0, P(sb)]$$

$$P(sb) = \text{Min}[175, P(sb)] \quad ! \text{ calculated for a whole Stripe}$$

$$W(sb) = 2^{**} \left[\frac{P(sb)}{16} \right] \quad ! \text{ see } \mathbf{Q} \text{ cost for } W(sb)$$

For RF the same remarks as in **Q** are of course valid. (internal modification of RF according to the mode selection of the traied SB_MB_fr)

To reconstruct c(sb):

```

If (q(sb) .EQ. 0) then
    c(sb)=0
else if (q(sb) .GT. 0) then
    c(sb)=nint ( ( q(sb)+pq2(sb) ) * W(sb) )
else
    c(sb)=nint ( ( q(sb)-pq2(sb) ) * W(sb) )
endif
    
```

pq2(sb) are values between 0.0 and 0.5 , defined for each subband nevertheless a fixed value for all subbands should be possible, and will be experimented. Pq2 tables cost 6*8*4=192 bits. (because of the vertical symmetry, and the use of same tables for INTRA and INTER fields). Double cost for DB+DR.

Q⁻¹ chip

#A = 2

#M = 1

#I = #O = 12 bits for coefficients

#MEM = 900

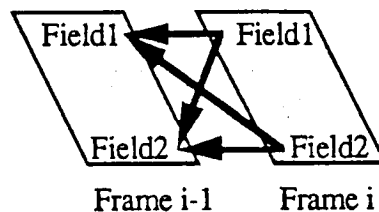
Motion estimation: **ME**

Motion vectors are calculated between original and previously decoded frames.
This box is not needed for Chrominance signals and for decoder.

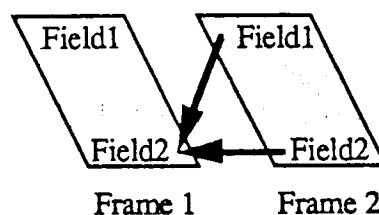
Blocks are **MB_{fi}** based (16*8 field based window). Then two blocks for a **MB_{fr}**.

Search area is -16:+15 horizontally and -8:+7 vertically (in a field).

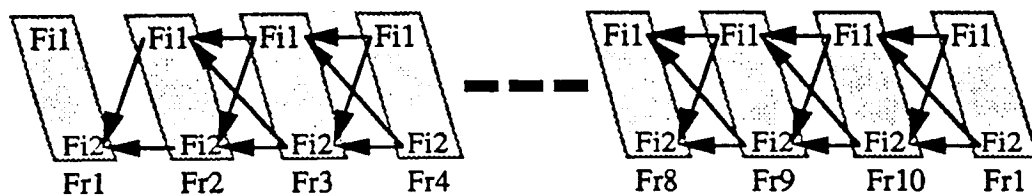
Double prediction strategy: Each **MB_{fi}** can be predicted by using either the field of same parity in the previous frame or the other field.



When field 2 is intra coded (frame 1 in a GOP), the next frame is predicted by using just field2 as possible predictor, avoiding then error propagation.



For a GOP the various searches are then:



(To save power consumption it is possible to inhibit the prediction of field2 for frame 1 in a GOP).

Full search block-matching at pel accuracy and then local half-pel accuracy search, bilinear interpolation.

This double prediction strategy cost the same numbers of operations than one block matching technique with frame based blocks of 16×16 and a search window of $-16:15 \times -16:15$.

An implementation in .8 micron technology with a systolic architecture like in the Thomson chip STI3220 is possible with two identical chips in parallel.

They both receive the reference MB_fr, but one receives the odd lines of the search window (then from field 1, MB_fi1 only) and the other one the even lines (then from field 2, MB_fi2 only).

They both give at the output the minimal distortions and the corresponding vectors for the two MB_fi in the reference MB_fr.

An outside comparison between the distortions has to be done to select finally which prediction is kept for each MB_fi.

For each chip: Number of transistors = 725000

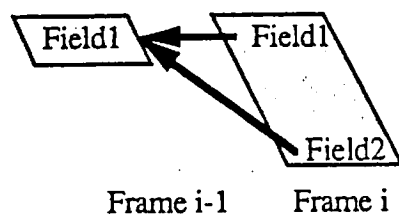
Area = 115 mm²

Package = 80 pins ceramic

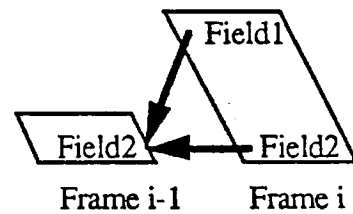
#I = 3 paths on 8 bits, one for the reference block, the 2 others for the search window

Internal Frequency = 27 MHz

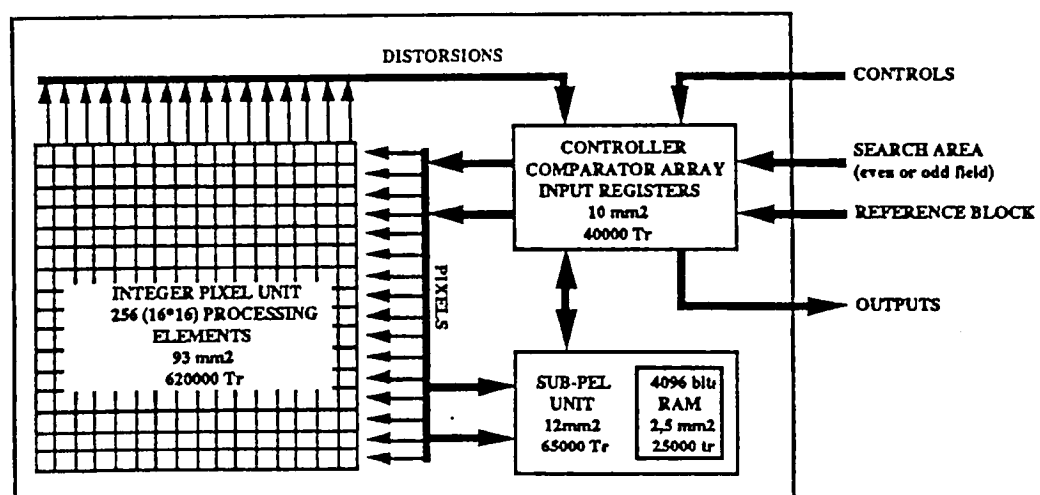
(Values kindly given by Mr R. PACALET from ENST-FRANCE, fax=33.1.45.80.40.36)



First ME chip



Second ME chip



ME chip

Motion compensation: MC

Classical block in a hybrid DCT coding scheme.

Prediction of a 16*8 block in a field with a 16*8 block taken in the previous decoded frame by applying a displacement vector given by the motion estimation process.

Frame memory: FM

The decoded frame after SB⁻¹ is stored in a frame memory, to be afterwards used for MC.

#I = #O = 8 bits for coefficients

#MEM ≈ 3,318,000 (=720*576*8)

Variable length coding and decoding: VLC and VLD

Each stripe in the subband domain (2*2*64*45) is composed in fact of 64 subband stripes SB_ST (a stripe inside a subband) of size 2*2*45 (see Various layers).

To go from a ST representation into a SB_ST representation (or inverse conversion for decoder), a block to line conversion is needed, which cost 2*2*64*45*12bits of memory. At the same time a counter works to calculate in each SB_ST the number of non-zero pixels. #MEM ≈ 138,200

The VLC strategy used here is clearly related to the approach chosen in [1,2] where HDTV/TV compatibility were explored. Nevertheless, as compatibility is not claimed here, this strategy could be simplified and use for example the same VLC blocks oriented (8*8), like in most DCT based codec, and take for example MPEG1 VLC tables... Thereafter will be explained, nevertheless, the VLC and VLD used in our simulations, even if this part could be greatly simplified, especially in term of memory (divided by 180 if VLC block oriented).

VLC block: Just for the encoder

The SB_ST are VLC encoded. One word to specify the number of non-zero pixels (nb_nz) in a SB_ST followed by the relative addresses and the values of non-zero coefficients (nb_nz pairs). Addresses and values are coded using ACVLC techniques (arithmetically computed VLC, a VLC technique created by Thomson LER).

A ACVLC table is fully described by means of 8 words of 4 bits.

example: 3211111B describes a full VLC table.

Each subband has different ACVLC tables for addresses and non-zero coefficients and also for intra and inter and they can be down-loaded to optimize the coding efficiency at different bit-rates. The intra tables are just used for the intra

refresh fields (field 2 frame 1 in a GOP => sb 32 to 63 in a SB BL, then ACVLC tables for sb=0,31 are not useful in that presented scheme, and consequently they are not counted). Cost in memory: #MEM = $32 \times 32 \times 1 + 32 \times 64 \times 1$. #MEM = 3072 bits

ACVLC principle. (extract from CMTT/321-E document)

$l_1 l_2 l_3 l_4 l_5 l_6 l_7 l_8$ describes a VLC in that way:

A VLC word includes k groups of bits, which lengths are $l_1, l_2, \dots, l_k$. All the bits belonging to the first $k-1$ groups are set to one. The last group is the only one that must contain at least one zero.

The arithmetic rules to encode an integer value e are the following ones:

-if $e < S_1 = 2^{l_1} - 1$ then e is coded with l_1 bits
 -if $S_1 \leq e < S_1 + 2^{l_2} - 1$ then the codeword is constituted of two informations:

*the prefix made of l_1 bits equal to '1'

*the suffix, which is the $e - S_1$ value, binary coded with l_2 bits

-and more generally if $S_i \leq e < S_{i+1} = S_i + 2^{l_{i+1}} - 1$

*the prefix is filled with $l_1 + l_2 + l_3 + \dots + l_i$ bits equal to '1'

*the suffix, which is the $e - S_i$ value, binary coded with l_{i+1} bits

ex 3211111B ACVLC

the value 0 is coded '000'

the value 2 is coded '010'

the value 6 is coded '110'

the value 7 is coded '111.00'

the value 9 is coded '111.10'

the value 11 is coded '111.11.1.0'

The maximum VLC word length is 32 bits.

The relative addresses of non-zero coefficients are directly coded that way, the non-zero coefficients are previously modified:

1 -> 0

-1 -> 1

2 -> 2

-2 -> 3 ..., in fact, $i \rightarrow 2*i-1$; $-i \rightarrow 2i$ ($i \geq 1$)

These now positive values are coded by classical ACVLC techniques.

For luminance signals, the Nb_nz values of four consecutive SB ST inside a ST are put together, and bit '0' is transmitted if all the nb_nz_i , $i=0,3$ are equal to zero ($\Leftrightarrow nb = \sum nb_nz_i = 0$), in the other hand bit '1' is transmitted followed by the sequential VLC coding of the 4 nb_nz_i .

Nb_nz_i VLC coding:

$nb_nz_i = 0$ then bit '0' is transmitted

$nb_nz_i \neq 0$ then bit '1' followed by the binary value on 8 bits = 9 bits

For chrominance signals, nb is the sum of eighth nb_nz_i , four consecutive nb_nz_i from DB and four from DR. Bit '0' is transmitted if $nb = 0$, in the

other hand bit '1' is transmitted followed by the sequential VLC coding of the 4 nb_nz_i from DB and the 4 nb_nz_i from DR.

Nb_nz_i VLC coding:

$nb_nz_i = 0$ then bit '0' is transmitted

$nb_nz_i \neq 0$ then bit '1' followed by the binary value on 7 bits = 8 bits

Motion vectors are encoded with a simple VLC. More sophisticated VLC are under study. The MB $fi1$ and MB $fi2$ can be predicted by using either field1 or field 2 of the previously decoded frame. This is the prediction-choice (pc) equal to 0 or 1, 0 if same parity, 1 either. (see Motion Estimation).

Inside a ST, the motion vectors $mv(i)$ ($i=0,44$) relative to MB $fi1$ (resp. MB $fi2$) are VLC encoded that way:

```

if( (pc(i) .eq. pc(i-1)) .and. (mv(i).eq.mv(i-1) ) then
    '0'                                ! Same motion vector than previous block
else if( (pc(i) .eq. pc(i-1)) .and. (|mv(i)-mv(i-1)| .le. .5) then
    '1' + '0' + 3bits                ! Difference with previous vector is (dx,dy)
                                        ! with |dx| ≤ .5 and |dy| ≤ .5
else
    '1' + '1' + 'pc(i)' + 11bits      ! All other cases, pc(i) is just one bit
                                        ! 11 bits to encode mv(i), the scanning is to
                                        ! be decided
endif

```

A simple VLC is also used to encode the two mode decisions for a SB_MB_fr.

SB MB $fi1$	SB MB $fi2$	MODE	VLC 1	VLC 2
INTER	INTER	FRAME	'0'	
INTRA	INTER	FIELD	'110'	
INTER	INTRA	FIELD	'111'	'0'
INTRA	INTRA	FRAME	'10'	'1'

VLC 1 is used for pictures 2-10 in a GOP (see "VARIOUS LAYERS"), VLC 2 is used for picture 1 in a GOP, for the simple reason that Field2 is forced to be in INTRA mode to perform the intra-refresh, with 10 frames periodicity.

The Regulation Factor, at present time, is constant over a whole frame, is FLC encoded and costs 8bits.

VLD block: just for decoder.

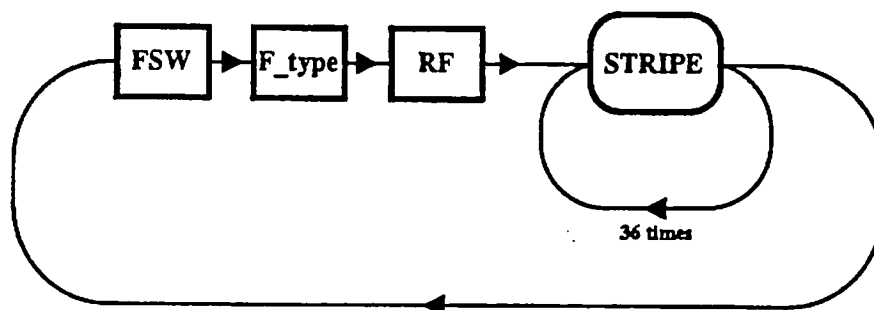
For VLD and the use of the ACVLC $l_1 l_2 l_3 l_4 l_5 l_6 l_7 l_8$:

The first '0' found give the value of k (A VLC word includes k groups of bits, which lengths are $l_1, l_2, \dots, l_k$, all the bits belonging to the first k-1 groups are set to one. The last group is the only one that must contain at least one zero).

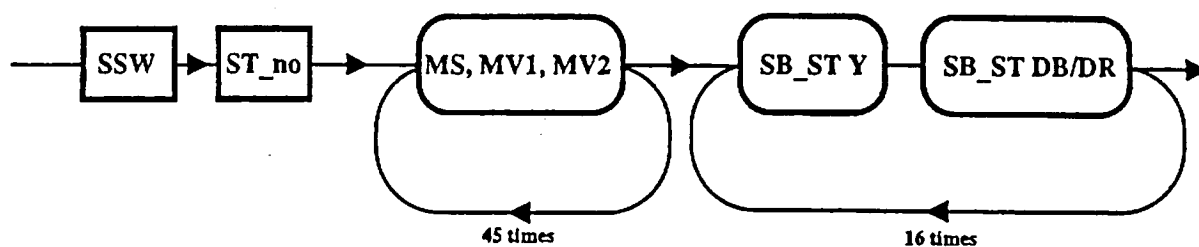
The l_k bits gives the value $e-S_{k-1}$ then e is equal to $S_{k-1} + (e-S_{k-1})$.

MUX box: see next page

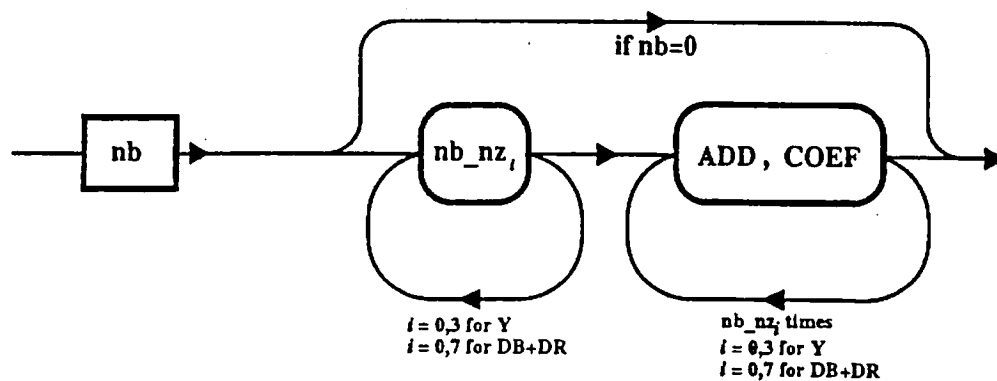
FRAME LAYER



STRIPE LAYER



SB_ST LAYER



Buffer memory capacity: **BUF**

It includes Y+DB+DR

#MEM = 800,000 bits at 4Mbit/s

#MEM = 1,200,000 bits at 9Mbit/s

Buffer regulation:

A regulation strategy is needed to smooth the intra refresh peak bit-rate and to have at the buffer output a constant bit-rate. The goal is obtained by modifying the regulation factor **RF** used in the subband weighing box.

At present time this regulation factor is constant over a whole frame, but should and could be changed at each stripe.

Global size of picture buffers for Y/DB/DR:

Encoder or decoder:

Just one Fi/Fr or Fr/Fi conversion is needed

$(288+16)*720*8 \approx 1,750,000$ bits for Luminance

$(288+16)*360*8*2 \approx 1,750,000$ bits for DB+DR

THEN GLOBALLY Y+DB+DR $\approx$ 3,500,000 bits

Just one Frame Memory is needed

$720*576*8 \approx 3,320,000$ bits for Luminance

$360*576*8*2 \approx 3,320,000$ bits for DB+DR

THEN GLOBALLY Y+DB+DR $\approx$ 6,650,000 bits

Data buffer size

800,000 Bits at 4Mbit/s bit-rate ($\Leftrightarrow$ 200 ms)

1,200,00 Bits at 9Mbit/s bit-rate ($\Leftrightarrow$ 133 ms)

As noted before it is maybe possible at the decoder side to merge into an unique memory the **Fr/Fi** process and the **FM** process, because the video lines needed in **Fr/Fi** are also stored in **FM** (Nevertheless, as I am far from being a hardware specialist, this point is let to the decision of hardware people ...)

HDTV/TV compatibility:

HDTV/TV compatible codec has already been detailed in [1,2] for progressive scannings. Promising results are already obtained for interlaced scannings with the use of a PRMF bank and will be demonstrated, but nevertheless the codec presented here is a non-compatible one.

Coding delay:

The maximum delay between the encoder and decoder is two times one field, plus the data buffering. This delay can be reduced for particular needs by acting on the buffer regulation to artificially reduce the buffer size (ex: an admitted excursion from 0% to 50% for the buffer, reduces the data buffer delay by a factor of 2).

Others:

Fast Forward and Fast Reverse are obtained by picking up in each GOP the intra coded field 2 of frame 1.

-
- [1] J. Mau , "HDTV/TV COMPATIBLE CODEC WITH PQMF FILTER BANK", THIRD HDTV WORKSHOP, TORINO 1991
 [2] J. Mau , "SYSTEME DE CODAGE NUMERIQUE EN SOUS-BANDES MULTISTANDARD ET COMPATIBLE, GRETSI 1991"
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ANNEXES

In annexes you will find the tables for :

ACVLC at 4 and 9 Mbits, for non-zero coefficients and addresses, for inter and intra modes.

F(sb) and PQ2(sb) for intra and inter modes.

SNR of the whole frames in a sequence under the numbers 1, 2,... 10 which are the numbers of the frames in a GOP. Under GOP is the mean SNR for a GOP. SNR are given for Y, DB, DR.

Count of Bits for a frame (Y+DB+DR) in a sequence, and under GOP the total number of bits until the beginning of the sequence.

Several items averaged over a sequence.

Bit-stream files and corresponding bit-rates

Mean SNR for Y, DB, DR over the sequences

ACVLC TABLES FOR NON_ZERO COEFFICIENTS AT 4Mbit/s

COEFF#0	LUMI	INTRA				
6641111B	2455221B	2244321B	2223411B	1111233B	1123111B	1111111B
3643111B	2441111B	1123231B	1122222B	1111221B	1111111B	1111111B
3521111B	2431111B	1122231B	1122112B	1111111B	1111111B	1111111B
2531111B	2331111B	1122211B	1121111B	1111111B	1111111B	1111111B
3444321B	2331321B	2123321B	1122221B	1111211B	1111111B	1111111B
3553111B	3333321B	2233331B	1122331B	1111222B	1111111B	1111111B
4541111B	3443111B	3333221B	2123222B	1121222B	1111111B	1111111B
7421121B	4554111B	3344321B	2333221B	2333221B	1111221B	1111111B
4442111B	2233211B	1123221B	2332111B	1112111B	1111111B	1111111B
1122231B	1111212B	1111111B	1111111B	1111111B	1111111B	1111111B
1111111B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
1111111B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
1111212B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
1121122B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
2233111B	2222222B	1111212B	1111111B	1111111B	1111111B	1111111B
4542111B	3343211B	2233321B	1123212B	1112113B	1111111B	1111111B

COEFF#0	DR	INTRA				
3454111B	2322211B	1123111B	1111111B	1111111B	1111111B	1111111B
2221311B	1121212B	1111111B	1111111B	1111111B	1111111B	1111111B
1111111B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
1111111B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
1111111B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
1111211B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
2232121B	1121222B	1111111B	1111111B	1111111B	1111111B	1111111B
4543211B	2333321B	2223311B	1121232B	1111212B	1111111B	1111111B

COEFF#0	LUMI	INTER				
1123443B	1111235B	1111243B	1111233B	1111222B	1111112B	1111211B
1111234B	1111231B	1111232B	1111232B	1111211B	1111211B	1111111B
1111234B	1111231B	1111232B	1111232B	1111211B	1111111B	1111111B
1121334B	1111212B	1111222B	1111112B	1111111B	1111111B	1111111B
1124422B	1111223B	1111222B	1111113B	1111211B	1111111B	1111111B
1124433B	1123332B	1122132B	1122222B	1111123B	1111111B	1111111B
1124541B	1123433B	1122132B	1123321B	1111223B	1111212B	1111221B
1146431B	1123543B	1122443B	1123334B	1123333B	1111313B	1111231B

COEFF#0	DB	INTER				
1123331B	1111222B	1111112B	1111111B	1111111B	1111111B	1111111B
1111111B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
1111111B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
1111111B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
1111212B	1111111B	1111111B	1111111B	1111111B	1111111B	1111111B
1111332B	1111211B	1111111B	1111111B	1111111B	1111111B	1111111B
1134421B	1111233B	1111223B	1111112B	1111111B	1111111B	1111111B

ACVLC TABLES FOR ADDRESSES OF NON ZERO COEFFICIENTS AT 4Mbit/s

ADDR#0		LUMI		INTRA	
11111118	11112248	11112258	12351118	13541118	15421118
11112248	12355118	12344118	12452218	25511118	31111118
11112248	12344118	12452218	12611118	25111118	42111118
11123448	12451118	12611118	25311118	34111118	11111118
11135538	11135548	12464118	12641118	13641118	11111118
11125528	11123618	11135548	12463218	12653118	45111118
11112550	11113358	11135538	12355118	12364118	36411118
11111118	11125538	1112558	11135548	11135618	12561118
11141118	12541118	12541118	12431118	12431118	31111118
12531118	12511118	12511118	12511118	23311118	11111118
12521118	24211118	24111118	42111118	21111118	11111118
24311118	24111118	21111118	21111118	11111118	11111118
24531118	55111118	54211118	43111118	31111118	11111118
12454118	23542118	12541118	55111118	54311118	21111118
11125518	12454218	12454118	24541118	25511118	54211118
11114218	11255118	12551118	12552218	12551118	15511118
11121118	12531118	12531118	12531118	13431118	24111118
11135218	12511118	12511118	13511118	21111118	12111118
12521118	12511118	34111118	11111118	21111118	11111118
34311118	24111118	32111118	11111118	11111118	11111118
25511118	35411118	52111118	11111118	11111118	11111118
12551118	35511118	35511118	45111118	51111118	21111118
12551118	12551118	13621118	13621118	36111118	42111118
11134218	11145228	12454218	12551118	12551118	15511118
13752118	37511118	37521118	37611118	37521118	37611118
24741118	37521118	47511118	47511118	75111118	66411118
24741118	47511118	75111118	75111118	75111118	54111118
35711118	47511118	75111118	75211118	75211118	65111118
13671118	47511118	75211118	75211118	63111118	51111118
24741118	47511118	75211118	75211118	63111118	51111118
13671118	25731118	37521118	47511118	37611118	37411118
13662811	13662811	13752118	13751118	25731118	15741118
26411118	16421118	16421118	16311118	15531118	16311118
36311118	64111118	64111118	53211118	41111118	42111118
55411118	46111118	51111118	51111118	41111118	31111118
55411118	55111118	51111118	43111118	41111118	32111118
36411118	55111118	42111118	41111118	31111118	31111118
36411118	62111118	51111118	51111118	42111118	31111118
26422118	64311118	64211118	61111118	54211118	41111118
12642118	26431118	12642118	12641118	15541118	31111118
15541118	16411118	16311118	16111118	15411118	21111118
55111118	54111118	52111118	51111118	32111118	21111118
54211118	43111118	31111118	21111118	11111118	11111118
55111118	21111118	31111118	11111118	11111118	21111118
45211118	51111118	42111118	11111118	11111118	11111118
36211118	61111118	51111118	32211118	11111118	11111118
26411118	64111118	61111118	52111118	33111118	42111118
12641118	26431118	12641118	12641118	15531118	15411118

ACVLC TABLES FOR NON ZERO COEFFICIENTS AT 9Mbit/s

[illegible]

MOBILE 5 CALENDER 4MBIT/s											MOBILE 5 CALENDER 9MBIT/s										
GOP											GOP										
1	2	3	4	5	6	7	8	9	10		1	2	3	4	5	6	7	8	9	10	
SNR_Y											SNR_Y										
27.36	32.36	27.74	27.81	27.33	27.11	26.98	26.71	26.61	26.62	26.67	31.31	36.72	32.49	31.66	31.47	30.96	30.80	30.46	30.41	30.48	30.56
27.71	28.45	27.96	28.02	27.77	27.83	27.65	27.56	27.40	27.27	27.28	32.00	32.64	32.24	31.88	32.05	32.02	32.00	31.99	32.02	31.59	31.61
28.36	29.01	28.38	28.29	28.09	28.31	28.31	28.35	28.31	28.42	28.36	32.81	33.50	32.63	32.40	32.39	32.72	32.70	32.72	32.81	33.11	33.28
28.44	29.69	28.40	28.25	27.88	27.96	28.10	28.21	28.44	28.74	29.07	32.89	34.69	32.94	32.36	32.26	32.27	32.28	32.57	32.86	33.43	33.95
28.08	30.20	28.43	28.52	28.50	28.31	28.09	27.79	27.48	27.27	26.98	32.26	35.09	32.94	32.53	32.66	32.57	32.27	31.99	31.49	31.22	30.90
26.98	28.62	27.38	26.99	26.84	26.58	26.51	26.49	26.56	26.44	26.44	30.95	32.83	32.61	31.57	31.01	30.68	30.38	30.12	30.36	30.82	30.56
27.12	28.28	27.69	27.56	27.30	27.21	26.90	26.79	26.62	26.59	26.57	31.23	32.85	31.88	31.35	31.22	31.17	31.12	30.82	30.83	30.82	30.80
27.32	28.41	27.63	27.68	27.58	27.45	27.20	26.98	26.92	26.89	26.70	31.45	32.85	32.10	31.43	31.53	31.42	31.41	31.14	31.13	30.92	30.88
27.78	28.44	27.97	28.14	27.94	27.74	27.53	27.54	27.53	27.57	27.56	32.10	32.85	32.27	31.96	32.09	31.98	31.70	31.91	31.92	32.19	32.22
28.34	29.18	28.26	28.62	28.92	28.84	28.63	28.28	28.01	27.70	27.34	32.80	33.93	32.60	32.75	33.36	33.54	33.40	33.13	32.58	31.97	31.37
27.39	28.07	27.99	27.86	27.64	27.46	27.26	27.12	26.88	26.78	26.49	31.53	33.16	32.87	31.92	31.82	31.50	31.24	31.17	30.91	30.83	30.60
27.62	28.15	27.76	27.71	27.50	27.58	27.53	27.37	27.50	27.55	27.60	31.62	32.55	32.08	31.53	31.43	31.35	31.52	31.28	31.49	31.51	31.57
29.03	27.76	27.97	27.77	27.68							33.38	32.24	31.76	31.76	31.67						
SNR_DB											SNR_DB										
33.05	35.30	34.12	33.67	33.25	32.93	32.75	32.55	32.37	32.28	32.24	35.35	39.36	36.67	35.98	35.56	35.09	34.87	34.61	34.48	34.45	34.47
33.06	33.08	33.74	33.53	33.34	33.26	33.08	32.91	32.73	32.59	32.52	35.75	36.52	36.52	35.97	35.88	35.73	35.54	35.49	35.22	35.18	35.18
33.29	33.35	33.78	33.56	33.39	33.34	33.25	33.16	33.09	33.06	32.99	36.20	36.55	36.57	36.07	36.08	36.13	36.07	36.02	36.09	36.20	36.20
33.25	33.54	33.73	33.55	33.30	33.20	33.09	32.97	32.99	33.05	33.16	36.27	37.24	36.82	36.28	36.16	36.02	35.86	35.91	36.01	36.29	36.50
32.93	33.63	33.70	33.47	33.29	33.08	32.87	32.70	32.47	32.30	32.11	35.80	37.43	36.72	36.24	36.22	36.02	35.77	35.54	35.19	34.93	34.66
32.36	32.96	33.32	32.88	32.62	32.41	32.15	31.97	31.87	31.89	31.79	35.80	37.43	36.72	36.24	36.22	36.02	35.77	35.54	35.19	34.93	34.66
32.39	32.67	33.35	33.05	32.74	32.47	32.29	32.11	31.94	31.84	31.78	34.70	36.02	36.19	35.42	34.95	34.59	34.28	34.04	34.04	34.15	34.15
32.48	32.70	33.28	33.03	32.82	32.64	32.42	32.22	32.10	31.98	31.80	35.00	35.91	36.01	35.54	35.26	35.00	34.60	34.52	34.44	34.42	34.42
32.49	32.69	33.29	33.07	32.80	32.58	32.31	32.18	32.12	32.08	32.00	35.22	35.91	36.03	35.44	35.26	35.15	34.95	34.87	34.68	34.56	34.56
32.80	32.86	33.44	33.24	33.03	32.83	32.61	32.49	32.49	32.30	32.10	35.43	36.00	36.19	35.78	35.66	35.42	35.16	35.12	35.06	35.01	35.01
32.39	32.86	33.36	33.07	32.79	32.56	32.28	32.09	31.91	31.80	31.58	35.92	36.45	36.38	36.21	36.23	36.32	36.19	36.00	35.65	35.22	34.77
32.36	32.44	33.02	32.82	32.57	32.39	32.25	32.11	32.08	32.04	32.00	35.16	36.14	36.38	36.21	36.23	36.32	36.19	36.00	35.65	35.22	34.77
32.74	33.21	32.97	32.73	32.53	32.39	32.25	32.11	32.08	32.04	32.00	35.19	36.03	36.03	35.63	35.53	35.11	35.03	34.81	34.86	34.78	34.81
SNR_DR											SNR_DR										
35.15	37.37	36.51	35.95	35.44	35.14	34.84	34.58	34.37	34.23	34.14	37.45	41.16	39.05	38.33	37.82	37.29	36.96	36.67	36.50	36.41	36.36
35.08	35.02	36.06	35.77	35.49	35.34	35.08	34.87	34.63	34.43	34.42	37.75	37.85	38.51	38.34	38.10	37.90	37.88	37.49	37.38	37.13	37.05
35.30	35.33	36.03	35.79	35.52	35.38	35.13	34.92	34.69	34.49	34.48	38.16	38.41	38.94	38.49	38.20	38.14	37.98	37.89	37.87	37.86	37.86
35.20	35.46	35.91	35.63	35.34	35.13	34.98	34.86	34.66	34.45	34.41	38.21	38.45	38.93	38.45	38.27	38.00	37.80	37.78	37.86	38.05	38.27
34.88	35.54	35.95	35.60	35.32	35.05	34.80	34.55	34.35	34.15	34.11	37.78	39.11	38.98	38.40	38.27	38.00	37.68	37.43	37.16	36.92	36.62
34.31	34.79	35.56	35.01	34.70	34.40	34.12	33.87	33.73	33.74	33.58	36.67	37.82	38.44	37.56	37.00	36.61	36.29	36.03	35.94	35.69	35.96
34.32	34.54	35.67	35.10	34.73	34.45	34.16	33.98	33.81	33.66	33.58	37.01	37.43	38.44	37.77	37.35	37.02	36.87	36.56	36.49	36.35	36.28
34.43	34.60	35.62	35.21	34.90	34.59	34.32	34.07	33.96	33.82	33.62	37.22	37.74	38.48	37.67	37.57	37.32	37.07	36.84	36.73	36.59	36.40
34.38	34.53	35.47	35.09	34.74	34.42	34.13	34.04	33.95	33.89	33.62	37.39	37.84	38.57	37.93	37.70	37.45	37.13	37.00	36.90	36.90	36.82
34.69	34.72	35.69	35.31	35.18	34.89	34.70	34.50	34.30	34.06	33.81	37.87	38.15	38.76	38.33	38.35	38.21	38.07	37.87	37.53	37.32	36.68
34.39	34.70	35.65	35.22	34.83	34.56	34.26	34.06	33.87	33.71	33.48	37.18	37.98	38.76	38.02	37.69	37.30	36.95	36.73	36.55	36.38	36.19
34.38	34.41	35.30	34.95	34.66	34.46	34.28	34.08	34.03	33.95	33.89	37.26	37.57	38.48	37.91	37.51	37.24	37.09	36.83	36.84	36.75	36.74
34.74	35.60	35.20	34.88	34.63							37.96	38.68	37.99	37.69	37.51						
BITS											BITS										
1548962	556934	82594	135744	123087	121404	111427	107608	106010	103452	100702	3452048	922127	352596	278235	320785	279310	264661	258995	247353	240350	
3143581	553372	87493	128451	112037	126663	113047	128155	123085	112063	110053	7039011	1006826	305226	260230	291107	278053	281147	305577	325715	272680	260295
4737890	550809	86918	132044	111631	122567	122787	122292	107173	119492	118596	10844560	1006223	299176	282799	276096	281436	285608	285299	279067	290900	318246
6338501	559381	94701	134840	104695	113124	123447	112981	113564	108966	134912	11422878	1041211	318938	290343	283799	267375	263808	287309	256956	274955	313623
7955865	568779	95754	120308	125585	121264	119399	121292	118021	113901	112431	117890968	1048150	338147	234576	320284	298517	303573	313475	278919	273642	268806
9533998	568781	129154	570127	103871	126451	113393	115565	106977	105571	101181	114371	117890968	1048150	338147	234576	320284	298517	303573	313475	278919	273642
11143774	570127	103871	126451	113393	115565	106977	105571	101181	114371	117890968	1048150	338147	234576	320284	298517	303573	313475	278919	273642	268806	
112745598	570849	107522	116695	116144	120849	120178	112563	115343	115108	106374	125074716	1046993	404657	270160	281996	284969	293272	275254	273569		

FLOWERGARDEN 4Mbit/s										
1	2	3	4	5	6	7	8	9	10	
SNR_Y										
29.37	35.58	29.68	29.77	29.46	28.92	28.75	28.66	28.53	28.41	
29.98	30.43	29.68	29.81	30.10	30.15	30.12	30.03	30.05	29.93	
29.73	31.55	30.13	30.12	30.36	29.59	29.50	29.68	29.43	29.58	
29.24	31.31	29.76	29.90	29.77	29.69	29.39	28.78	28.59	28.62	
29.20	30.25	30.02	29.84	29.53	29.22	29.17	28.80	28.54	28.48	
30.59	30.50	29.86	30.40	30.75	30.80	31.11	30.56	30.32	30.45	
29.93	31.99	30.15	30.54	30.68	30.77	30.47	29.76	28.91	28.52	
29.17	30.53	30.10	30.01	29.88	29.47	28.76	28.74	28.41	28.00	
28.94	30.99	29.13	28.97	28.78	28.55	28.18	28.17	28.04	27.84	
28.61	30.06	29.52	29.43	29.10	28.80	28.29	28.07	27.94	27.77	
29.20	29.86	29.01	28.97	29.00	29.02	29.11	29.60	29.62	29.06	
31.17	29.40	29.27	29.24	29.69						
SNR_DB										
33.70	36.34	34.69	34.16	33.89	33.66	33.34	33.19	33.03	32.93	
33.97	34.85	34.62	34.35	34.30	34.15	34.00	33.84	33.75	33.61	
33.83	34.34	34.75	34.41	34.33	33.88	33.70	33.61	33.47	33.19	
33.75	34.04	34.39	34.00	33.80	33.66	33.72	33.68	33.72	33.43	
33.44	34.14	34.05	34.09	33.96	33.77	33.52	33.14	32.94	32.61	
33.41	33.79	34.61	34.05	33.81	33.48	33.32	33.12	32.88	32.60	
34.24	33.65	34.68	34.59	34.51	34.41	34.00	34.35	34.06	34.03	
33.87	34.47	34.69	34.65	34.53	34.41	34.30	33.55	33.20	32.94	
33.53	33.72	34.69	34.17	34.13	33.85	33.42	33.04	32.78	32.57	
33.25	33.64	34.03	33.81	33.61	33.33	33.09	32.98	32.82	32.60	
33.37	33.67	34.66	34.08	33.76	33.47	33.17	32.95	32.82	32.70	
33.60	33.74	34.11	33.86	33.74	33.51	33.58	33.70	33.53	33.17	
34.09	34.31	33.96	33.82	33.83						
SNR_DR										
34.72	36.75	35.70	35.23	35.04	34.76	34.42	34.21	34.08	33.96	
34.93	34.77	35.61	35.44	35.35	35.11	34.98	34.76	34.64	34.49	
34.97	35.10	35.71	35.26	35.43	35.12	34.92	34.80	34.68	34.45	
34.85	35.16	35.30	35.35	35.35	35.30	34.94	34.79	34.55	34.40	
34.69	34.97	35.63	35.24	35.11	34.89	34.67	34.48	34.20	34.15	
35.23	34.75	35.64	35.65	35.56	35.42	35.28	35.28	35.10	35.00	
34.93	35.26	35.59	35.61	35.48	35.34	34.98	34.63	34.40	34.27	
34.73	34.88	35.60	35.26	35.09	34.68	34.51	34.29	34.13	33.89	
34.58	34.75	35.22	35.13	34.95	34.69	34.52	34.39	34.23	33.96	
34.60	34.81	35.58	35.21	35.03	34.78	34.48	34.30	34.12	33.95	
34.69	34.78	35.26	35.12	34.97	34.73	34.72	34.71	34.49	34.19	
34.86	35.31	35.13	34.91	34.81						
BITS										
1546042	452091	92829	133043	126569	131061	118630	124204	128994	118552	120069
3158897	531555	94627	104568	113045	127298	127910	122285	126766	128414	136387
4732079	530775	98950	113877	134037	127224	100287	123305	136741	120801	87185
6320844	524127	103226	129930	116245	108289	133237	99358	145645	133515	95193
7921404	538939	112306	121783	125141	123278	140765	118754	135928	108310	75356
9536696	524513	89898	137507	129459	112628	139053	117106	122841	129539	112749
111116284	534613	90113	93770	113968	139134	111005	165501	83177	148768	100538
112752462	537150	104394	102802	116335	125979	144725	155162	117903	115047	114682
141364764	550832	90250	127102	127902	130269	127902	127902	130269	118117	114191
155949744	553660	115565	114931	120283	118117	114191	111201	112131	113778	111124
175432324	542838	91082	127220	125735	123991	123487	112941	126079	111675	107943
191314928	549794	109533	117261	110588	116846	100819	117747	146514	118227	104365
527572	111842	121155	92589	143038						

TABLE TENNIS 40bit/s										
SNR_Y										
1	2	3	4	5	6	7	8	9	10	
27.93	30.54	25.97	26.40	27.10	27.28	27.92	26.57	29.54	30.17	31.58
33.92	32.89	32.46	33.00	33.48	34.62	34.18	35.19	34.68	34.89	34.80
34.72	35.58	34.44	34.55	34.68	34.76	34.67	34.74	34.64	34.54	34.61
34.13	35.44	34.08	34.30	34.29	34.08	33.92	33.82	33.77	33.73	33.61
33.55	34.70	33.34	33.55	33.54	33.42	33.33	33.45	33.48	33.52	33.52
31.99	34.57	33.24	33.49	31.06	30.75	30.96	31.37	31.75	32.04	32.22
32.58	33.32	31.07	31.45	31.93	32.54	32.73	33.21	33.27	33.75	33.48
33.03	33.87	33.44	31.74	32.41	32.75	33.24	33.44	33.76	33.74	33.48
29.43	34.83	30.95	28.83	28.45	28.46	28.56	28.82	29.07	29.50	29.85
31.08	30.94	23.16	29.96	30.74	31.26	31.79	32.03	32.24	31.79	31.93
30.55	32.48	30.40	30.21	30.57	30.32	30.31	30.37	30.34	30.47	30.43
31.32	31.58	30.14	30.52	30.82	31.03	31.36	32.09	32.31	31.93	32.00
32.86	30.65	30.82	30.97	31.03	31.36	32.09	32.31	31.93	32.00	
SNR_DB										
35.33	35.91	34.81	34.73	34.68	34.67	34.74	34.73	36.31	36.42	37.16
38.66	37.77	38.16	38.19	38.38	38.87	38.71	38.20	39.12	39.21	39.24
39.26	39.43	39.29	39.22	39.28	39.32	39.28	39.23	39.24	39.18	39.10
38.86	39.45	39.28	39.23	39.04	39.00	38.83	38.67	38.48	38.50	38.31
38.40	38.77	38.60	38.51	38.47	38.39	38.29	38.22	38.24	38.27	38.27
36.80	38.63	38.56	38.40	38.08	38.07	38.01	38.05	38.29	38.47	38.62
37.68	37.63	37.32	37.32	37.40	37.59	37.64	37.85	37.89	38.16	38.08
37.94	38.18	37.39	37.39	37.57	37.65	37.87	38.03	38.24	38.24	38.06
35.01	38.84	37.28	34.44	34.35	34.35	34.35	34.43	34.48	34.69	34.90
36.15	38.70	35.68	35.81	35.96	36.13	36.33	36.45	36.60	36.49	36.47
36.13	36.81	36.29	36.13	36.11	36.08	36.04	36.01	35.96	35.98	35.99
36.48	36.44	36.24	36.26	36.29	36.33	36.45	36.61	36.81	36.70	36.69
37.04	36.44	36.36	36.33	36.31						
SNR_DB										
39.80	39.94	37.96	37.72	37.55	37.52	37.60	37.29	37.56	37.70	38.39
40.26	39.29	39.90	39.87	40.02	40.49	40.18	40.78	40.75	40.85	40.76
40.60	41.12	41.17	40.88	40.83	40.69	40.55	40.37	40.33	40.20	40.03
39.66	40.58	40.42	40.13	39.97	39.87	39.57	39.33	39.14	39.07	38.90
39.16	39.71	39.40	39.21	39.19	39.09	38.96	38.95	39.05	39.06	39.03
38.21	39.81	39.77	39.43	37.61	37.47	37.38	37.61	37.79	37.99	38.20
39.75	39.44	39.69	39.49	39.54	39.68	39.71	39.91	39.91	40.16	40.16
40.08	40.32	39.69	39.60	39.71	39.86	40.03	40.13	40.28	40.40	40.40
37.25	40.93	39.57	36.80	36.43	36.41	36.43	36.64	36.85	37.11	37.36
38.99	38.25	38.97	38.83	38.87	38.98	39.18	39.25	39.32	39.15	39.16
38.78	39.52	39.31	39.00	38.88	38.75	38.61	38.55	38.49	38.43	38.44
39.02	38.96	39.21	38.94	38.88	38.83	38.87	39.07	39.22	39.12	39.09
39.46	39.40	39.13	38.99	38.89	38.78					
BITS										
1514585	482143	92024	87582	129629	92736	233813	110588	89267	79851	116952
54849	100282	127052	212096	118097	216243	125284	163185	140147	7024065	3308666
3123148	351328	72986	122106	126653	151913	138749	149923	144591	149108	143087
4725631	405736	633874	452597	75388	115872	133952	142984	137380	142996	135861
7927762	449019	80094	128217	140219	128517	129560	128562	135979	133077	135014
9524816	460756	74896	124261	1474362	32144	43466	69799	95955	110947	102877
11081724	528026	86240	83077	97857	125072	111383	143863	119432	169716	92532
112869552	523316	100286	82214	119355	108206	136938	124078	154930	116804	321601
14335234	591263	80167	410153	33673	35104	38521	47593	83765	91806	91806
15398320	473566	71677	87052	105987	117049	141724	139057	174915	134498	157562
117547278	589392	118067	77029	135916	112862	114803	109274	106546	115594	118774
19140088	541567	97089	100140	113994	112588	92728	115219	157503	122420	139561
551818	101177	112708	115372	111274						

MOBILE & CALENDER 4Mbit/s									
FLOWERGARDEN 4Mbit/s									
BITRATES									
NB_COEFF Y	12766	0.319148	9022	0.225548	NB_COEFF Y	16400	0.409996	13328	0.33208
NB_COEFF DB	2687	0.067174	2667	0.066684	NB_COEFF DB	6765	0.159118	6768	0.169203
NB_COEFF DR	1795	0.044883	1507	0.037682	NB_COEFF DR	4576	0.114399	5761	0.144015
ADD Y	63267	1.581687	58702	1.467557	ADD Y	156357	3.908932	134598	3.364941
ADD DB	5021	0.125536	6106	0.152639	ADD DB	16508	0.412699	20630	0.515741
ADD DR	4084	0.102110	3851	0.096280	ADD DR	11789	0.294722	15031	0.375779
COEFF Y	37076	0.926905	41889	1.047220	COEFF Y	102860	2.571499	114202	2.855039
COEFF DB	4472	0.111810	5018	0.125455	COEFF DB	12378	0.309446	13648	0.341195
COEFF DR	3863	0.096568	2913	0.072830	COEFF DR	10194	0.254860	8817	0.224416
TOTAL Y	113110	2.827740	109613	2.740324	TOTAL Y	275617	6.890425	262128	6.553187
TOTAL DB	12181	0.304521	13791	0.344778	TOTAL DB	35651	0.891263	41046	1.026139
TOTAL DR	9742	0.243561	8272	0.206792	TOTAL DR	26559	0.663981	29608	0.740210
TOTAL Y+DB+DR	135033	3.375823	131676	3.291894	TOTAL Y+DB+DR	337827	8.456711	332781	8.319536
INTRA/INTER	1620	0.040510	1620	0.040506	INTRA/INTER	1620	0.040509	1620	0.040505
VECTORS	21759	0.543974	25366	0.634142	VECTORS	18477	0.461926	23480	0.587009
INITIAL REGUL									
Intra	Inter	Intra	Inter	Intra	Inter	Intra	Inter	Intra	Inter
124	152	124	152	106	130	106	130	106	130
AVERAGE REGUL									
Intra	Inter	Intra	Inter	Intra	Inter	Intra	Inter	Intra	Inter
127	152	124	150	107	134	106	130	106	130
BUFFER OCCUPANCY MIN (%) MAX (%)									
11.75	69.62	9.79	64.80	10.21	71.31	9.08	62.05	9.08	62.05
TABLE TENNIS 4Mbit/s									
FLOWERGARDEN 4Mbit/s									
BITRATES									
NB_COEFF Y	11616	0.290403	9022	0.225548	NB_COEFF Y	16517	0.412921	14562	0.364058
NB_COEFF DB	2190	0.079758	2667	0.066684	NB_COEFF DB	7322	0.183041	8031	0.200771
NB_COEFF DR	2630	0.065746	1507	0.037682	NB_COEFF DR	5685	0.142117	6306	0.157655
ADD Y	58041	1.451020	58702	1.467557	ADD Y	146367	3.659168	106803	2.670085
ADD DB	5572	0.139299	6106	0.152639	ADD DB	18018	0.450453	25943	0.648579
ADD DR	5716	0.142906	3851	0.096280	ADD DR	14375	0.359382	21911	0.547771
COEFF Y	38873	0.971825	41889	1.047220	COEFF Y	105533	2.638315	86166	2.154145
COEFF DB	4418	0.110445	5018	0.125455	COEFF DB	11208	0.280188	27268	0.681712
COEFF DR	5015	0.125387	2913	0.072830	COEFF DR	11052	0.276300	25220	0.630497
TOTAL Y	108530	2.713249	109613	2.740324	TOTAL Y	268416	6.710404	207532	5.188288
TOTAL DB	13180	0.329502	13791	0.344778	TOTAL DB	36547	0.913682	61242	1.531062
TOTAL DR	13362	0.334039	8272	0.206792	TOTAL DR	31112	0.777799	53437	1.335922
TOTAL Y+DB+DR	135072	3.376789	131676	3.291894	TOTAL Y+DB+DR	336076	8.401891	322211	8.052273
INTRA/INTER	1645	0.041134	1620	0.040510	INTRA/INTER	1644	0.041088	1621	0.040530
VECTORS	28098	0.702442	25366	0.634142	VECTORS	26603	0.665068	34078	0.851951
INITIAL REGUL									
Intra	Inter	Intra	Inter	Intra	Inter	Intra	Inter	Intra	Inter
124	152	124	152	106	130	106	130	106	130
AVERAGE REGUL									
Intra	Inter	Intra	Inter	Intra	Inter	Intra	Inter	Intra	Inter
120	139	101	121	101	121	100	130	100	130
BUFFER OCCUPANCY MIN (%) MAX (%)									
3.51	96.44	6.39	90.25	6.39	90.25	6.75	61.27	6.75	61.27

```

#####
#####  LS -L for bit_stream files after 239 fields and 249 fields  #####
#####
#
#
# -r----- 1 mauj      20131124 Oct 12 04:27 fl4_bit_stream_249_fields
# -r----- 1 mauj      20185854 Oct 12 07:37 mo4_bit_stream_249_fields
# -r----- 1 mauj      20132436 Oct  8 08:47 tn4_bit_stream_249_fields
#
# -r----- 1 mauj      19134928 Oct 12 04:27 fl4_bit_stream_239_fields
# -r----- 1 mauj      19148942 Oct 12 07:37 mo4_bit_stream_239_fields
# -r----- 1 mauj      19140088 Oct  8 08:47 tn4_bit_stream_239_fields
#
# -r----- 1 mauj      45134708 Oct 12 11:06 fl9_bit_stream_249_fields
# -r----- 1 mauj      45305292 Oct 12 23:54 mo9_bit_stream_249_fields
# -r----- 1 mauj      45329256 Oct 12 15:23 po9_bit_stream_249_fields
# -r----- 1 mauj      45158784 Oct 12 19:38 tn9_bit_stream_249_fields
#
# -r----- 1 mauj      43024704 Oct 12 11:06 fl9_bit_stream_239_fields
# -r----- 1 mauj      43069048 Oct 12 23:54 mo9_bit_stream_239_fields
# -r----- 1 mauj      43085224 Oct 12 15:23 po9_bit_stream_239_fields
# -r----- 1 mauj      43053368 Oct 12 19:38 tn9_bit_stream_239_fields
#
#####
#
# fl4 mean bit-rate after 249 fields: 4.042 Mbit/s
# mo4 mean bit-rate after 249 fields: 4.053 Mbit/s
# tn4 mean bit-rate after 249 fields: 4.043 Mbit/s
#
# fl4 mean bit-rate after 239 fields: 4.003 Mbit/s
# mo4 mean bit-rate after 239 fields: 4.006 Mbit/s
# tn4 mean bit-rate after 239 fields: 4.004 Mbit/s
#
# fl9 mean bit-rate after 249 fields: 9.063 Mbit/s
# mo9 mean bit-rate after 249 fields: 9.097 Mbit/s
# po9 mean bit-rate after 249 fields: 9.102 Mbit/s
# tn9 mean bit-rate after 249 fields: 9.068 Mbit/s
#
# fl9 mean bit-rate after 239 fields: 9.001 Mbit/s
# mo9 mean bit-rate after 239 fields: 9.010 Mbit/s
# po9 mean bit-rate after 239 fields: 9.014 Mbit/s
# tn9 mean bit-rate after 239 fields: 9.007 Mbit/s
#
#####
#
# SNR_MEAN      Y      DB      DR
#
# fl4      29.43      33.66      34.83
# mo4      27.70      32.73      34.70
# tn4      31.53      36.99      39.02
#
# fl9      34.20      36.65      37.04
# mo9      31.88      35.49      37.49
# po9      34.33      37.16      37.58
# tn9      35.29      39.36      41.77
#
#####

```