

FIGURE 8/Q.724 (Sheet 3 of 4)
Blocking and unblocking signal sending (BLS)

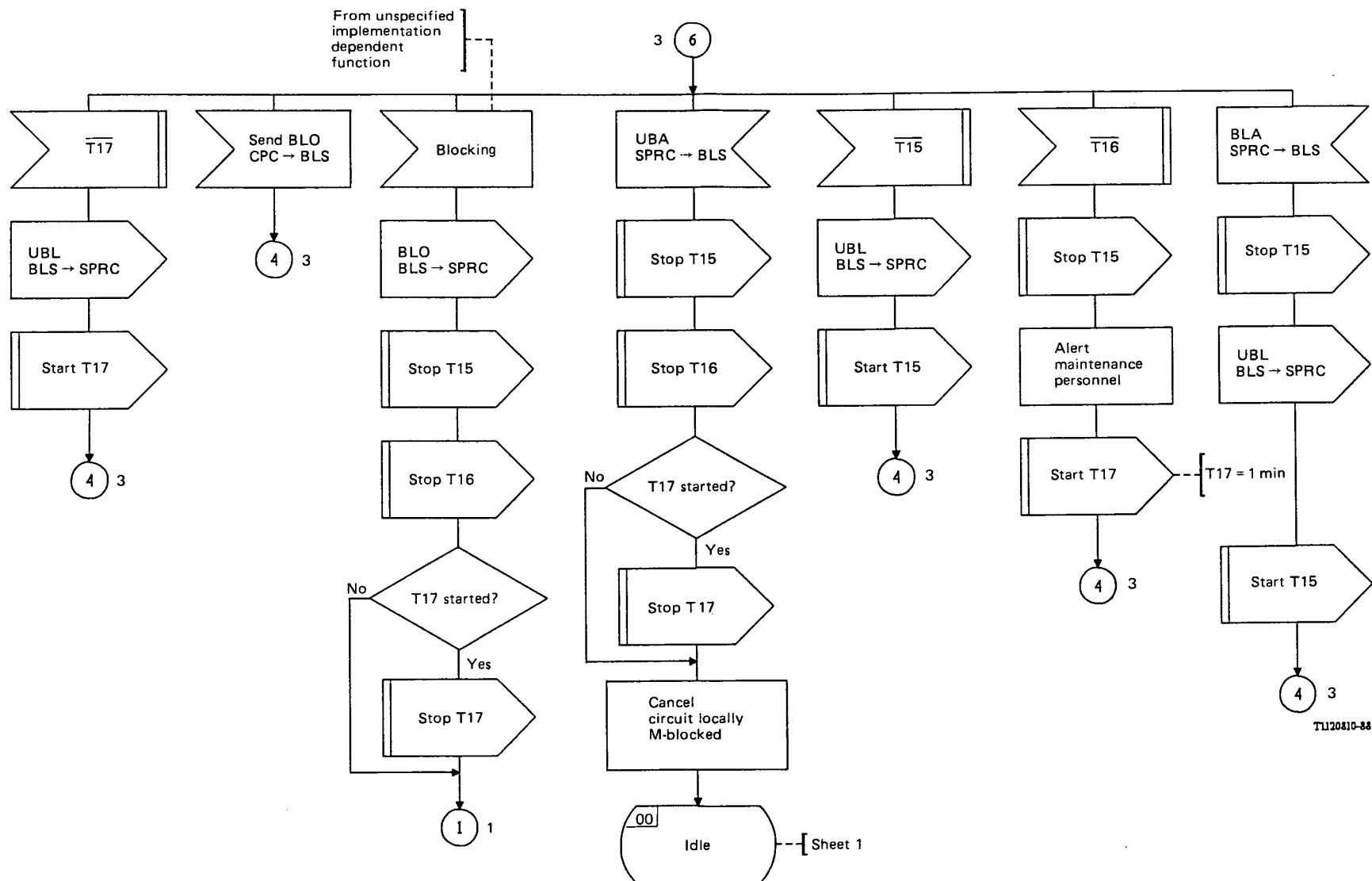
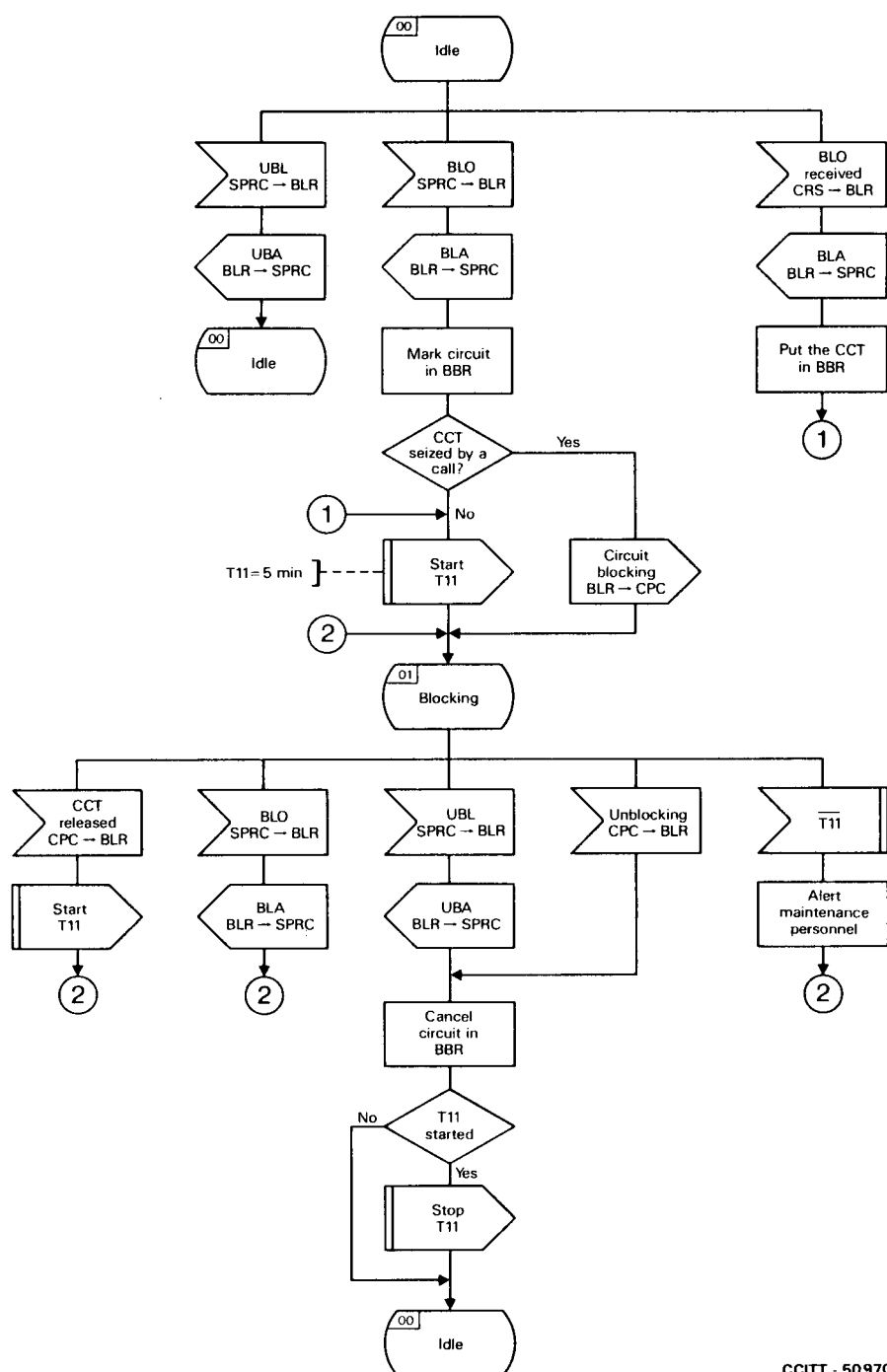


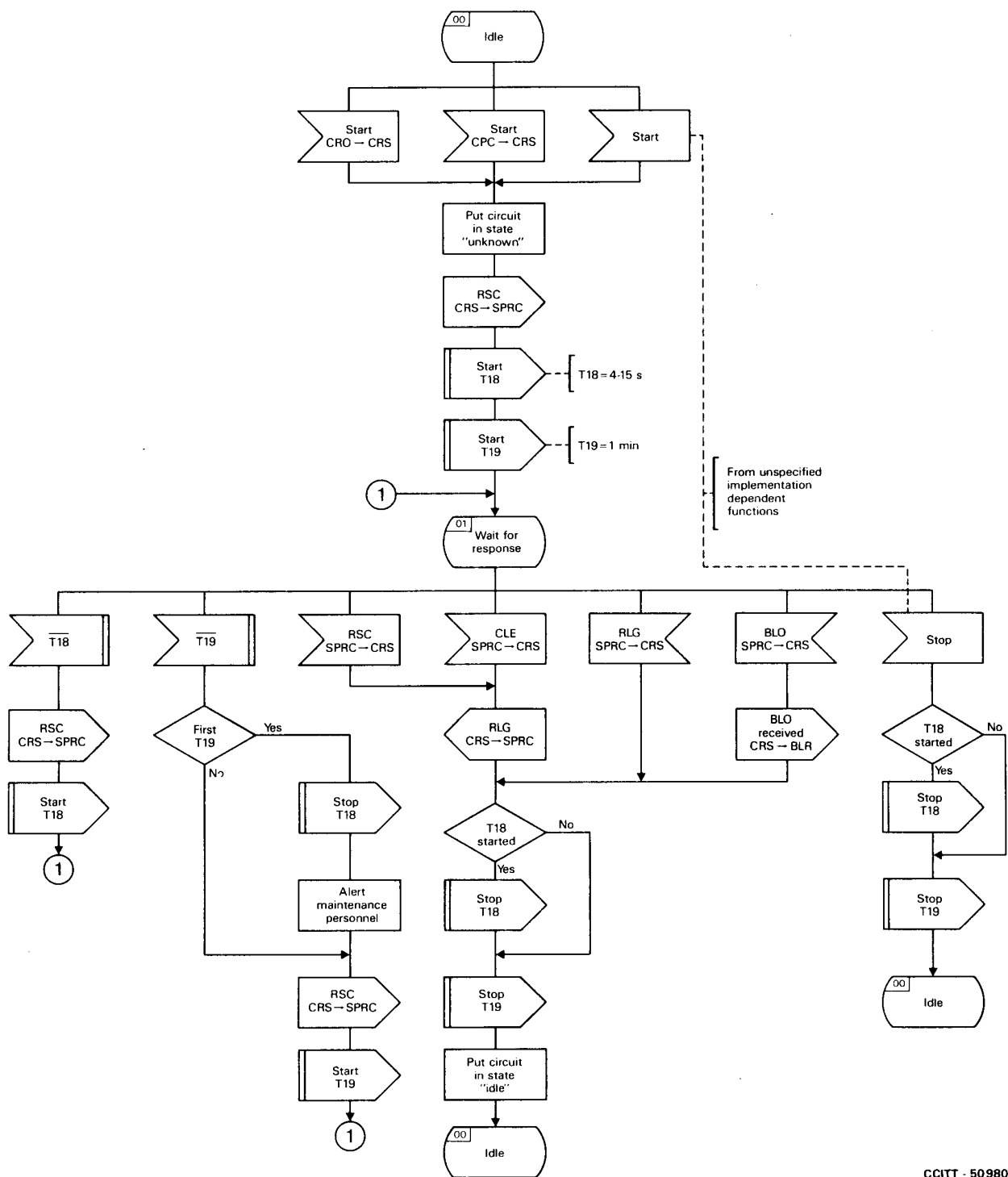
FIGURE 8/Q.724 (Sheet 4 of 4)
Blocking and unblocking signal sending (BLS)



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FIGURE 9/Q.724

Blocking and unblocking signal reception (BLR)



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FIGURE 10/Q.724

Circuit-reset (CRS)

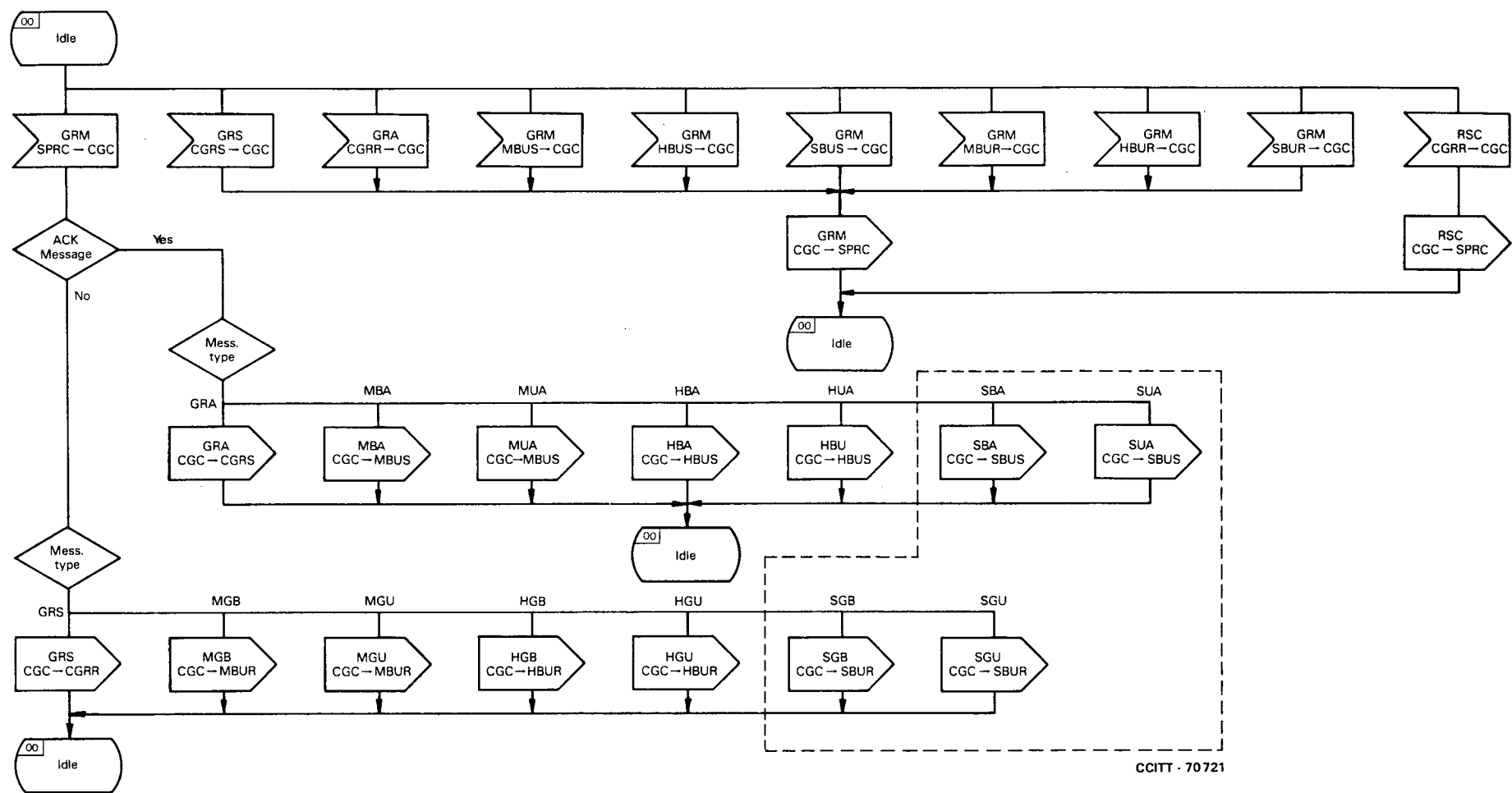


FIGURE 11/Q.724

Circuit group control (CGC)

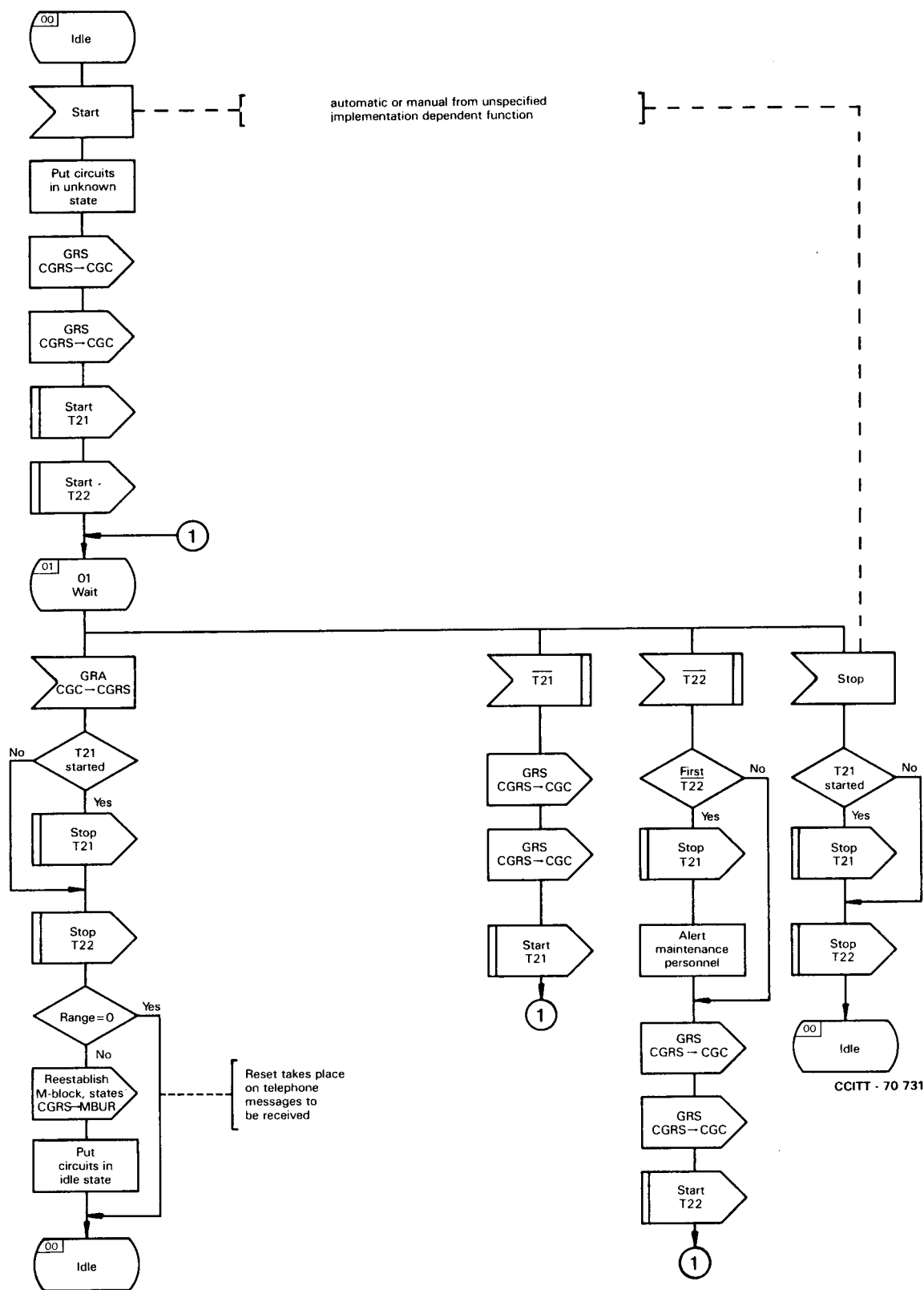
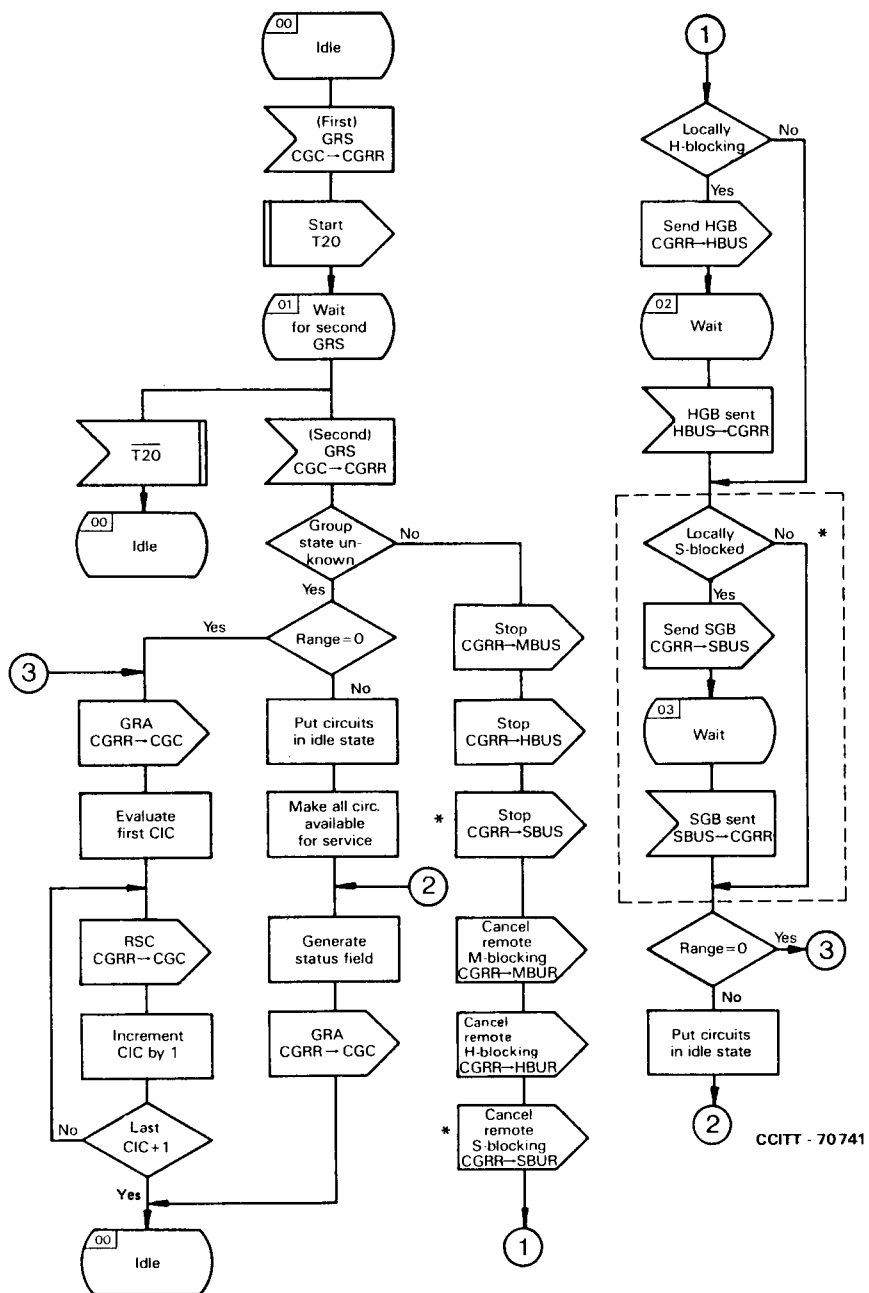


FIGURE 12/Q.724

Circuit group reset sending (CGRS)



* Not included if the national option (SBUS and SBUR) is not implemented.

FIGURE 13/Q.724
Circuit group reset receipt (CGRR)

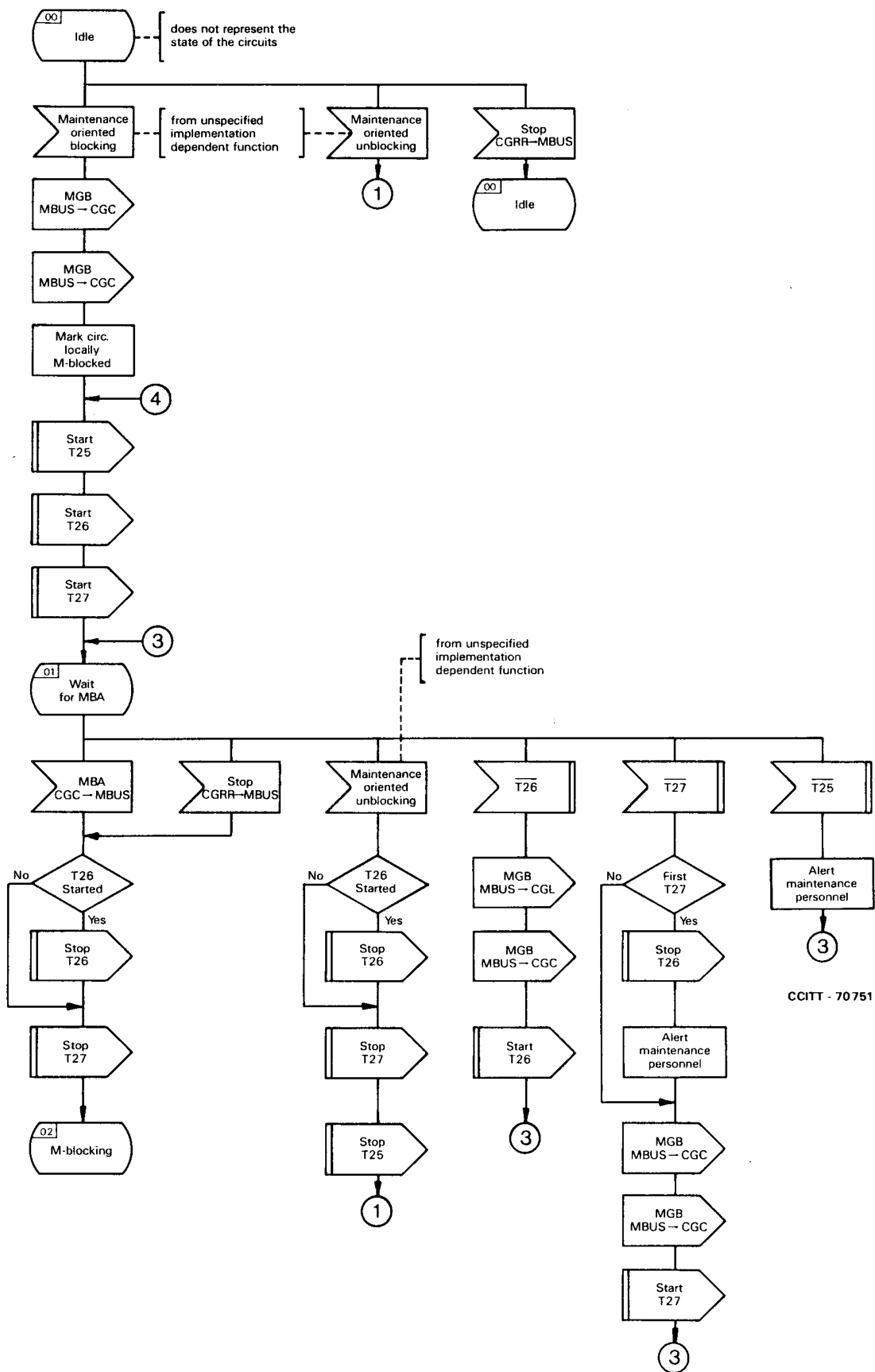


FIGURE 14/Q.724 (Sheet 1 of 2)

Maintenance oriented circuit group blocking and unblocking sending (MBUS)