ITU-T

Q.612

TELECOMMUNICATION STANDARDIZATION SECTOR OF ITU

INTERWORKING OF SIGNALLING SYSTEMS

LOGIC PROCEDURES FOR INCOMING SIGNALLING SYSTEM No. 5

ITU-T Recommendation Q.612

(Extract from the Blue Book)

NOTES

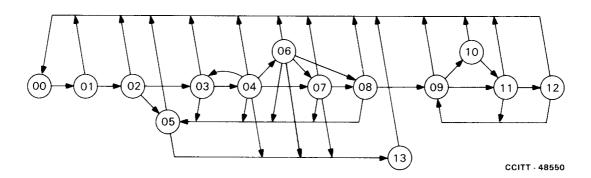
| 1 | ΓU-T Recommendation Q.612 was published in Fascicle VI.6 of the Blue Book. This file is an extract the | from |
|----------|---------------------------------------------------------------------------------------------------------------|--------|
| the Blue | ook. While the presentation and layout of the text might be slightly different from the Blue Book version | i, the |
| contents | the file are identical to the <i>Blue Book</i> version and copyright conditions remain unchanged (see below). | |

| 2 | In | this | Recommendation, | the | expression | "Administration" | is | used | for | conciseness | to | indicate | both | a |
|---------------------------------------------------------------------|----|------|-----------------|-----|------------|------------------|----|------|-----|-------------|----|----------|------|---|
| telecommunication administration and a recognized operating agency. | | | | | | | | | | | | | | |

© ITU 1988, 1993

Recommendation Q.612

LOGIC PROCEDURES FOR INCOMING SIGNALLING SYSTEM No. 5



| State number | State description | Sheet reference | Running timers |
|--------------|--------------------------------|-----------------|------------------|
| 00 | Idle | 1,4 | |
| 01 | Wait for register activation | 1 | |
| 02 | Wait for register signal | 1 | $t_1 + t_2$ |
| 03 | Wait for next register signal | 1 | \mathfrak{t}_2 |
| 04 | Wait for digit analysis | 2 | t_2^2 |
| 05 | Wait for acknowledgement | 2 | t_3^2 |
| 06 | Wait for next register signal | 3 | \mathfrak{t}_2 |
| 07 | Wait for register deactivation | 3 | 2 |
| 08 | Wait for answer | 3 | |
| 09 | Wait for acknowledgement | 4 | t ₃ |
| 10 | Answered | 4 | 3 |
| 11 | Wait for acknowledgement | 4 | t ₃ |
| 12 | Clear-back | 4 | 3 |
| 13 | Wait for clear-forward | 2 | |

FIGURE 1/Q.612

State overview diagram for incoming Signalling System No. 5

Supervisory timers

| $t_1 = 10-20 \text{ s}$ | Recommendation Q.141, § 2.1.3.1, e); Q.141, § 2.1.6, d) |
|-------------------------|---------------------------------------------------------|
| $t_2 = 20-40 \text{ s}$ | Recommendation Q.156, § 3.6.2, b) |
| $t_3 = 10-20 \text{ s}$ | Recommendation Q.141, § 2.1.3.1, e) |

Procedures not shown

The following procedures, not directly relevant to interworking, are not shown in the logic procedures.

 \boldsymbol{P}_1 - Procedure for timeout of receipt of busy flash acknowledgement signal

P₂ - Procedure for timeout of receipt of answer acknowledgement signal

 $P_{3}\mbox{ -} Procedure for timeout of receipt of clearback acknowledgement signal$

FIGURE 2/Q.612

Notes to incoming Signalling System N° . 5

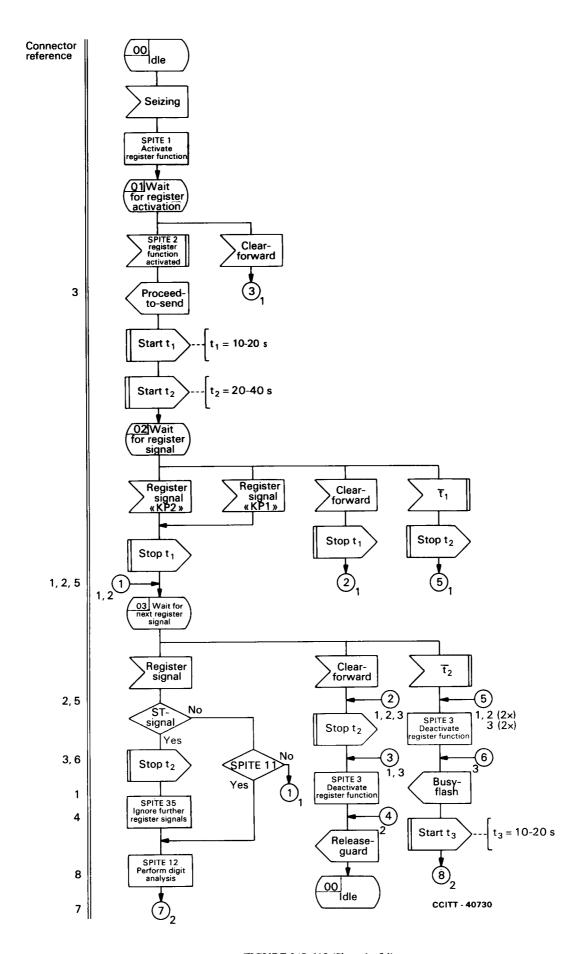


FIGURE 3/Q.612 (Sheet 1 of 4)
Incoming Signalling System No. 5

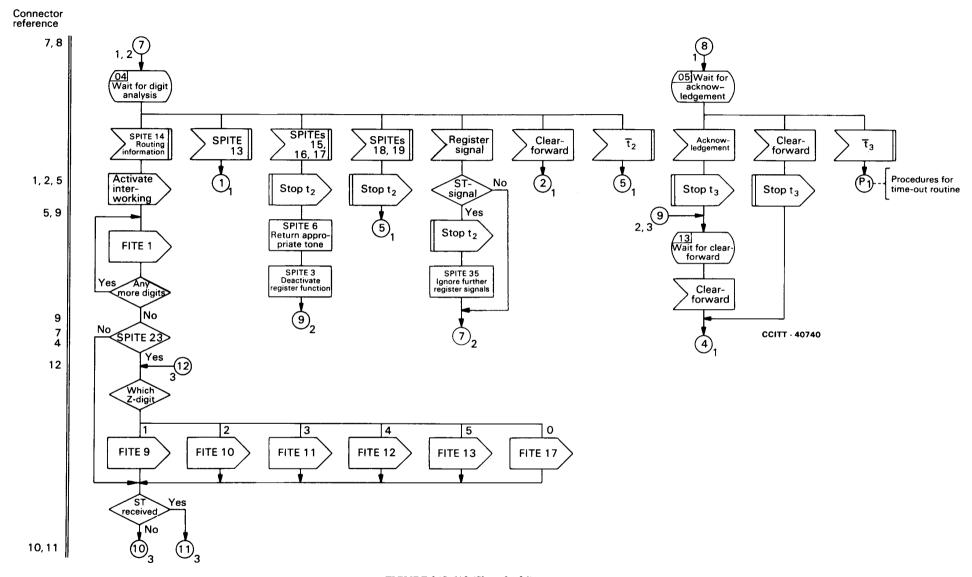


FIGURE 3/Q.612 (Sheet 2 of 4)

Incoming Signalling System No. 5

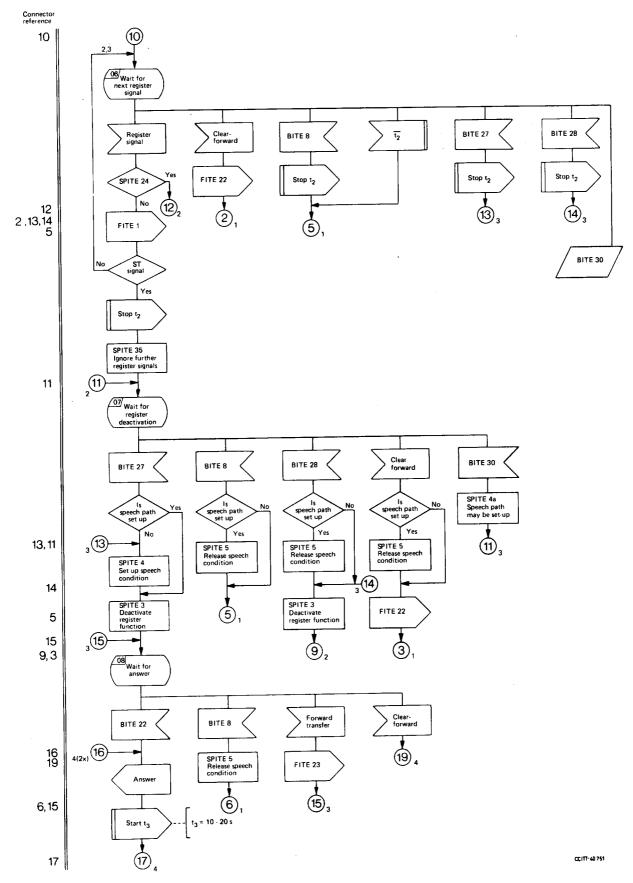


FIGURE 3/Q.612 (Sheet 3 of 4)

Incoming Signalling System No. 5

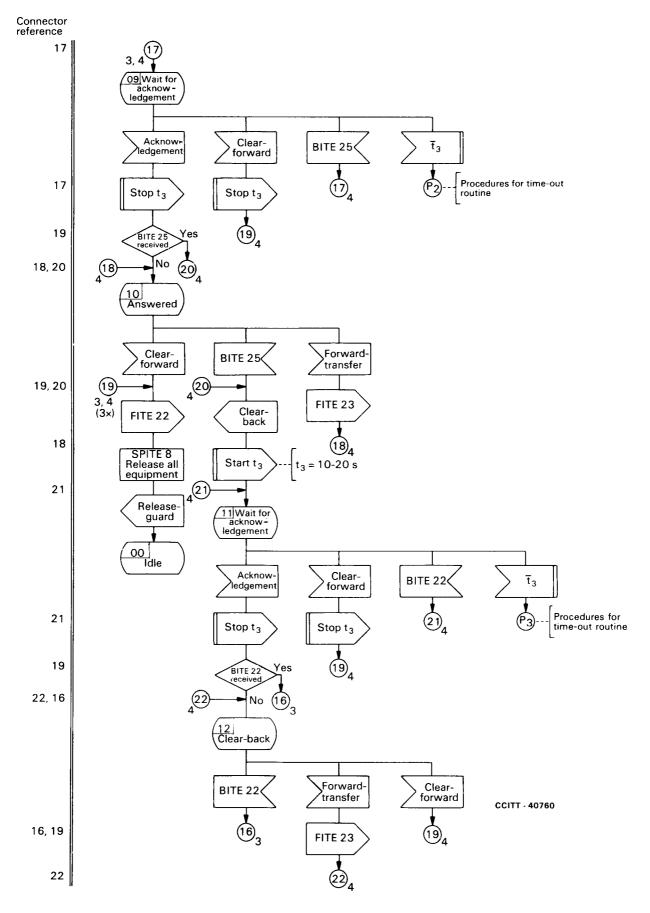


FIGURE 3/Q.612 (Sheet 4 of 4)
Incoming Signalling System No. 5