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SERIES G: TRANSMISSION SYSTEMS AND MEDIA,
DIGITAL SYSTEMS AND NETWORKS

Digital sections and digital line system – Optical fibre
submarine cable systems

Forward error correction for submarine systems

ITU-T Recommendation G.975

(Formerly CCITT Recommendation)

ITU-T G-SERIES RECOMMENDATIONS

TRANSMISSION SYSTEMS AND MEDIA, DIGITAL SYSTEMS AND NETWORKS

INTERNATIONAL TELEPHONE CONNECTIONS AND CIRCUITS	G.100–G.199
GENERAL CHARACTERISTICS COMMON TO ALL ANALOGUE CARRIER-TRANSMISSION SYSTEMS	G.200–G.299
INDIVIDUAL CHARACTERISTICS OF INTERNATIONAL CARRIER TELEPHONE SYSTEMS ON METALLIC LINES	G.300–G.399
GENERAL CHARACTERISTICS OF INTERNATIONAL CARRIER TELEPHONE SYSTEMS ON RADIO-RELAY OR SATELLITE LINKS AND INTERCONNECTION WITH METALLIC LINES	G.400–G.449
COORDINATION OF RADIOTELEPHONY AND LINE TELEPHONY TESTING EQUIPMENTS	G.450–G.499
TRANSMISSION MEDIA CHARACTERISTICS	G.500–G.599
DIGITAL TERMINAL EQUIPMENTS	G.600–G.699
DIGITAL NETWORKS	G.700–G.799
DIGITAL SECTIONS AND DIGITAL LINE SYSTEM	G.800–G.899
General	G.900–G.999
Parameters for optical fibre cable systems	G.900–G.909
Digital sections at hierarchical bit rates based on a bit rate of 2048 kbit/s	G.910–G.919
Digital line transmission systems on cable at non-hierarchical bit rates	G.920–G.929
Digital line systems provided by FDM transmission bearers	G.930–G.939
Digital line systems	G.940–G.949
Digital line systems	G.950–G.959
Digital section and digital transmission systems for customer access to ISDN	G.960–G.969
Optical fibre submarine cable systems	G.970–G.979
Optical line systems for local and access networks	G.980–G.989
Access networks	G.990–G.999

For further details, please refer to the list of ITU-T Recommendations.

Forward error correction for submarine systems

Summary

The present Recommendation is primarily concerned with the implementation of a Forward Error Correction (FEC) function in the multigigabit-per-second optical fibre submarine cable systems. The applications being addressed in this Recommendation are both optically amplified repeatered systems and repeaterless optical systems (described in ITU-T G.973 [3]). The use of this FEC function in submarine terminal transmission equipment is not mandatory.

Source

ITU-T Recommendation G.975 was revised by ITU-T Study Group 15 (1997-2000) and approved under the World Telecommunication Standardization Assembly (Montreal, 27 September – 6 October 2000).

FOREWORD

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CONTENTS

	Page
1 Scope.....	1
2 References.....	1
3 Terms and definitions	1
4 Abbreviations.....	2
5 FEC features	2
5.1 General principles of the FEC function	2
5.2 Error monitoring capability	3
5.3 Interest of the FEC function for submarine systems.....	4
5.4 Inter-terminal channels	4
6 Definition of the FEC function.....	4
6.1 Definitions	4
6.2 Forward Error Correction algorithm.....	4
6.3 Properties of the RS(255,239) code.....	5
6.4 FEC frame structure.....	5
6.4.1 FEC encoder and FEC decoder architectures.....	5
6.4.2 Framing structure.....	8
6.4.3 Scrambling.....	8
6.4.4 Redundancy ratio.....	8
7 FEC function performance.....	9
7.1 Theoretical FEC function performance	9
7.2 Coding gain.....	10
Appendix I – FEC frame structure (optional).....	11
I.1 FEC encoder and FEC decoder architecture.....	11
I.2 Framing structure.....	14
I.3 Scrambling.....	14
I.4 Redundancy ratio	15

ITU-T Recommendation G.975

Forward error correction for submarine systems

1 Scope

The present Recommendation is primarily concerned with the implementation of a Forward Error Correction (FEC) function in the multigigabit-per-second optical fibre submarine cable systems. The applications being addressed in this Recommendation are both optically amplified repeatered systems and repeaterless optical systems (ITU-T G.973 [3]). The use of this FEC function in submarine terminal transmission equipments (TTEs) should not be considered as mandatory.

It is not the intention of this Recommendation to pursue the transverse compatibility of the system. Therefore the selection of the FEC frame structures described in this Recommendation is a matter of joint engineering.

The transmission data rates under consideration in this Recommendation are 2.5 Gbit/s STM-16 (ITU-T G.707 [1]) and integer multiples of 2.5 Gbit/s (interleaved STM-16 tributaries).

Clause 5 presents the main features of the FEC function implemented in the submarine systems, and in particular the error monitoring facility.

Clause 6 provides the definition of the forward error correction algorithm to be used, which is a Reed-Solomon code, and gives guidelines for the implementation of this algorithm in the submarine Terminal Transmission Equipments (TTEs).

Clause 7 is dedicated to the measurement of the performance of this Reed-Solomon code and the expected gain on the optical transmission power budget.

2 References

The following ITU-T Recommendations and other references contain provisions which, through reference in this text, constitute provisions of this Recommendation. At the time of publication, the editions indicated were valid. All Recommendations and other references are subject to revision; users of this Recommendation are therefore encouraged to investigate the possibility of applying the most recent edition of the Recommendations and other references listed below. A list of the currently valid ITU-T Recommendations is regularly published.

- [1] ITU-T G.707 (1996), *Network node interface for the synchronous digital hierarchy (SDH)*.
- [2] ITU-T G.972 (1997), *Definition of terms relevant to optical fibre submarine cable systems*.
- [3] ITU-T G.973 (1996), *Characteristics of repeaterless optical fibre submarine cable systems*.

3 Terms and definitions

This Recommendation uses the following terms defined in other Recommendations:

- Synchronous Digital Hierarchy (SDH): See ITU-T G.707 [1].
- Synchronous Transport Module (STM): See ITU-T G.707 [1].
- Optical fibre submarine cable system: See ITU-T G.972 [2].
- Terminal Transmission Equipment (TTE): See ITU-T G.972 [2].
- Optical power budget: See ITU-T G.972 [2].
- Service channel: See ITU-T G.972 [2].

- Order wire channel: See ITU-T G.972 [2].
- Line error ratio: See ITU-T G.972 [2].
- Forward Error Correction (FEC): See ITU-T G.972 [2].
- FEC frame: See ITU-T G.972 [2].
- FEC encoder: See ITU-T G.972 [2].
- FEC decoder: See ITU-T G.972 [2].

4 Abbreviations

This Recommendation uses the following abbreviations:

BER	Bit Error Ratio
EDFA	Erbium-Doped Fibre Amplifier
FEC	Forward Error Correction
GF	Galois Field
RS	Reed-Solomon

5 FEC features

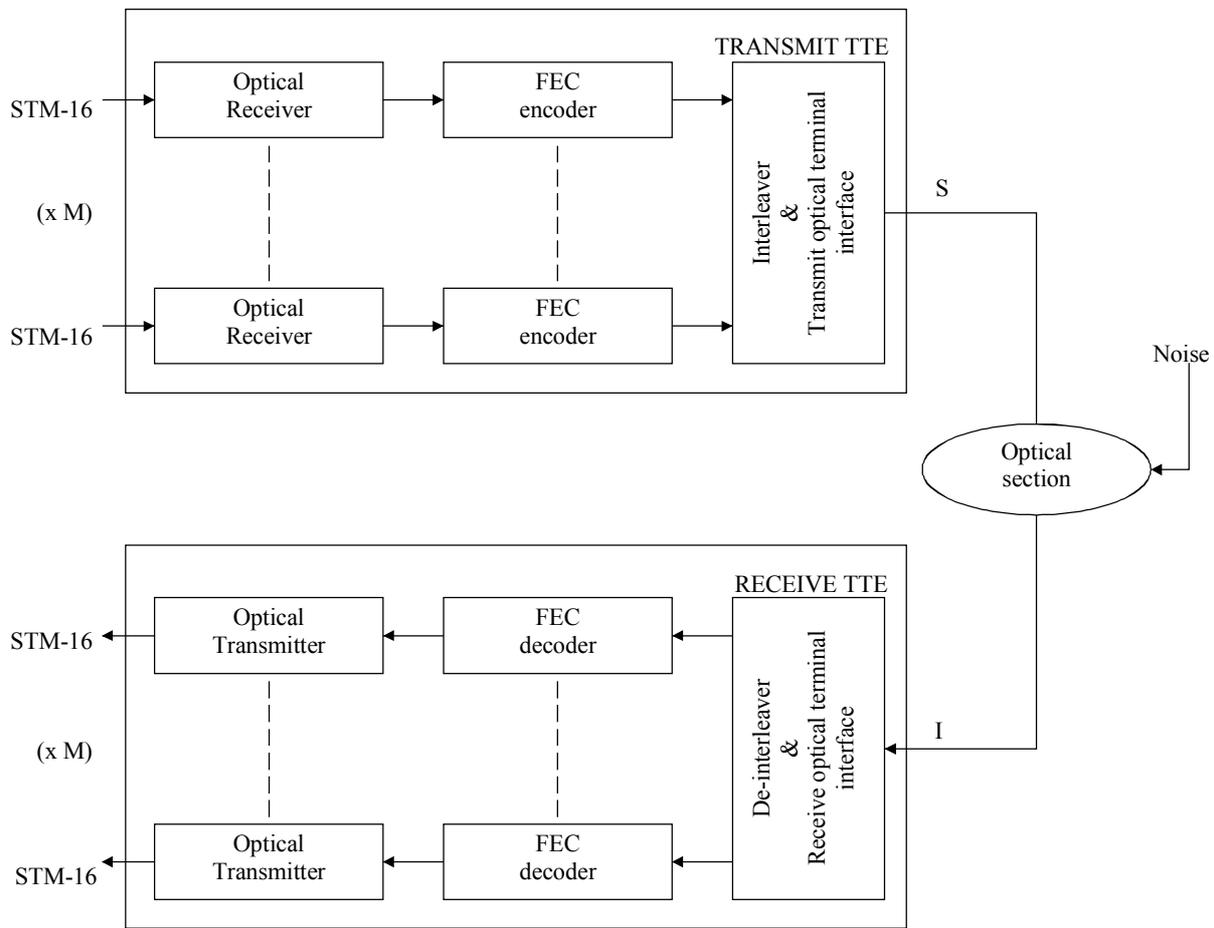
5.1 General principles of the FEC function

The FEC function defined in this Recommendation works on the STM-16 basis. When M (M integer different from 0) STM-16 signals are interleaved to achieve $M \times 2.5$ Gbit/s transmission data rates, the encoding is performed before the interleaving of the M STM-16 tributaries and the decoding is performed after the de-interleaving of the optical line signal.

The FEC function essentially comprises:

- a FEC encoder in the transmit Terminal Transmission Equipment (TTE) that accepts information bits and adds computed redundant symbols, producing encoded data at a higher bit rate;
- a FEC decoder in the receive Terminal Transmission Equipment (TTE) that performs the error correction while extracting the redundancy to regenerate the data that was encoded by the FEC encoder.

Figure 1 outlines the fact that the encoding and decoding procedures are performed at the Terminal Transmission Equipment (TTE) level only, on electrical signals, and benefit the overall optical fibre submarine cable system, which comprises the optical fibre and possibly optical modules such as optical amplifiers using EDFA technology.



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Figure 1/G.975 – Block diagram of a submarine system which uses a FEC function

5.2 Error monitoring capability

The implementation of a FEC function allows the in-line monitoring of the line Bit Error Ratio before correction (BER_{Input}) through the knowledge of the exact number of corrected bits (BER_C). The errors that remain uncorrected after forward error correction (BER_{Output}) (these errors remain when the number of line errors is beyond the decoder's ability to correct) can be considered negligible in the computation of BER_{Input} ($BER_{Input} = BER_C + BER_{Output} \approx BER_C$), for low error rates.

The conditions that could make the previous statement inaccurate ($BER_{Input} > 10^{-3}$) would lead the system into an intermittent loss of FEC frame alignment state (see 5.4 for the FEC frame definition). In fact, the 10^{-3} bit error rate represents the limit beyond which the FEC function becomes inefficient.

The forward error correction code reports the evolution of the line errors (the measurable values of the BER_{Input} are comprised of between 10^{-3} and 10^{-15}) while keeping the system faultless by correcting these errors. Consequently, the FEC function can dynamically provide an evaluation of the system margins relative to the required level of performance. If a maintenance of the line appears to be necessary, it can then be planned before any effective degradation of the transmission.

5.3 Interest of the FEC function for submarine systems

The implementation of a Forward Error Correction (FEC) function in optical fibre submarine cable systems provides significant gains over the overall optical power budget of the link, and lowers at the same time the line BER floor of the system (see 7.2).

The resulting gain over the optical power budget obtained with the FEC technique can then be used to improve either:

- the line parameters:
For repeaterless submarine applications, the FEC function will possibly be used to increase the maximum span length.
For optically amplified submarine applications, the FEC function will possibly be used to either increase the inter-repeater distances or relax optical component and line fibre specifications.
- the overall quality of communication for protection against unwanted degraded operating conditions (component or cable failure, due to ageing for instance).

In counterpart, the use of the FEC function in the submarine Terminal Transmission Equipments (TTEs) introduces an increase of the line bit rate.

5.4 Inter-terminal channels

Provided that a framing structure is included in the FEC frame (see 6.4.2), it is possible to transmit tributary markers for systems carrying several interleaved STM-16 signals, or to transmit order wire channels or service channels through the unused bits of the framing structure.

6 Definition of the FEC function

6.1 Definitions

6.1.1 block code: Such codes are characterized by the fact that the encoder accepts K information symbols from the information source and appends a set of R redundant symbols derived from the information symbols, in accordance with the code algorithm.

6.1.2 cyclic code: A linear code is said to be cyclic when any cyclic shift of a codeword is also a codeword.

6.1.3 systematic code: With such codes, the information word is not disturbed in any way in the encoder and the redundant symbols are added separately to each block.

6.1.4 information word: The information word contains K information symbols.

6.1.5 codeword: The block of N symbols that carries the K information symbols and the R redundant symbols ($N = K + R$).

6.2 Forward Error Correction algorithm

The Forward Error Correction code used to protect the STM-16 information against in-line errors in the optical fibre submarine cable systems is a Reed-Solomon code, already specified in the CMTT Recommendation CCIR 723: the RS(255,239) code. The RS(255,239) code is a non-binary code (the FEC algorithm operates on 8-bit symbols) and belongs to the family of systematic linear cyclic block codes.

The generator polynomial of the code is given by:

$$G(z)=\prod_{i=0}^{15}(z-\alpha^i)$$

where α is a root of the binary primitive polynomial $x^8 + x^4 + x^3 + x^2 + 1$. A data byte ($d_7, d_6, \dots, d_1, d_0$) is identified with the element $d_7 \cdot \alpha^7 + d_6 \cdot \alpha^6 + \dots + d_1 \cdot \alpha^1 + d_0$ in GF(256), the finite field with 256 elements.

6.3 Properties of the RS(255,239) code

With regard to the wide variety of forward error correction codes, the selection of a particular forward error correction code consists in part of matching the features of a coding technique with the system objectives being addressed.

The choice of the Reed-Solomon code for optical fibre submarine cable systems is essentially motivated by the following properties:

- an important error correcting capacity with respect to the redundancy ratio applied to the information word: the RS(255,239) algorithm can correct up to 8 erroneous byte-symbols in a single codeword of length 255;
- a low complexity of both the FEC encoder and the FEC decoder;
- a coding structure compatible with binary transmissions, providing that a demultiplexing operation is performed;
- an important correcting capacity of burst errors. This intrinsic property of the Reed-Solomon codes is even enhanced by the interleaving of elementary RS(255,239) codecs. This technique, implemented on the 2.5 Gbit/s optical fibre submarine cable systems, puts the error correcting capacity to bursts of 1024 bits maximal length, for 16 interleaved codecs.

In addition, the Reed-Solomon codes remain among the most efficient codes which can be implemented using the state-of-the-art hardware and software technology.

6.4 FEC frame structure

Another FEC frame structure (Optional) is given in Appendix I for information.

6.4.1 FEC encoder and FEC decoder architectures

In order to enhance the immunity of the optical fibre submarine cable system to burst errors, several RS(255,239) codes can be interleaved. In Figures 2 and 3, (n) denotes the interleaving order (n is a non-zero integer).

Given the interleaving to depth " n " of RS(255,239) codes, the architectures of both the FEC encoder and the FEC decoder are detailed in Figures 2 and 3 respectively.

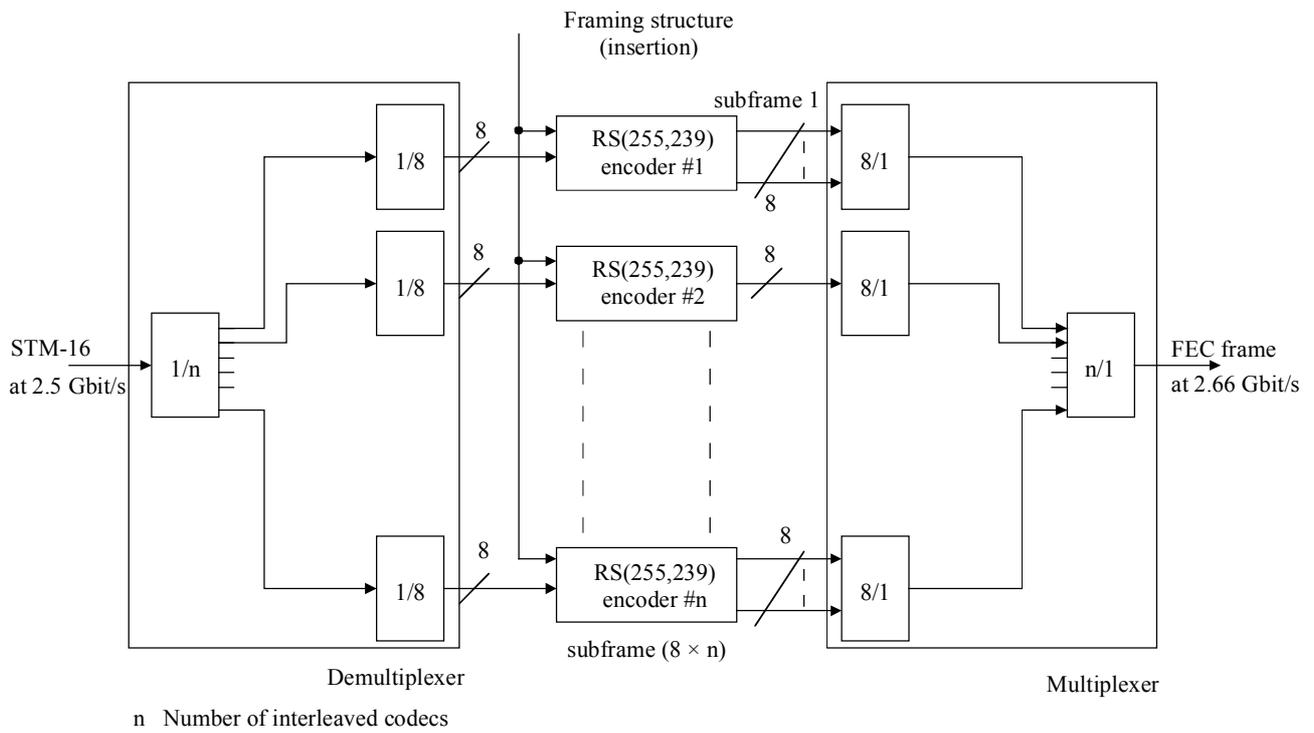


Figure 2/G.975 – FEC encoder architecture

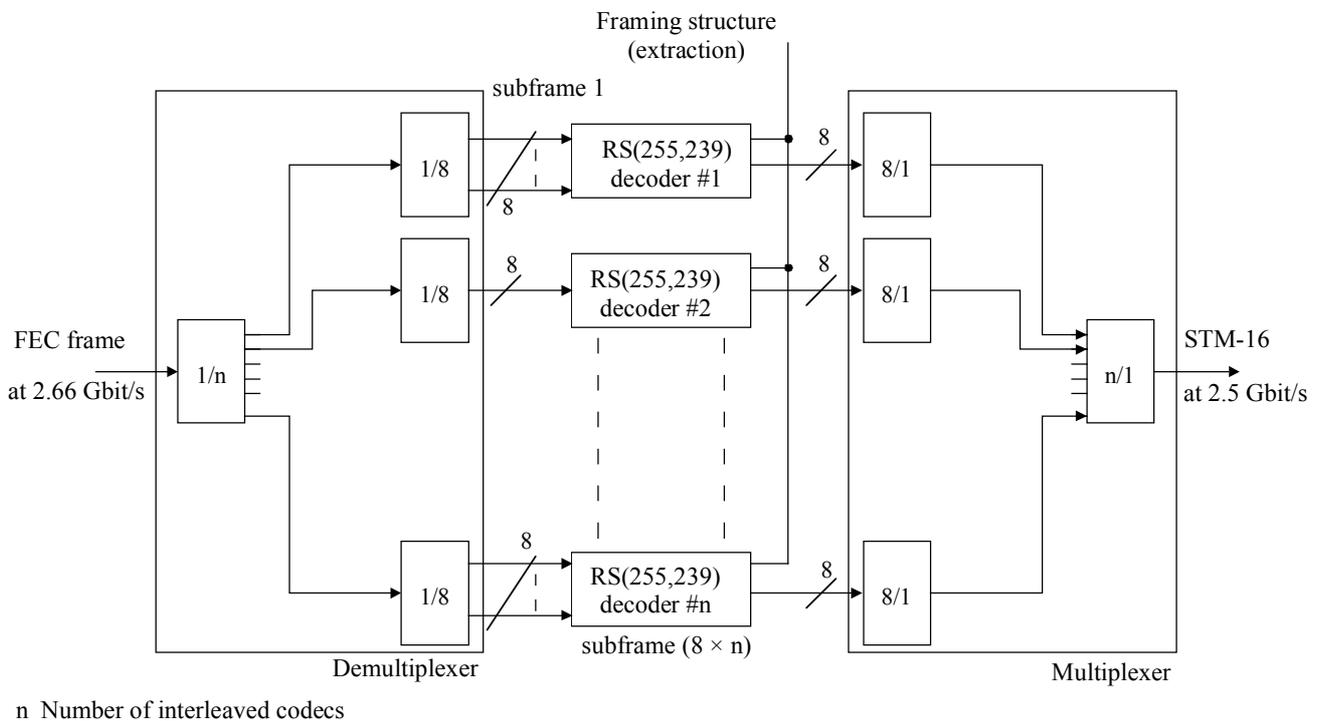


Figure 3/G.975 – FEC decoder architecture

For data integrity, the digital multiplexer and the digital demultiplexer represented in Figures 2 and 3 are strictly symmetrical. In addition, the same digital multiplexers and the same digital demultiplexers are used for both the FEC encoder and the FEC decoder.

Due to the fact that each elementary Reed-Solomon algorithm process byte information and therefore works on 8 parallel data stream, the demultiplexers deliver $(8 \times n)$ data stream to the (n) interleaved codecs while the multiplexers do the reverse operation.

Provided the FEC encoder and the FEC decoder architectures, the FEC frame construction is described in Figure 4.

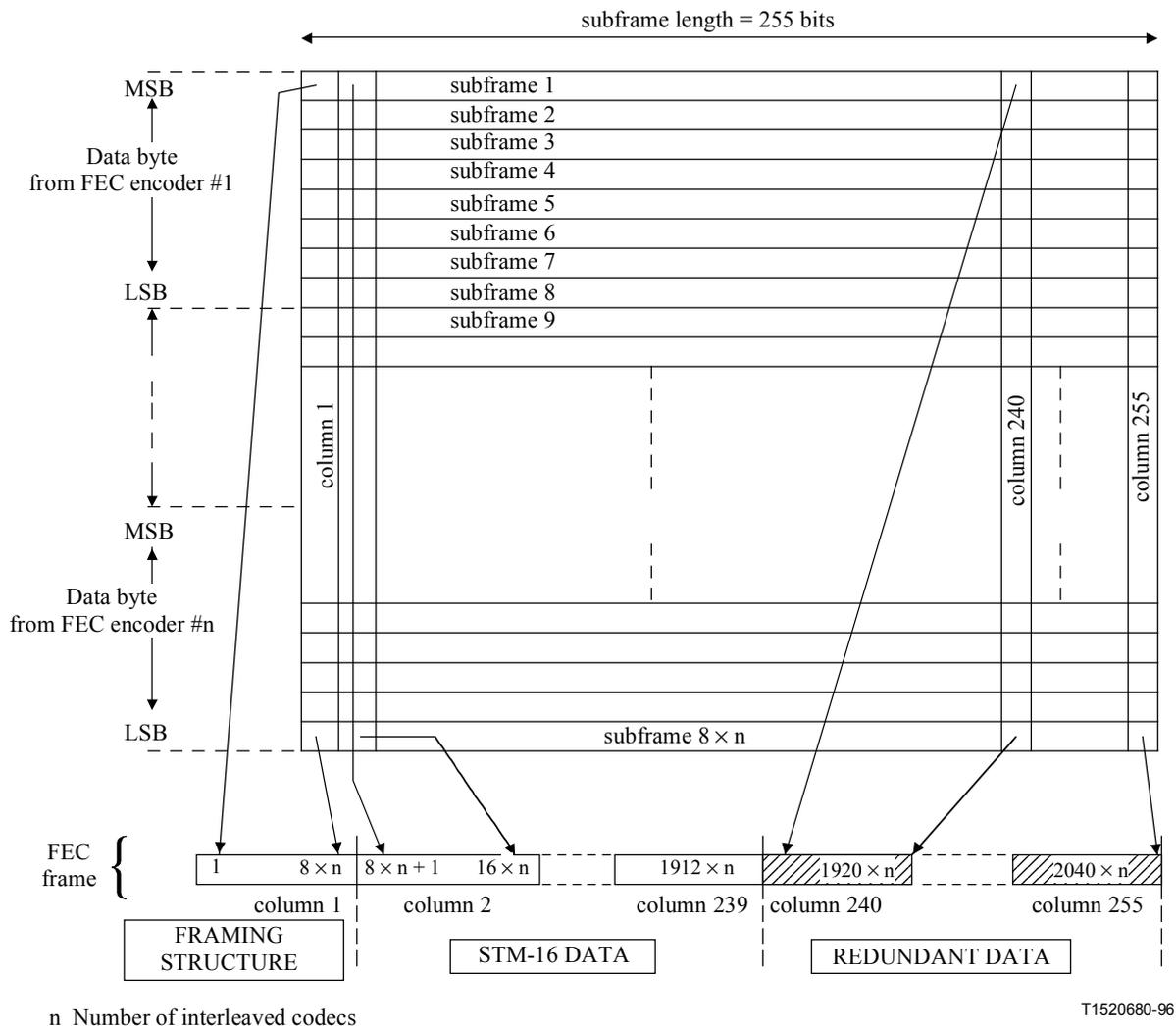


Figure 4/G.975 – FEC frame construction

Due to the interleaving of RS(255,239) codes to depth n , the FEC frame is $(2040 \times n)$ bits long and is made of $(8 \times n)$ bit interleaved subframes.

And, as a consequence of the symmetry of the digital demultiplexers and the digital multiplexers on each side of the Reed-Solomon algorithms, the sequence of STM-16 data bits within the FEC frame is identical to that within the STM-16 input signal.

6.4.2 Framing structure

A framing structure is added in the FEC frame to possibly insert a FEC frame alignment word, which is required to perform the synchronization of the FEC frame with the FEC decoder structure at the receive Terminal Transmission Equipment (TTE).

The remaining spare bits can be used for carrying tributary markers, order wire channels or service channels.

The FEC frame presented in 6.4.1 can be divided into $(8 \times n)$ 255 bits long subframe. Each subframe (see Figure 5) contains the following information:

- bit 1 of each subframe carries the framing structure [either the FEC Frame Alignment Word, the tributary markers for STM-16 data stream identification in systems carrying multiples of STM-16 (if required) or order wire channels or service channels for inter-terminal communication];
- bits 2 to 239 of each subframe carry the STM-16 information;
- bits 240 to 255 of each subframe carry the redundant bits, computed by the RS(255,239) algorithm.

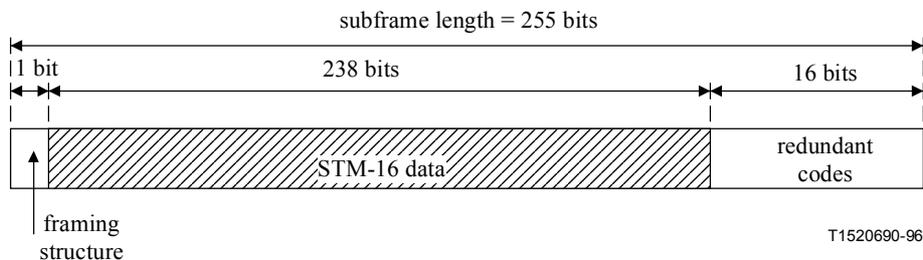


Figure 5/G.975 – Content of the subframes of the FEC frame

6.4.3 Scrambling

STM-16 data in the FEC frame are already scrambled as shown in ITU-T G.707 [1]. Hence, rescrumbling of the FEC frame is not generally required.

However, when implemented in the submarine Terminal Transmission Equipment (TTE), the scrambling facility should possibly be inhibited.

The scrambling of the FEC frame may be carried out in the following procedure: the FEC frame is scrambled, with the exception of the framing structure bits of the FEC frame, by a $x^7 + x + 1$ polynomial initiated at each frame on the first bit which follows the framing structure in the FEC frame.

The first bits of the scrambler sequence are 1111111. Thereafter, the scrambler runs continuously throughout the complete FEC frame.

6.4.4 Redundancy ratio

The redundancy ratio of the FEC function defined in ITU-T G.975 is equal to 1/14. Consequently, the line bit rates of optical fibre submarine cable systems using the forward error correction feature are as follows:

- one STM-16 tributary carried: $2\,488.320 \times 15/14$ Mbit/s;
- two STM-16 tributaries carried: 2 multiplexed signals at $2\,488.320 \times 15/14$ Mbit/s;
- M STM-16 tributaries carried (M is a non-zero integer): M multiplexed signals at $2\,488.320 \times 15/14$ Mbit/s.

7 FEC function performance

7.1 Theoretical FEC function performance

A criterion for the evaluation of the intrinsic correcting performance of the RS(255,239) code is the theoretical relationship between the line BER after FEC function correction (BER_{Output}) and the line BER before FEC function correction (BER_{Input}).

For the RS codes, this criterion can be mathematically computed with the assumptions that errors occur independently from each other and that the decoder never fails (probability of incorrect decoding equal to zero):

$$\left\{ \begin{array}{l} P_{UE} = \sum_{i=9}^N \frac{i}{N} \cdot \binom{N}{i} \cdot P_{SE}^i \cdot (1 - P_{SE})^{N-i} \quad \text{with } N = 255 \\ BER_{Input} = 1 - (1 - P_{SE})^{1/8} \\ BER_{Output} = 1 - (1 - P_{UE})^{1/8} \end{array} \right.$$

with:

- P_{UE} Probability of an Uncorrectable Error
- P_{SE} Probability of a Symbol (byte) Error
- N Codeword length (255)

Figure 6 and Table 1 give an indication of the theoretical intrinsic performance of the RS(255,239) code.

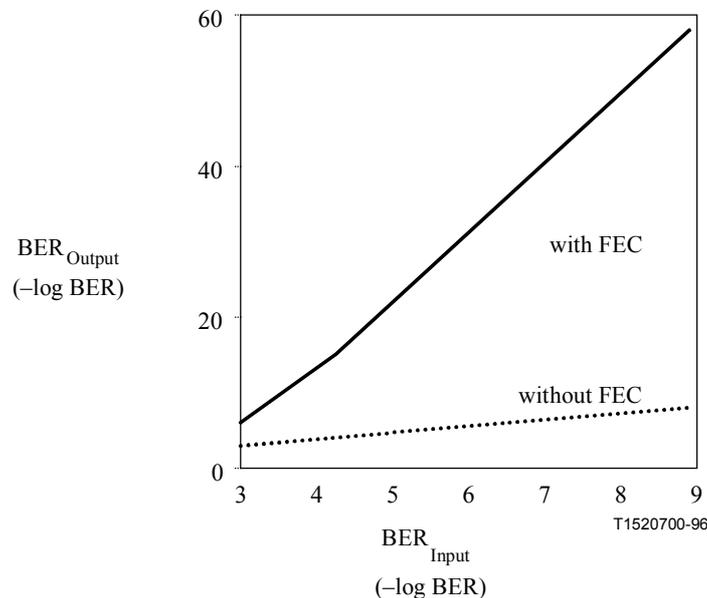


Figure 6/G.975 – Theoretical output versus input BER

Table 1/G.975 – Theoretical output versus input BER

BER_{Input}	BER_{Output}
10^{-4}	$5 \cdot 10^{-15}$
10^{-5}	$6.3 \cdot 10^{-24}$
10^{-6}	$6.4 \cdot 10^{-33}$

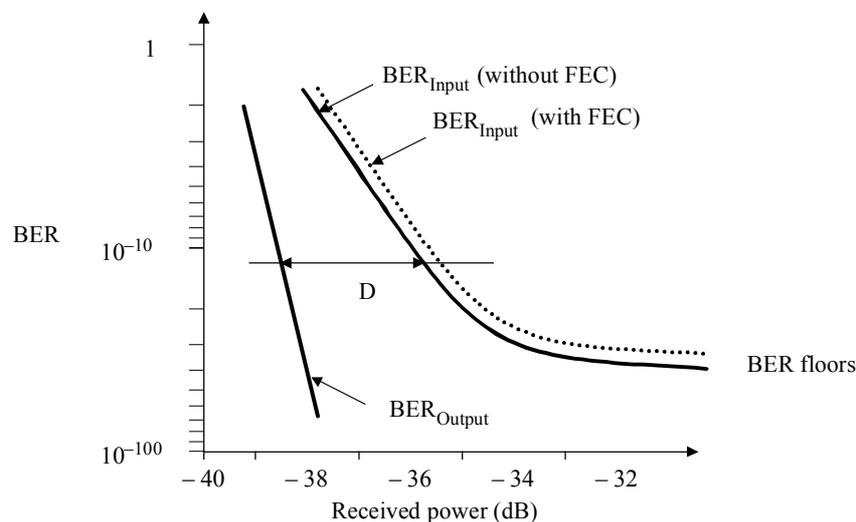
The statistical independence between consecutive errors is generally considered to be relevant with the Reed-Solomon interleaving technique: with this approach, a bursty channel is transformed into several independent error channels.

For BER_{Input} above 10^{-3} , the probability of incorrect decoding (an incorrect decoding occurs when the decoder attempts correction but acts incorrectly because the error pattern is beyond its ability to correct) becomes un-negligible and makes the previous calculation of BER_{Output} inaccurate. In such cases, the BER_{Output} versus BER_{Input} curves are even located below the "without FEC" curve in Figure 6.

This calculation remains identical whatever the optical fibre submarine cable system, but gives no indication on the degradation brought by the in-line bit rate increase over the optical transmission channel performances.

7.2 Coding gain

The FEC function performance can also be evaluated through the coding gain, that is, the difference in the input optical power of the receiver required for coded and uncoded operation to provide a specified level of communication performance ($BER = 10^{-10}$ in Figure 7).



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Figure 7/G.975 – Coding gain (D) evaluation scheme

While correcting the BER_{Input} , the FEC function brings a positive coding gain to the system. This coding gain is slightly attenuated by the effect of the signal bandwidth increase brought by the redundant symbols.

The coding gain value intrinsically depends on the overall structure of the optical fibre submarine cable system (inter-repeater distances, photodetection parameters and optical amplifier output power in particular).

When a line BER floor penalizes the optical fibre submarine cable system, one of the most valuable effects of the FEC function is to translate these line BER floors down to acceptable levels which the physical system could never achieve without FEC, regardless of the receiver input power.

The expected coding gain for optical fibre submarine cable systems is 4 to 5 dB per fibre span.

APPENDIX I

FEC frame structure (optional)

I.1 FEC encoder and FEC decoder architecture

In contrast to the subframe length of 255 bits described in the main body, 256 bits can be used where one dummy bit is added to the 255-bit subframe. The resultant 256-bit subframe can be then divided by four. This allows four-parallel calculation thus making it possible to use an FEC IC with a 2.667 Gbit/s throughput.

The architecture of FEC encoder and the FEC decoder for STM-16 is shown in Figures I.1 and I.2, where the entire signal is calculated by using a single FEC IC. For STM-64 systems, signals are demultiplexed into four signal groups, and each demultiplexed signal is processed with a single FEC IC. The architecture of FEC encoder and the FEC decoder for STM-64 is shown in Figures I.3 and I.4.

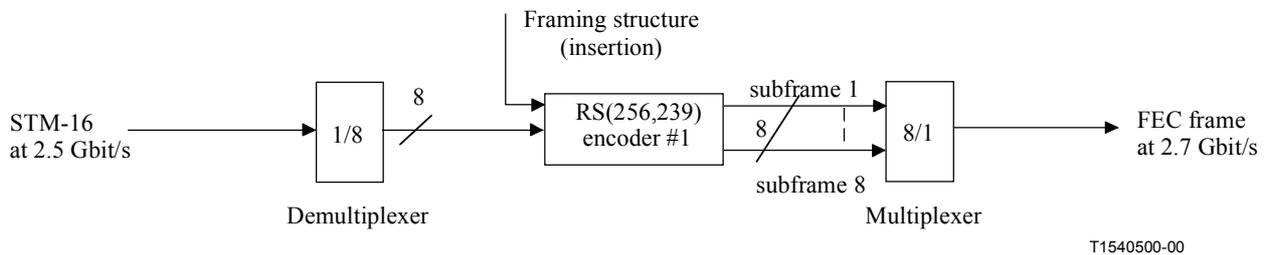


Figure I.1/G.975 – FEC encoder architecture for STM-16 systems

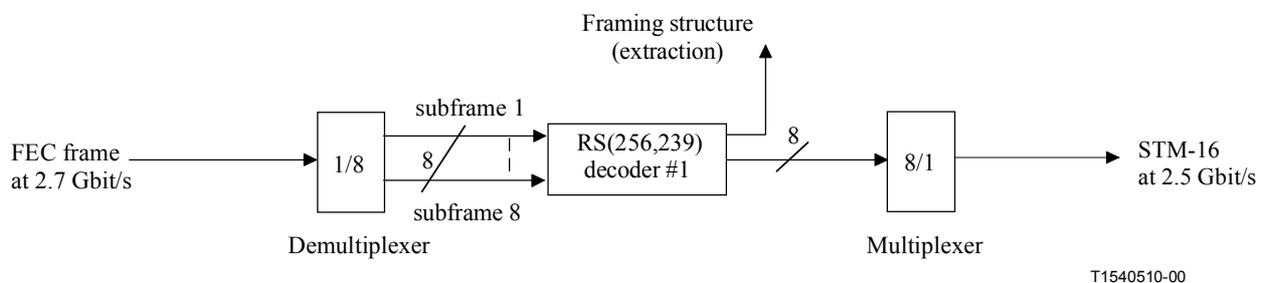


Figure I.2/G.975 – FEC decoder architecture for STM-64 systems

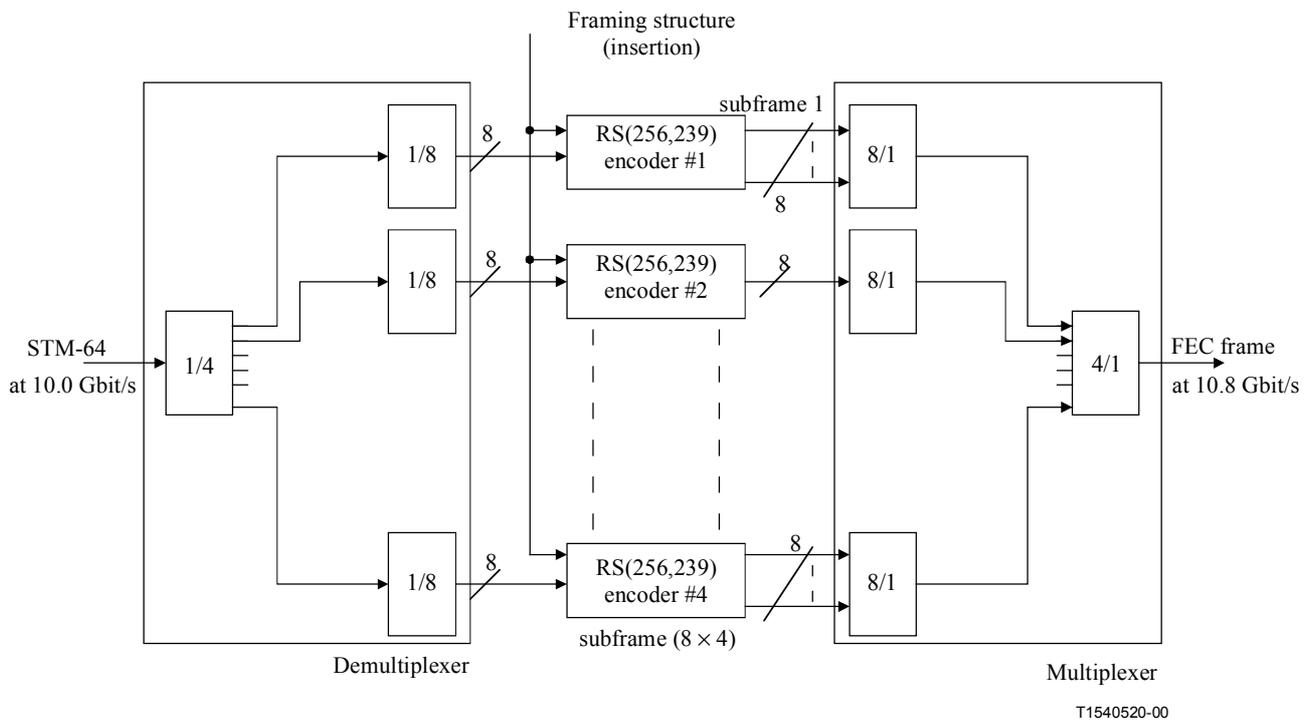


Figure I.3/G.975 – FEC encoder architecture for STM-64 systems

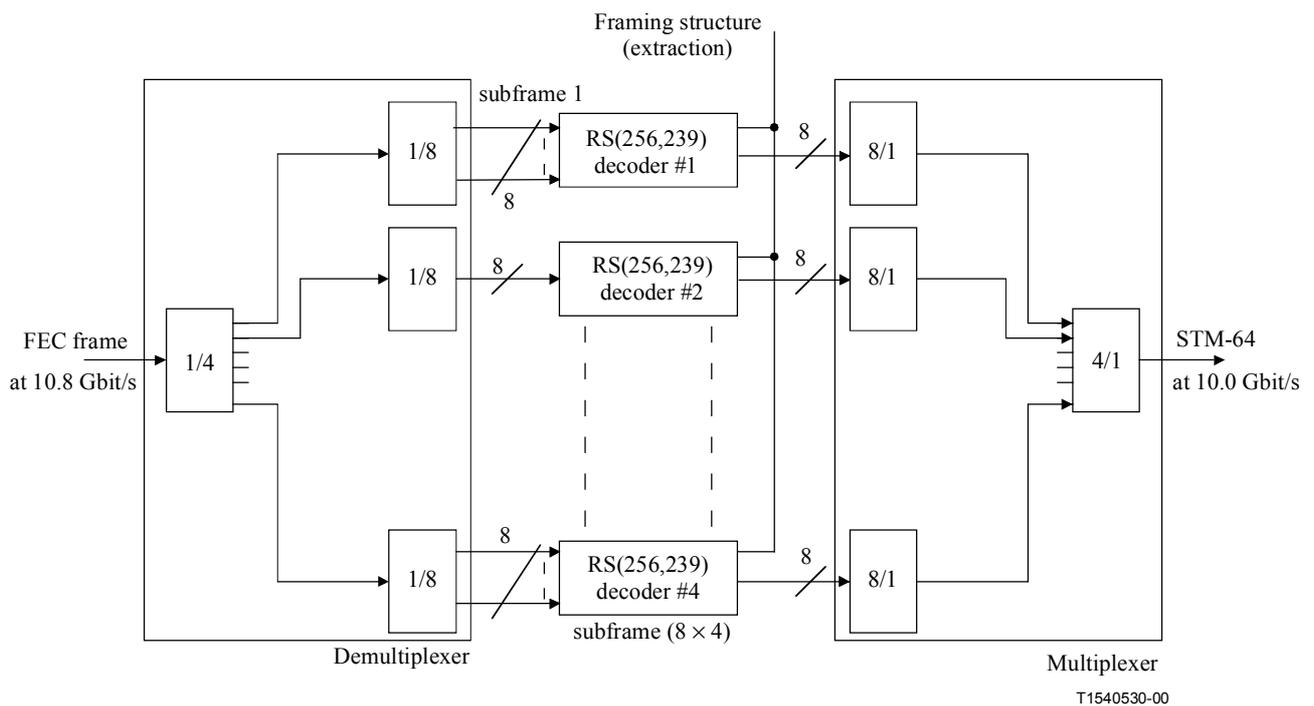


Figure I.4/G.975 – FEC decoder architecture for STM-64 systems

Based on the above FEC encoder/decoder architecture applied to STM-16 signals and STM-64 signals, the FEC frame constructions are respectively described in Figures I.5 and I.6.

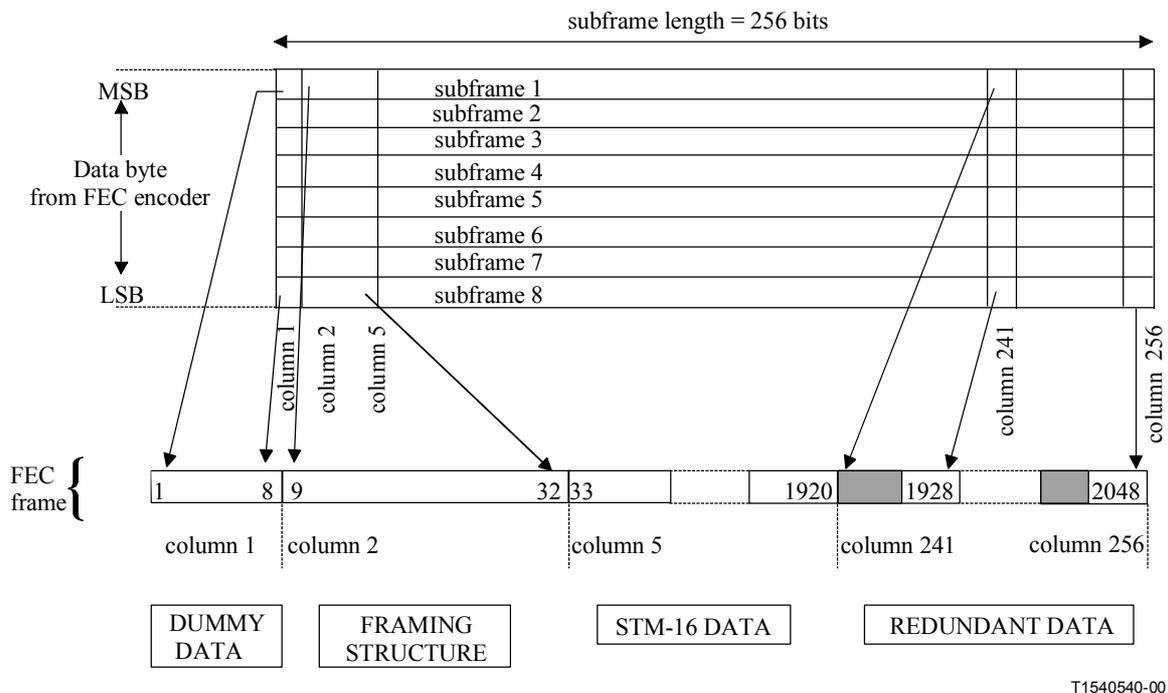


Figure I.5/G.975 – FEC frame construction for STM-16 systems

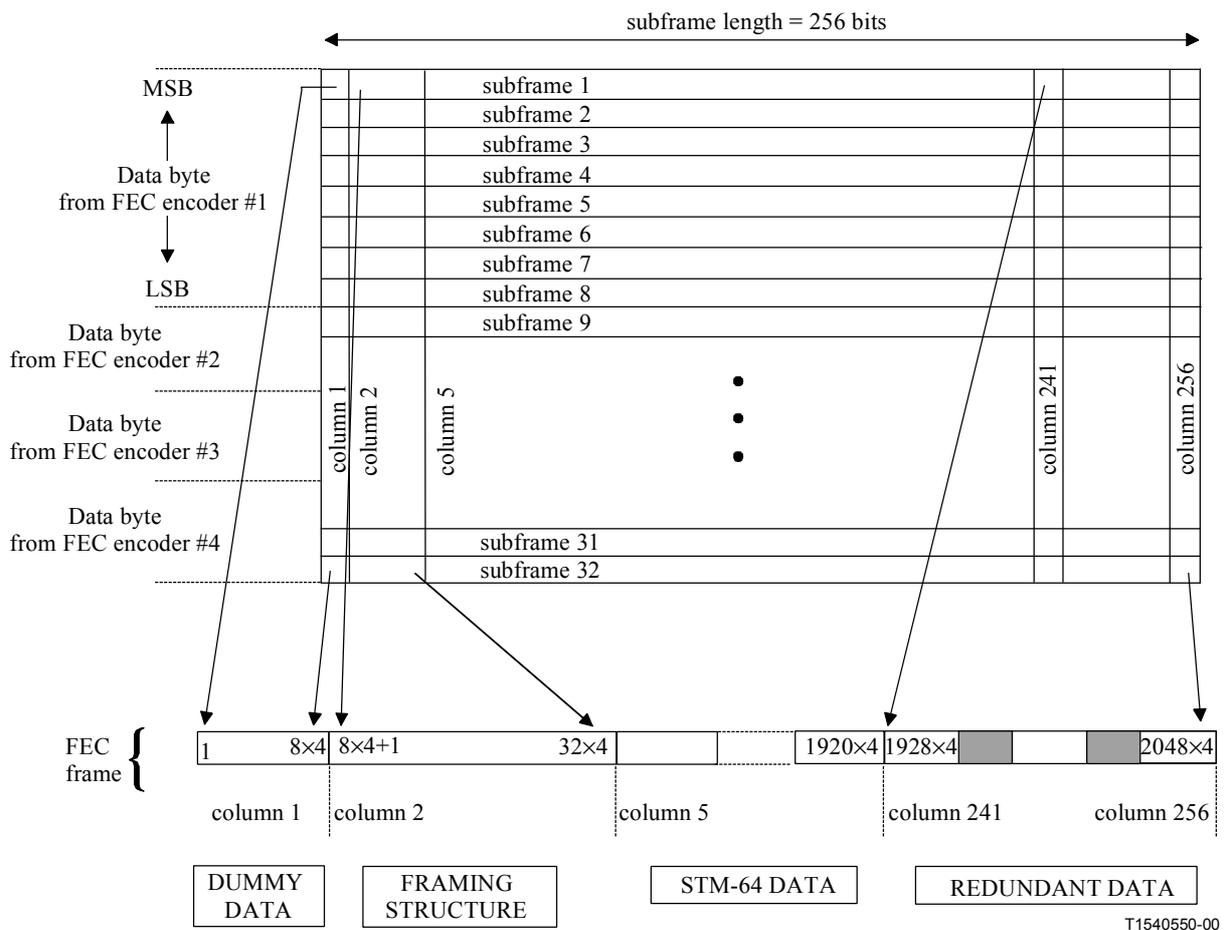


Figure I.6/G.975 – FEC frame construction for STM-64 systems

The FEC frames for STM-16 and STM-64 are 2048 bits long and 2048×4 bits long, and are respectively made of single-bit and four-bit interleaved subframes. The sequences of STM-16 data bits and STM-64 data bits within the FEC frame are respectively identical to those within the STM-16 and STM-64 input signal.

I.2 Framing structure

To achieve four-parallel calculation that allows to use a high-throughput FEC IC, one bit is added to the 255-bit subframe described in 6.4.2. Each of the resultant 256-bit subframe contains the following information:

- bit 1 is a dummy bit;
- bits 2-4 carry the framing structure (either the FEC Frame Alignment Word, the tributary markers for STM-16 data stream identification in systems carrying multiples of STM-16 (if required) or order wire channels or service channels for inter-terminal communication);
- bits 5 to 240 of each subframe carry the STM-16 information;
- bits 241 to 256 of each subframe carry the redundant bits, computed by the RS(255,239) algorithm.

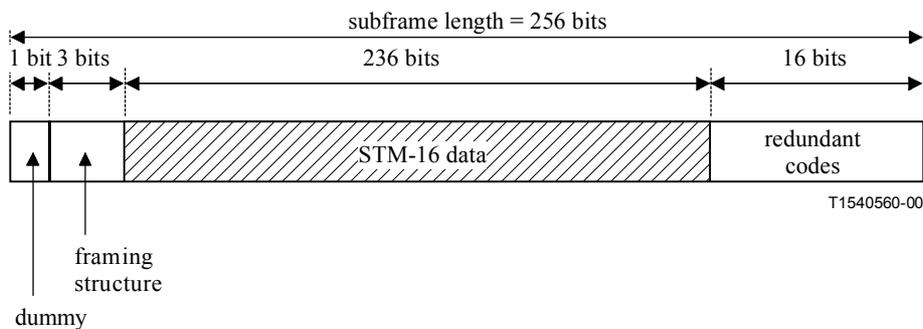


Figure I.7/G.975 – 256-bit subframe format that allows the parallel calculation

I.3 Scrambling

STM-16 data in the FEC frame are already scrambled as shown in ITU-T G.707 [1]. Hence, rescrambling of the FEC frame is not generally required.

However, when implemented in the submarine Terminal Transmission Equipment (TTE), the scrambling facility should possibly be inhibited.

The scrambling of the FEC frame may be carried out in the following procedure: the FEC frame is scrambled, with the exception of the framing structure bits of the FEC frame, by a $x^7 + x + 1$ polynomial initiated at each frame on the first bit which follows the framing structure in the FEC frame.

The first bits of the scrambler sequence are 1111111. Thereafter, the scrambler runs continuously throughout the complete FEC frame.

I.4 Redundancy ratio

The redundancy ratio of the FEC function described in Appendix I is equal to $5/59$. Consequently, the line bit rates of optical fibre submarine cable systems using the forward error correction feature described in Appendix I are as follows:

- one STM-16 tributary carried: $2\,488.320 \times 64/59$ Mbit/s;
- one STM-64 tributary carried: $9\,953.280 \times 64/59$ Mbit/s.

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