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**Timing characteristics of telecom grandmaster  
clocks for frequency synchronization**

Recommendation ITU-T G.8266/Y.1376

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## Recommendation ITU-T G.8266/Y.1376

### Timing characteristics of telecom grandmaster clocks for frequency synchronization

#### Summary

Recommendation ITU-T G.8266/Y.1376 specifies the requirements for packet master clocks suitable for frequency synchronization in packet networks.

#### History

Edition	Recommendation	Approval	Study Group	Unique ID*
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#### Keywords

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## Table of Contents

	<b>Page</b>
1 Scope.....	1
2 References.....	1
3 Definitions .....	2
4 Abbreviations and acronyms .....	2
5 Conventions .....	2
6 Frequency accuracy .....	2
7 Pull-in, hold-in, and pull-out ranges .....	2
8 Noise generation .....	2
8.1 Wander in locked mode.....	2
8.2 Jitter .....	3
9 Noise tolerance .....	3
9.1 Wander tolerance.....	3
9.2 Jitter tolerance .....	3
10 Noise transfer.....	3
11 Transient response and holdover performance .....	3
11.1 Short-term phase transient response.....	3
11.2 Long-term phase transient response (hold-over).....	4
11.3 Phase discontinuity.....	4
12 Interfaces.....	4
Annex A – Telecom Grandmaster clock functional model.....	5
Appendix I – Measurement method for wander transfer .....	7



# Recommendation ITU-T G.8266/Y.1376

## Timing characteristics of telecom grandmaster clocks for frequency synchronization

### 1 Scope

This Recommendation defines the minimum requirements for the timing functions of the telecom grandmaster clocks that operate in the network architecture as defined in [ITU-T G.8265] and the related profile defined in [ITU-T G.8265.1]. It supports frequency synchronization distribution when using packet-based methods. It allows for proper network operation when a telecom grandmaster clock is timed from a physical frequency source traceable to a primary reference clock (PRC) or primary reference time clock (PRTC) according to the architecture defined in [ITU-T G.803].

The case of a telecom grandmaster whose frequency is provided by an embedded PRTC or PRC function is for further study.

This Recommendation focuses on the types of networks corresponding to the HRM-1 and HRM-2 as defined in [ITU-T G.8261.1].

The HRM-2 type of network is for further study in the current version of this Recommendation. Other types of networks are out of the scope of this Recommendation.

This Recommendation includes clock accuracy, noise tolerance, noise transfer, noise generation, and holdover specifications for the telecom grandmaster clocks for frequency synchronization. These requirements apply under the normal environmental conditions specified for the equipment.

### 2 References

The following ITU-T Recommendations and other references contain provisions which, through reference in this text, constitute provisions of this Recommendation. At the time of publication, the editions indicated were valid. All Recommendations and other references are subject to revision; users of this Recommendation are therefore encouraged to investigate the possibility of applying the most recent edition of the Recommendations and other references listed below. A list of the currently valid ITU-T Recommendations is regularly published. The reference to a document within this Recommendation does not give it, as a stand-alone document, the status of a Recommendation.

- [ITU-T G.703] Recommendation ITU-T G.703 (2016), *Physical/electrical characteristics of hierarchical digital interfaces*.
- [ITU-T G.803] Recommendation ITU-T G.803 (2000), *Architecture of transport networks based on the synchronous digital hierarchy (SDH)*.
- [ITU-T G.810] Recommendation ITU-T G.810 (1996), *Definitions and terminology for synchronization networks*.
- [ITU-T G.812] Recommendation ITU-T G.812 (2004), *Timing requirements of slave clocks suitable for use as node clocks in synchronization networks*.
- [ITU-T G.8260] Recommendation ITU-T G.8260 (2015), *Definitions and terminology for synchronization in packet networks*.
- [ITU-T G.8261.1] Recommendation ITU-T G.8261.1 (2012), *Packet delay variation network limits applicable to packet based methods (Frequency synchronization)*.
- [ITU-T G.8262] Recommendation ITU-T G.8262 (2015), *Timing characteristics of a synchronous Ethernet equipment slave clock*.

- [ITU-T G.8265] Recommendation ITU-T G.8265 (2010), *Architecture and requirements for packet-based frequency delivery*.
- [ITU-T G.8265.1] Recommendation ITU-T G.8265.1 (2014), *Precision time protocol telecom profile for frequency synchronization*.

### **3 Definitions**

The terms and definitions used in this Recommendation are contained in [ITU-T G.810] and [ITU-T G.8260].

### **4 Abbreviations and acronyms**

This Recommendation uses the following abbreviations and acronyms:

GNSS	Global Navigation Satellite System
MTIE	Maximum Time Interval Error
NE	Network Element
PEC-M-F	Packet-based Equipment Clock – Master – Frequency
PRC	Primary Reference Clock
PRTC	Primary Reference Time Clock
PTP	Precision Time Protocol
SyncE	Synchronous Ethernet
TDEV	Time Deviation
ToD	Time of Day

### **5 Conventions**

The telecom grandmaster clock for frequency synchronization (PEC-M-F) is based on [ITU-T G.812]. The packet-based equipment clock – master – frequency (PEC-M-F) conforms to one of [ITU-T G.812] Type I, Type II or Type III specifications with additional requirements or modifications as listed in this recommendation:

- A PEC-M-F Type I is based on [ITU-T G.812] Type I
- A PEC-M-F Type II is based on [ITU-T G.812] Type II
- A PEC-M-F Type III is based on [ITU-T G.812] Type III.

### **6 Frequency accuracy**

The PEC-M-F should comply with [ITU-T G.812] clause 6 for its respective clock Type.

### **7 Pull-in, hold-in, and pull-out ranges**

The PEC-M-F should comply with [ITU-T G.812] clause 7 for its respective clock Type.

### **8 Noise generation**

#### **8.1 Wander in locked mode**

The PEC-M-F should comply with [ITU-T G.812] clause 8.1 for its respective clock Type.

The PEC-M-F PTP port output is measured through a first order low-pass filter with bandwidth of 0.1 Hz, and only observation intervals above 1s are considered.

## **8.2 Jitter**

A PEC-M-F operating in a locked mode synchronized to a noise-free frequency reference at the SyncE/SDH input, should comply with:

- [ITU-T G.812] clause 8.3.1, when measured at a 2048 kHz or 2048 kbit/s output interface
- [ITU-T G.812] clause 8.3.2, when measured at a 1544 kbit/s output interface
- Peak-to-peak time error of less than 70 ns, when measured at a precision time protocol (PTP) output interface through first-order high-pass filter of 0.1 Hz over a measurement interval of 1000 s.

## **9 Noise tolerance**

### **9.1 Wander tolerance**

The measurement method of clause 9.1.3 is recommended for wander tolerance testing.

#### **9.1.1 MTIE wander tolerance**

The PEC-M-F complies with [ITU-T G.812] clause 9.1.1 for its respective clock Type.

#### **9.1.2 TDEV wander tolerance**

The PEC-M-F complies with [ITU-T G.812] clause 9.1.2 for its respective clock Type.

#### **9.1.3 Sinusoidal wander tolerance**

The PEC-M-F complies with [ITU-T G.812] clause 9.1.3 for its respective clock Type.

### **9.2 Jitter tolerance**

The PEC-M-F should comply with the jitter tolerance requirements of:

- [ITU-T G.812] clause 9.2.1, applied at a 2048 kHz or 2048 kbit/s input interface
- [ITU-T G.812] clause 9.2.2, applied at a 1544 kbit/s input interface
- [ITU-T G.8262] clause 9.2, applied at a synchronous Ethernet input interface.

## **10 Noise transfer**

The PEC-M-F should comply with [ITU-T G.812] clause 10 (a) for its respective clock Type.

Guidance on the measurement techniques for these requirements is given in Appendix I.

NOTE – The noise transfer measurement technique must account for the allowed noise generation (e.g., wander and timestamp noise) of the clock.

## **11 Transient response and holdover performance**

In each of the following clauses, the PEC-M-F PTP output interface is measured through a first order low-pass filter with bandwidth of 0.1 Hz, and only observation intervals above 1 s are considered.

### **11.1 Short-term phase transient response**

If the frequency reference of a PEC-M-F Type I clock is a physical layer clock, the output 2048 kHz and 2048 kbit/s interfaces should comply with the requirements of [ITU-T G.812] clause 11.1.1. The output PTP interface should also comply with the 2048 kHz and 2048 kbit/s requirements of

[ITU-T G.812] clause 11.1.1. The output SyncE interface should comply with the STM-N requirements of [ITU-T G.812] clause 11.1.2.

If the frequency reference of a PEC-M-F Type II or Type III clock is a physical layer clock, the output 1544 kbit/s interface should comply with the requirements of [ITU-T G.812] clause 11.1.3. The output PTP interface should also comply with the 1544 kbit/s requirements of [ITU-T G.812] clause 11.1.3. The output SyncE interface should comply with the STM-N requirements of [ITU-T G.812] clause 11.1.4.

### **11.2 Long-term phase transient response (hold-over)**

The PEC-M-F should comply with [ITU-T G.812] clause 11.2 for its respective clock Type.

### **11.3 Phase discontinuity**

The PEC-M-F should comply with [ITU-T G.812] clause 11.4 for its respective clock Type.

## **12 Interfaces**

The requirements in this Recommendation are related to reference points internal to the network elements (NEs) in which the clock is embedded and are therefore not necessarily available for measurement or analysis by the user. Therefore the performance of the [ITU-T G.812] clock is not defined at these internal reference points, but rather at the external interfaces of the equipment.

Note that not all of the interfaces below need to be implemented on all equipment.

The external input and output are:

- 2 048 kHz interfaces according to [ITU-T G.703] with additional jitter and wander requirements as specified herein;
- 2 048 kbit/s interfaces according to [ITU-T G.703] with additional jitter and wander requirements as specified herein;
- 1 544 kbit/s interfaces according to [ITU-T G.703] with additional jitter and wander requirements as specified herein;
- Ethernet interface carrying PTP messages;  
NOTE 1 – This interface is only used for output.
- Synchronous Ethernet (SyncE) interfaces;  
NOTE 2 – Ethernet interfaces can combine Synchronous Ethernet for frequency and PTP messages.

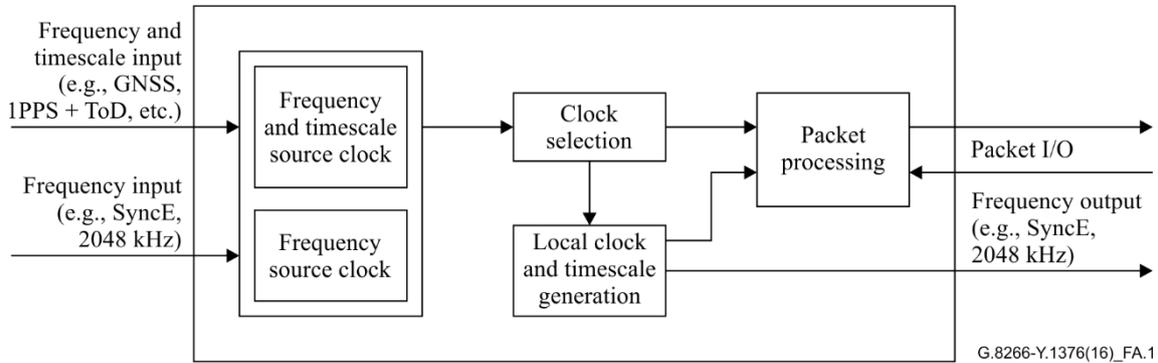
Other interfaces are for further study.

## Annex A

### Telecom Grandmaster clock functional model

(This annex forms an integral part of this Recommendation.)

Figure A.1 illustrates a Telecom Grandmaster clock functional model.



**Figure A.1 – PEC-M functional model**

NOTE 1 – This diagram is a functional model. It is not intended to specify any specific implementation. Any implementation specific detail is outside the scope of this Recommendation.

NOTE 2 – Not all interfaces are mandatory to be present in an implementation.

NOTE 3 – Not all functional blocks are mandatory to be present in an implementation.

Table A.1 describes the functionality of the blocks depicted in Figure A.1.

**Table A.1 – Proposed clock model functional blocks**

Functional block label	Functionality
Frequency & Timescale Source Clock	Synchronizes to a frequency & timescale input reference (e.g., GNSS, 1PPS and ToD, etc.) and provides this as a reference to the clock selection block.
Frequency Source Clock	Synchronizes to a frequency input reference (e.g., SyncE, 2048 kHz, etc.) and provides this as a reference to the clock selection block.
Clock Selection	Selects the local source clock to be used, from amongst the various possible available inputs. The selected local source clock is input to the local timescale generation block.
Local Clock & Timescale Generation	Generates the local clock and time scale of the clock using the selected local source clock information supplied by the clock selection block. Provides the local time scale information to the Packet Processing block. NOTE – Timescale generation is only used for the PTP output.
Packet Processing	Acting as a packet master clock (PTP master), will process ingress PTP packets and generate egress PTP packets according to the PTP profile. Timestamps PTP packets using the local timescale generation information, where required by the PTP profile.

Table A.2 describes the functionality of the interfaces depicted in Figure A.1.

**Table A.2 – Proposed clock model external interfaces**

External interfaces label	Functionality
Packet I/O	Packet interface to transmit and receive PTP packets from the packet processing block. It will interact with external PTP slave devices.
Frequency & Timescale Input (e.g., GNSS, 1PPS+ToD, etc.)	Interface to receive information from applicable interfaces available on the clock (e.g., GNSS, 1PPS input and ToD serial time information, etc.). Provides timing information to the frequency & timescale source clock block.
Frequency Input (e.g., SyncE, 2048 kHz)	Frequency input from applicable interfaces available on the clock (e.g., SyncE, [ITU-T G.703], 2048 kHz). Provides frequency information to the frequency source clock block.
Frequency Output (e.g., SyncE, 2048 kHz)	Frequency output (e.g., SyncE, [ITU-T G.703], 2048 kHz) provides frequency information. Generated by the local timescale generation block.

## **Appendix I**

### **Measurement method for wander transfer**

(This appendix does not form an integral part of this Recommendation.)

The clock specification in [ITU-T G.812] used wideband noise, defined as a TDEV mask, as an input stimulus to measure the wander transfer of a clock. However, this method is unsuitable for use with a packet interface because the timestamp noise is likely to be larger than the 2% margin allowed between the input and output masks.

For this Recommendation it is proposed to use sinusoidal tones at different frequencies across the frequency response of the clock. Such tones can be generated very accurately and therefore can be used for measuring gain-peaking, provided the noise generation (e.g., wander and timestamp noise) is properly considered.

[ITU-T G.812] only defines the bandwidth and the gain peaking of the filter, and does not define the response at frequencies above the nominal bandwidth. The choice of sinusoidal tone frequencies, input signal amplitudes, and associated output signal amplitudes are for further study.



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Interfaces and protocols	Y.400–Y.499
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General	Y.1000–Y.1099
Services and applications	Y.1100–Y.1199
Architecture, access, network capabilities and resource management	Y.1200–Y.1299
<b>Transport</b>	<b>Y.1300–Y.1399</b>
Interworking	Y.1400–Y.1499
Quality of service and network performance	Y.1500–Y.1599
Signalling	Y.1600–Y.1699
Operation, administration and maintenance	Y.1700–Y.1799
Charging	Y.1800–Y.1899
IPTV over NGN	Y.1900–Y.1999
<b>NEXT GENERATION NETWORKS</b>	
Frameworks and functional architecture models	Y.2000–Y.2099
Quality of Service and performance	Y.2100–Y.2199
Service aspects: Service capabilities and service architecture	Y.2200–Y.2249
Service aspects: Interoperability of services and networks in NGN	Y.2250–Y.2299
Enhancements to NGN	Y.2300–Y.2399
Network management	Y.2400–Y.2499
Network control architectures and protocols	Y.2500–Y.2599
Packet-based Networks	Y.2600–Y.2699
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Generalized mobility	Y.2800–Y.2899
Carrier grade open environment	Y.2900–Y.2999
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<b>CLOUD COMPUTING</b>	<b>Y.3500–Y.3999</b>
<b>INTERNET OF THINGS AND SMART CITIES AND COMMUNITIES</b>	
General	Y.4000–Y.4049
Definitions and terminologies	Y.4050–Y.4099
Requirements and use cases	Y.4100–Y.4249
Infrastructure, connectivity and networks	Y.4250–Y.4399
Frameworks, architectures and protocols	Y.4400–Y.4549
Services, applications, computation and data processing	Y.4550–Y.4699
Management, control and performance	Y.4700–Y.4799
Identification and security	Y.4800–Y.4899
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