ITU-T

G.8264/Y.1364

TELECOMMUNICATION STANDARDIZATION SECTOR OF ITU **Amendment 2** (04/2016)

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Amendment 2

Recommendation ITU-T G.8264/Y.1364 (2014) – Amendment 2



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Distribution of timing information through packet networks

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Summary

Amendment 2 to Recommendation ITU-T G.8264/Y.1364 (2014) adds additional text to cover extended ESMC messages.

History

Edition	Recommendation	Approval	Study Group	Unique ID*
1.0	ITU-T G.8264/Y.1364	2008-10-29	15	11.1002/1000/9420
1.1	ITU-T G.8264/Y.1364 (2008) Cor. 1	2009-11-13	15	11.1002/1000/10433
1.2	ITU-T G.8264/Y.1364 (2008) Amd. 1	2010-09-22	15	11.1002/1000/10927
1.3	ITU-T G.8264/Y.1364 (2008) Cor. 2	2012-02-13	15	11.1002/1000/11526
1.4	ITU-T G.8264/Y.1364 (2008) Amd. 2	2012-02-13	15	11.1002/1000/11525
2.0	ITU-T G.8264/Y.1364	2014-05-14	15	11.1002/1000/12192
2.1	ITU-T G.8264/Y.1364 (2014) Amd. 1	2015-01-13	15	11.1002/1000/12390
2.2	ITU-T G.8264/Y.1364 (2014) Amd. 2	2016-04-13	15	11.1002/1000/12810

^{*} To access the Recommendation, type the URL http://handle.itu.int/ in the address field of your web browser, followed by the Recommendation's unique ID. For example, http://handle.itu.int/11.1002/1000/11830-en.

FOREWORD

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The World Telecommunication Standardization Assembly (WTSA), which meets every four years, establishes the topics for study by the ITU-T study groups which, in turn, produce Recommendations on these topics.

The approval of ITU-T Recommendations is covered by the procedure laid down in WTSA Resolution 1.

In some areas of information technology which fall within ITU-T's purview, the necessary standards are prepared on a collaborative basis with ISO and IEC.

NOTE

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Recommendation ITU-T G.8264/Y.1364

Distribution of timing information through packet networks

Amendment 2

1) Additions to clause 4, Abbreviations and acronyms

Add the following acronyms to the existing list:

eEEC Enhanced Ethernet Equipment Clock

ePRTC Enhanced Primary Reference Time Clock

PRTC Primary Reference Time Clock

SyncE Synchronous Ethernet

2) Changes to clause 11.3.1, ESMC Format

Add the following text to the end of clause 11.3.1:

An NE must discard and not forward upon reception any TLV within the ESMC PDU that it does not recognize.

3) Insertion of new clauses 11.3.1.3 and 11.3.1.4

Insert the following new clauses after clause 11.3.1.2:

11.3.1.3 Extended QL TLV format

In order to support new clocks and added functionality, an extended QL TLV is defined in Table 11-4.1. This TLV is twenty bytes in length and supports the information contained in the specified fields. The related format is in Table 11-4.2, and the new clock quality levels are defined in Table 11-4.3. This supports new clock types as well as carrying new information.

Table 11-4.1 – Extended QL TLV

Octet number	Size/bits	Field
1	8 bits	Type: 0x02
2-3	16 bits	Length: 0x0014
4-20	136 bits	Extended SSM (see Table 11-4.2)

Table 11-4.2 – Extended SSM format

Octet number	Size/bits	Field
1	8 bits	Enhanced SSM code (see Table 11-4.3)
2-9	64 bits	SyncE Master ID, Note 1
10	8 bits	Flag; Note 2
11	8 bits	Number of cascaded eEECs from the nearest SSU/PRC
12	8 bits	Number of cascaded EECs from the nearest SSU/PRC
13-17	40 bits	Reserved for future use

NOTE 1 – The SyncE Master ID is for further study.

NOTE 2 – bit 0 means mixed EEC/eEEC (i.e., 1 if at least one of the clocks is not an eEEC; 0 if all clocks are eEEC); bit 1 means partial chain (i.e., 1, if the TLV has been generated in the middle of the chain and the count of the EEC/eEEC is incomplete); bits 2-7 reserved for future use. See also clause 11.3.1.4.

Table 11-4.3 – Enhanced SSM codes for SyncE

Clock	Message	SSM code
As per [ITU-T G.781]/ [ITU-T G.8264]	QL as per [ITU-T G.781]/ [ITU-T G.8264] (refer to the QL TLV)	0xFF
PRTC	QL-PRTC	0x20
ePRTC	QL-ePRTC	0x21
eEEC	QL-eEEC	0x22

11.3.1.4 Interworking between different SyncE generations

While the Extended QL TLV is developed for use with the eEEC, the basic mechanism could be applied in future to the older EEC. This results in three possible combinations of clocks that need to be considered; eEEC with Extended QL TLV support, EEC with no Extended QL TLV support, and the EEC with Extended QL TLV support.

In case of already deployed nodes not supporting the Extended QL TLV, interworking between different generations of synchronous Ethernet is achieved by the fact that a network element must discard and not forward upon reception any TLV within the ESMC PDU that it does not recognize.

The Extended QL TLV allows the count of the number of cascaded eEEC and EEC clocks. If a clock not supporting the Extended QL TLV is present within a chain of clocks, discarding the TLV, as noted above, will result in incomplete counts. The Extended QL TLV specifies a flag to allow clocks supporting enhanced ESMC messages to have the ability to report the presence of clocks that may have discarded TLVs.

As an example, in case of a chain of eEECs, Bit 0 and Bit 1 will both be "0" at the output of the chain indicating that the syncE chain is fully based on eEECs, and the count of clocks is complete.

In case of an intermediate EEC, not able to process the Extended QL TLV, the EEC shall drop the TLV. At the next eEEC in the SyncE chain, the TLV will be added with both Bit 0 and Bit 1 set to "1" to indicate that the SyncE chain is not fully based on eEEC, and the count of clocks is not complete.

However, in case of an intermediate EEC that is able to process the Extended QL TLV, at the output of that EEC bit 0 is set to 1 to indicate that the SyncE chain includes a mix of EEC and eEEC, and Bit 1 is set to 0 to indicate that the count of the clocks in the chain is complete.

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