

International Telecommunication Union

**ITU-T**

**G.8121/Y.1381**

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STANDARDIZATION SECTOR  
OF ITU

(11/2013)

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## **Characteristics of MPLS-TP equipment functional blocks**

Recommendation ITU-T G.8121/Y.1381



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## Recommendation ITU-T G.8121/Y.1381

### Characteristics of MPLS-TP equipment functional blocks

#### Summary

Recommendation ITU-T G.8121/Y.1381 specifies both the functional components and the methodology that should be used in order to specify the multi-protocol label switching transport profile (MPLS-TP) layer network functionality of network elements. It does not specify individual MPLS-TP network equipment.

#### History

Edition	Recommendation	Approval	Study Group	Unique ID*
1.0	ITU-T G.8121/Y.1381	2006-03-29	15	<a href="http://handle.itu.int/11.1002/1000/8785">11.1002/1000/8785</a>
1.1	ITU-T G.8121/Y.1381 (2006) Cor. 1	2006-12-14	15	<a href="http://handle.itu.int/11.1002/1000/9007">11.1002/1000/9007</a>
1.2	ITU-T G.8121/Y.1381 (2006) Amd. 1	2007-10-22	15	<a href="http://handle.itu.int/11.1002/1000/9178">11.1002/1000/9178</a>
2.0	ITU-T G.8121/Y.1381	2012-09-21	15	<a href="http://handle.itu.int/11.1002/1000/11517">11.1002/1000/11517</a>
2.1	ITU-T G.8121/Y.1381 (2012) Amd. 1	2012-12-22	15	<a href="http://handle.itu.int/11.1002/1000/11800">11.1002/1000/11800</a>
3.0	ITU-T G.8121/Y.1381	2013-11-06	15	<a href="http://handle.itu.int/11.1002/1000/12020">11.1002/1000/12020</a>

#### Keywords

Atomic functions, equipment functional blocks, MPLS-TP layer network, MPLS-TP.

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# Recommendation ITU-T G.8121/Y.1381

## Characteristics of MPLS-TP equipment functional blocks<sup>1</sup>

### 1 Scope

This Recommendation describes both the functional components and the methodology that should be used in order to describe the multi-protocol label switching transport profile (MPLS-TP) layer network functionality of network elements. It does not describe individual MPLS-TP network equipment.

This Recommendation provides a representation of MPLS-TP technology which uses the methodologies that have been used for other transport technologies (e.g., synchronous digital hierarchy (SDH), optical transport network (OTN) and Ethernet).<sup>2</sup>

This Recommendation forms part of a suite of Recommendations covering the full functionality of network equipment. These Recommendations are [ITU-T G.806], [ITU-T G.798], [ITU-T G.783], [ITU-T G.705] and [ITU-T G.8021]. This Recommendation also follows the principles defined in [ITU-T G.805].

These Recommendations specify a library of basic building blocks and a set of rules by which they may be combined in order to describe digital transmission equipment. The library comprises the functional building blocks needed to specify completely the generic functional structure of the MPLS-TP layer network. In order to be compliant with this Recommendation, equipment needs to be describable as an interconnection of a subset of these functional blocks contained within this Recommendation. The interconnections of these blocks should obey the combination rules given.

Not every atomic function defined in this Recommendation is required for every application. Different subsets of atomic functions may be assembled in different ways according to the combination rules given in this Recommendation to provide a variety of different capabilities.

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<sup>1</sup> Cisco Systems has expressed concerns that in the event of a difference between this ITU-T Recommendation and any of the normatively referenced IETF RFCs, interoperability issues may arise. To prevent interoperability issues, the behaviour defined in the IETF RFCs must be maintained, and any such differences must be resolved in coordination with the IETF in a timely manner.

France Telecom Orange has expressed concerns that in the event of a difference between this ITU-T Recommendation and any of the normatively referenced IETF RFCs, interoperability issues may arise. To prevent interoperability issues, the behaviour defined in the IETF RFCs must be maintained, and any such differences must be resolved in coordination with the IETF in a timely manner.

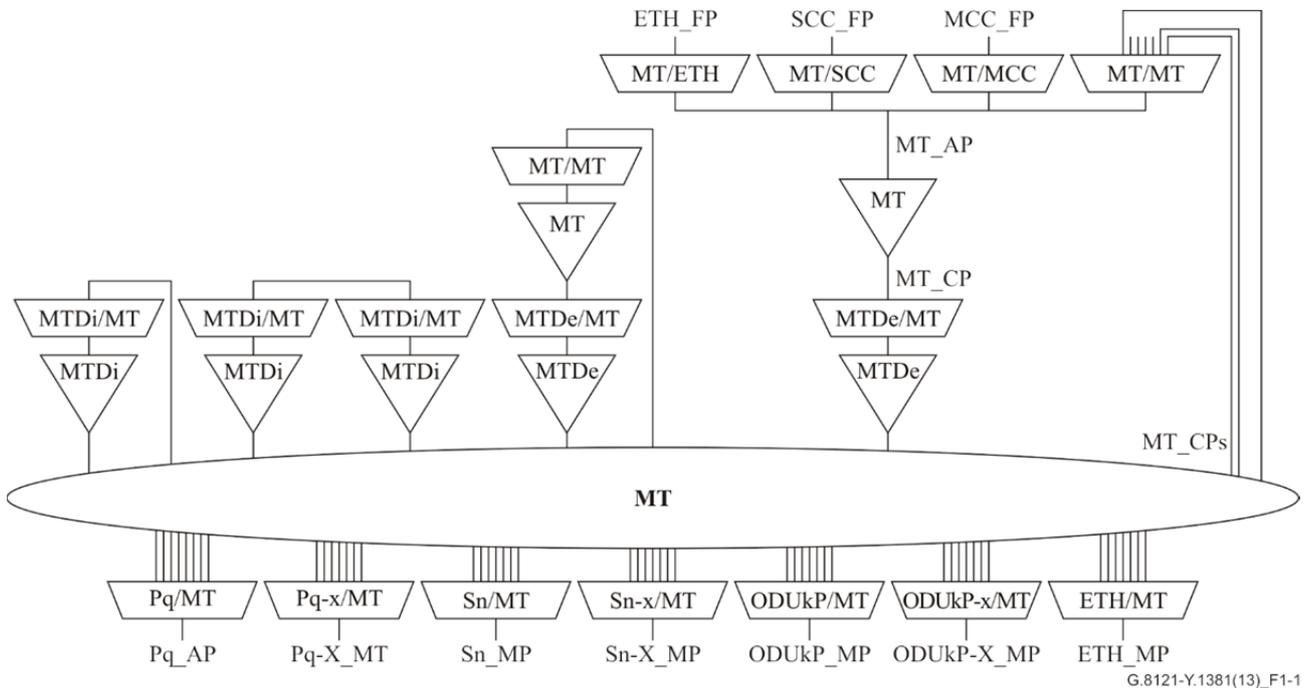
Nokia Siemens Networks (NSN) is concerned that, in the event of a difference between this ITU-T Recommendation and any of the IETF RFCs to which it makes normative reference, interoperability issues may arise. This Recommendation contains no guidance on how to resolve such a situation. NSN believes that in the case of such an event, differences should be resolved in coordination with the IETF and that the behaviour specified in the RFC(s) must be maintained.

Verizon Communications has expressed concerns that in the event of a difference between this ITU-T Recommendation and the behaviour defined in the normatively referenced IETF RFCs, interoperability issues may arise. There is no guidance in this ITU-T Recommendation that describes how to address differences in behaviour between the Recommendation and the normatively referenced IETF RFCs. Verizon Communications feels that any difference should be resolved in coordination with the IETF in a timely manner and until the issue is resolved, the behaviour defined in the IETF RFCs should be maintained.

<sup>2</sup> This ITU-T Recommendation is intended to be aligned with the IETF MPLS RFCs normatively referenced by this Recommendation.

Network operators and equipment suppliers may choose which functions must be implemented for each application.

Figure 1 presents the set of atomic functions associated with the traffic signal transport. The functions are based on the functional architecture described in [ITU-T G.8110.1]. It is noted that this Recommendation only defines Ethernet for the client of MPLS-TP as an MT/ETH adaptation function.



**Figure 1-1 – MPLS-TP atomic functions**

## 2 References

The following ITU-T Recommendations and other references contain provisions which, through reference in this text, constitute provisions of this Recommendation. At the time of publication, the editions indicated were valid. All Recommendations and other references are subject to revision; users of this Recommendation are therefore encouraged to investigate the possibility of applying the most recent edition of the Recommendations and other references listed below. A list of the currently valid ITU-T Recommendations is regularly published. The reference to a document within this Recommendation does not give it, as a stand-alone document, the status of a Recommendation.

- [ITU-T G.704] Recommendation ITU-T G.704 (1998), *Synchronous frame structures used at 1544, 6312, 2048, 8448 and 44 736 kbit/s hierarchical levels.*
- [ITU-T G.705] Recommendation ITU-T G.705 (2000), *Characteristics of plesiochronous digital hierarchy (PDH) equipment functional blocks.*
- [ITU-T G.707] Recommendation ITU-T G.707/Y.1322 (2003), *Network node interface for the synchronous digital hierarchy (SDH).*
- [ITU-T G.709] Recommendation ITU-T G.709/Y.1331 (2009), *Interfaces for the Optical Transport Network (OTN).*
- [ITU-T G.780] Recommendation ITU-T G.780/Y.1351 (2004), *Terms and definitions for synchronous digital hierarchy (SDH) networks.*
- [ITU-T G.783] Recommendation ITU-T G.783 (2006), *Characteristics of synchronous digital hierarchy (SDH) equipment functional blocks.*

- [ITU-T G.798] Recommendation ITU-T G.798 (2012), *Characteristics of optical transport network hierarchy equipment functional blocks.*
- [ITU-T G.805] Recommendation ITU-T G.805 (2000), *Generic functional architecture of transport networks.*
- [ITU-T G.806] Recommendation ITU-T G.806 (2012), *Characteristics of transport equipment – Description methodology and generic functionality.*
- [ITU-T G.832] Recommendation ITU-T G.832 (1998), *Transport of SDH elements on PDH networks – Frame and multiplexing structures.*
- [ITU-T G.7041] Recommendation ITU-T G.7041/Y.1303 (2011), *Generic framing procedure.*
- [ITU-T G.7043] Recommendation ITU-T G.7043/Y.1343 (2004), *Virtual concatenation of plesiochronous digital hierarchy (PDH) signals.*
- [ITU-T G.7044] Recommendation ITU-T G.7044/Y.1347 (2011), *Hitless adjustment of ODUflex(GFP).*
- [ITU-T G.7712] Recommendation ITU-T G.7712/Y.1703 (2010), *Architecture and specification of data communication network.*
- [ITU-T G.8021] Recommendation ITU-T G.8021/Y.1341 (2012), *Characteristics of Ethernet transport network equipment functional blocks.*
- [ITU-T G.8040] Recommendation ITU-T G.8040/Y.1340 (2005), *GFP frame mapping into Plesiochronous Digital Hierarchy (PDH).*
- [ITU-T G.8101] Recommendation ITU-T G.8101/Y.1355 (2013), *Terms and definitions for MPLS Transport profile.*
- [ITU-T G.8110.1] Recommendation ITU-T G.8110.1/Y.1370.1 (2011), *Architecture of the Multi-Protocol Label Switching transport profile layer network.*
- [ITU-T G.8251] Recommendation ITU-T G.8251 (2010), *The control of jitter and wander within the optical transport network (OTN).*
- [ITU-T Y.1415] Recommendation ITU-T Y.1415 (2005), *Ethernet-MPLS network interworking – User plane interworking.*
- [IETF RFC 4448] IETF RFC 4448 (2006), *Encapsulation Methods for Transport of Ethernet over MPLS Networks.*
- [IETF RFC 4720] IETF RFC 4720 (2006), *Pseudowire Emulation Edge-to-Edge (PWE3) – Frame Check Sequence Retention.*
- [IETF RFC 5332] IETF RFC 5332 (2008), *MPLS Multicast Encapsulations.*
- [IETF RFC 5586] IETF RFC 5586 (2009), *MPLS Generic Associated Channel.*
- [IETF RFC 5718] IETF RFC 5718 (2010), *An In-band Data Communication Network For the MPLS Transport Profile.*
- [IETF RFC 6371] IETF RFC 6371 (2011), *Operations, Administration and Maintenance Framework for MPLS-Based Transport Networks.*
- [IETF RFC 6435] IETF RFC 6435 (2011), *MPLS Transport Profile Lock Instruct and Loopback Functions*, plus Errata 3429 (2013).

### 3 Definitions

#### 3.1 Terms defined elsewhere

This Recommendation uses the following terms defined elsewhere:

- 3.1.1 **access point:** [ITU-T G.805]
- 3.1.2 **adapted information:** [ITU-T G.805]
- 3.1.3 **associated channel header:** [ITU-T G.8101]
- 3.1.4 **bottom of stack:** [ITU-T G.8101]
- 3.1.5 **characteristic information:** [ITU-T G.805]
- 3.1.6 **client/server relationship:** [ITU-T G.805]
- 3.1.7 **connection:** [ITU-T G.805]
- 3.1.8 **connection point:** [ITU-T G.805]
- 3.1.9 **explicitly TC-encoded-PSC LSP:** [ITU-T G.8101]
- 3.1.10 **G-ACh label:** [ITU-T G.8101]
- 3.1.11 **generic associated channel:** [ITU-T G.8101]
- 3.1.12 **label:** [ITU-T G.8101]
- 3.1.13 **label inferred PHB scheduling class LSP:** [ITU-T G.8101]
- 3.1.14 **label stack:** [ITU-T G.8101]
- 3.1.15 **label switched path:** [ITU-T G.8101]
- 3.1.16 **label value:** [ITU-T G.8101]
- 3.1.17 **layer network:** [ITU-T G.805]
- 3.1.18 **matrix:** [ITU-T G.805]
- 3.1.19 **MPLS label stack:** [ITU-T G.8101]
- 3.1.20 **network:** [ITU-T G.805]
- 3.1.21 **network connection:** [ITU-T G.805]
- 3.1.22 **per-hop behaviour:** [ITU-T G.8101]
- 3.1.23 **reference point:** [ITU-T G.805]
- 3.1.24 **subnetwork:** [ITU-T G.805]
- 3.1.25 **subnetwork connection:** [ITU-T G.805]
- 3.1.26 **termination connection point:** [ITU-T G.805]
- 3.1.27 **time to live:** [ITU-T G.8101]
- 3.1.28 **traffic class:** [ITU-T G.8101]
- 3.1.29 **trail:** [ITU-T G.805]
- 3.1.30 **trail termination:** [ITU-T G.805]
- 3.1.31 **transport:** [ITU-T G.805]
- 3.1.32 **transport entity:** [ITU-T G.805]
- 3.1.33 **transport processing function:** [ITU-T G.805]

**3.1.34 unidirectional connection:** [ITU-T G.805]

**3.1.35 unidirectional trail:** [ITU-T G.805]

## **3.2 Terms defined in this Recommendation**

None.

## **4 Abbreviations and acronyms**

This Recommendation uses the following abbreviations and acronyms:

ACH	Associated Channel Header
AI	Adapted Information
AIS	Alarm Indication Signal
AP	Access Point
APS	Automatic Protection Switching
CC	Continuity Check
CC/CV	Continuity Check or Connectivity Verification
CC-V	Continuity Check and Connectivity Verification
CI	Characteristic Information
CII	Common Interworking Indicator
CoS	Class of Service
CP	Connection Point
CSF	Client Signal Fail
CV	Connectivity Verification
CW	Control Word
DM	Delay Measurement
DP	Drop Precedence
DT	Diagnostic Test
ECC	Embedded Communication Channel
E-LSP	Explicitly TC-encoded-PSC LSP
EMF	Equipment Management Function
ETH	Ethernet MAC layer network
ETY	Ethernet PHY layer network
FDI	Forward Defect Indication
FP	Flow Point
FTP	Flow Termination Point
G-ACh	Generic Associated Channel
GAL	G-ACh Label
GFP	Generic Framing Procedure
HAO	Hitless Adjustment of ODUflex

LCAS	Link Capacity Adjustment Scheme
LCK	Locked
LKI	Lock Instruct
LKR	Lock Report
L-LSP	Label-only-inferred PSC LSP
LM	Loss Measurement
LOS	Loss of Signal
LSP	Label Switched Path
LStack	Label Stack
MCC	Maintenance Communication Channel
MEG	Maintenance Entity Group
MEP	Maintenance entity group (MEG) End Point
MIP	Maintenance entity group (MEG) Intermediate Point
MP	Management Point
MPLS	Multi-Protocol Label Switching
MPLS-TP	Multi-Protocol Label Switching – Transport Profile
MT	Multi-Protocol Label Switching – Transport Profile
MTDe	MPLS-TP MEP Diagnostic function
MTDi	MPLS-TP MIP Diagnostic function
NCS	Network Connectivity Status
NMS	Network Management System
OAM	Operation, Administration and Maintenance
ODU	Optical channel Data Unit
ODUk	Optical channel Data Unit – order k
ODUk-Xv	Virtual concatenated Optical Channel Data Unit – order k
OPU	Optical Payload Unit
OPUk	Optical Payload Unit of level k
OPUk-Xv	Virtually concatenated Optical Payload Unit of level k
OTH	Optical Transport Hierarchy
PDU	Protocol Data Unit
PHB	Per Hop Behaviour
PM	Performance Monitoring
PSC	PHB Scheduling Class
PSI	Payload Structure Indication
PT	Payload Type
PW	Pseudowire

P11s	1544 kbit/s PDH path layer with synchronous 125 $\mu$ s frame structure according to [ITU-T G.704]
P12s	2048 kbit/s PDH path layer with synchronous 125 $\mu$ s frame structure according to [ITU-T G.704]
P31s	34368 kbit/s PDH path layer with synchronous 125 $\mu$ s frame structure according to [ITU-T G.832]
P32e	44 736 kbit/s PDH path layer with frame structure according to [ITU-T G.704]
RDI	Remote Defect Indication
RES	Reserved overhead
RI	Remote Information
RP	Remote Point
RT	Route Trace
SCC	Signalling Communication Channel
SSF	Server Signal Fail
TC	Traffic Class
TCP	Termination Connection Point
TFP	Termination Flow Point
TH	Throughput
TLV	Type Length Value
TSD	Trail Signal Degrade
TTL	Time-To-Live
TTSI	Trail Termination Source Identifier
TSF	Trail Signal Fail
VcPLM	Virtual concatenation Payload Mismatch
vcPT	virtual concatenation Payload Type

## 5 Conventions

The diagrammatic convention for connection-oriented layer networks described in this Recommendation is that of [ITU-T G.805].

## 6 Supervision

The generic supervision functions are defined in clause 6 of [ITU-T G.806]. Specific supervision functions for the MPLS-TP network are defined in this clause.

### 6.1 Defects

#### 6.1.1 Summary of entry/exit conditions for defects

The defect entry and exit conditions are based on events. Occurrence or absence of specific events may raise or reset specific defects.

In the following:

Valid means a received value is *equal* to the value configured via the MI input interface(s).

Invalid means a received value is *not equal* to the value configured via the MI input interface(s).

A quick overview of the events defined for this Recommendation are summarized in Table 6-1. Events, other than the protection switching events, are generated by processes in the MT\_TT\_Sk function as defined in clause 9.2. These processes define the exact conditions for these events; Table 6-1 only provides a quick overview.

**Table 6-1 – Overview of events**

Event	Meaning
unexpMEG	Reception of a CC-V packet with an invalid MEG value. (Note 1) NOTE – Section 5.1.1 of [IETF RFC 6371] describes the conditions when a received CC-V packet is considered to have invalid MEG and MEP values. In case (1) a continuity check (CC) packet is received by a sink MEP monitoring the MEG for CC and connectivity verification (CV) functions, or (2) a CV packet is received by a sink MEP monitoring the MEG for a CC-only function; the received CC-V packet is considered as having an invalid MEG value (thus triggering the unexpMEG event).
unexpMEP	Reception of a CV packet with an invalid MEP value, but with a valid MEG value.
unexpPeriod	Reception of a CC-V packet with an invalid periodicity value, but with valid MEG and MEP values.
unexpCoS	Reception of a CC-V packet with an invalid TC value, but with valid MEG and MEP values.
expCC-V	Reception of a CC-V packet with valid MEG and MEP values.
RDI=x	Reception of a CC-V packet for the peer MEP with the RDI information indicate to x; where x=0 (remote defect clear) and x=1 (remote defect set).
LCK	Reception of an LCK packet. (Note 2)
AIS	Reception of an AIS packet.
BS	Bad second, a second in which the lost frame ratio exceeds the bad second threshold (BS_THR).
CSF-LOS	Reception of a client signal fail (CSF) packet that indicates Client loss of Signal.
CSF-FDI	Reception of a CSF packet that indicates "Client Forward Defect Indication".
CSF-RDI	Reception of a CSF packet that indicates "Client Reverse Defect Indication".
NOTE 1 – According to [IETF RFC 6371], a CC-V packet is either a CC packet or a CV packet. A CV packet performs both CC and CV OAM functions. A CC packet performs only the CC OAM function. NOTE 2 – IETF uses this term LCK as LKR and LKI in [IETF RFC 6371].	

The occurrence or absence of these events may detect or clear a defect. An overview of the conditions is given in Table 6-2. The notation "#event=x (K\*period)" is used to indicate the occurrence of x events within the period, as specified between the brackets.

Table 6-2 gives a quick overview of the types of defects for the MPLS-TP layer and the raising and clearing conditions for these defects as described in [IETF RFC 6371].

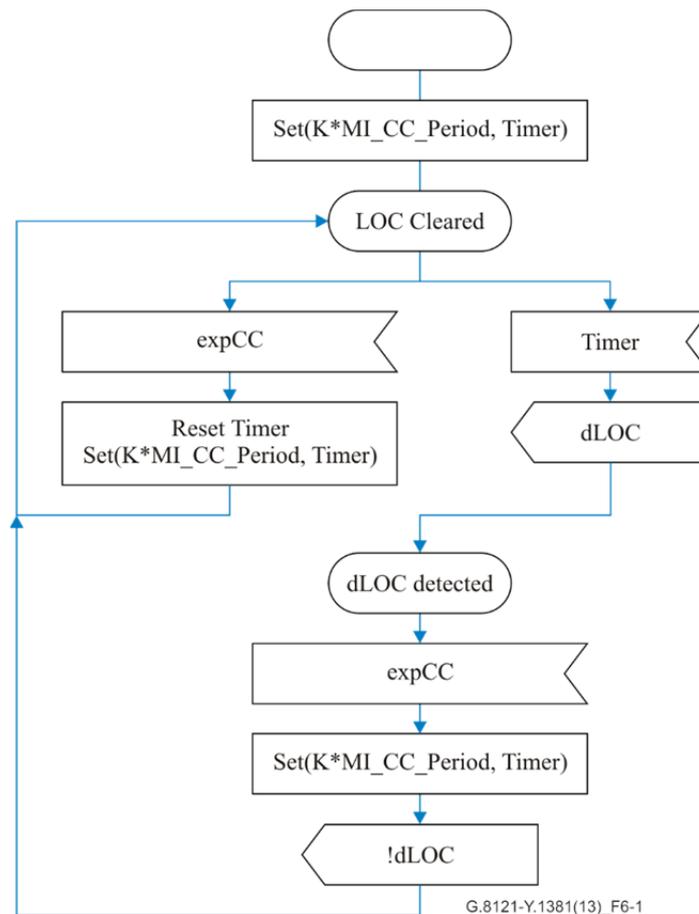
**Table 6-2 – Overview of detection and clearing conditions**

Defect	Defect detection	Clearing condition
dLOC	#expCC-V==0 (K*CC_Period)	expCC-V
dUNC	unexpCoS	#unexpCoS==0 (K*CC-V_Period)

**Table 6-2 – Overview of detection and clearing conditions**

Defect	Defect detection	Clearing condition
dMMG	unexpMEG	#unexpMEG==0 (K*CC-V_Period)
dUNM	unexpMEP	#unexpMEP==0 (K*CC-V_Period)
dUNP	unexpPeriod	#unexpPeriod==0 (K*CC-V_Period)
dRDI	RDI==1	RDI==0
dAIS	AIS	#AIS==0 (K*AIS_Period)
dLCK	LCK	#LCK==0 (K*LCK_Period)
dCSF-LOS	CSF-LOS	#CSF-LOS == 0 (K*CSF_Period or CSF-DCI)
dCSF-FDI	CSF-FDI	#CSF-FDI == 0 (K*CSF_Period or CSF-DCI)
dCSF-RDI	CSF-RDI	#CSF-RDI == 0 (K*CSF_Period or CSF-DCI)
dDEG	#BS==DEGM (DEGM*1second)	#BS==0 (M*1second)

**6.1.2 Continuity supervision**



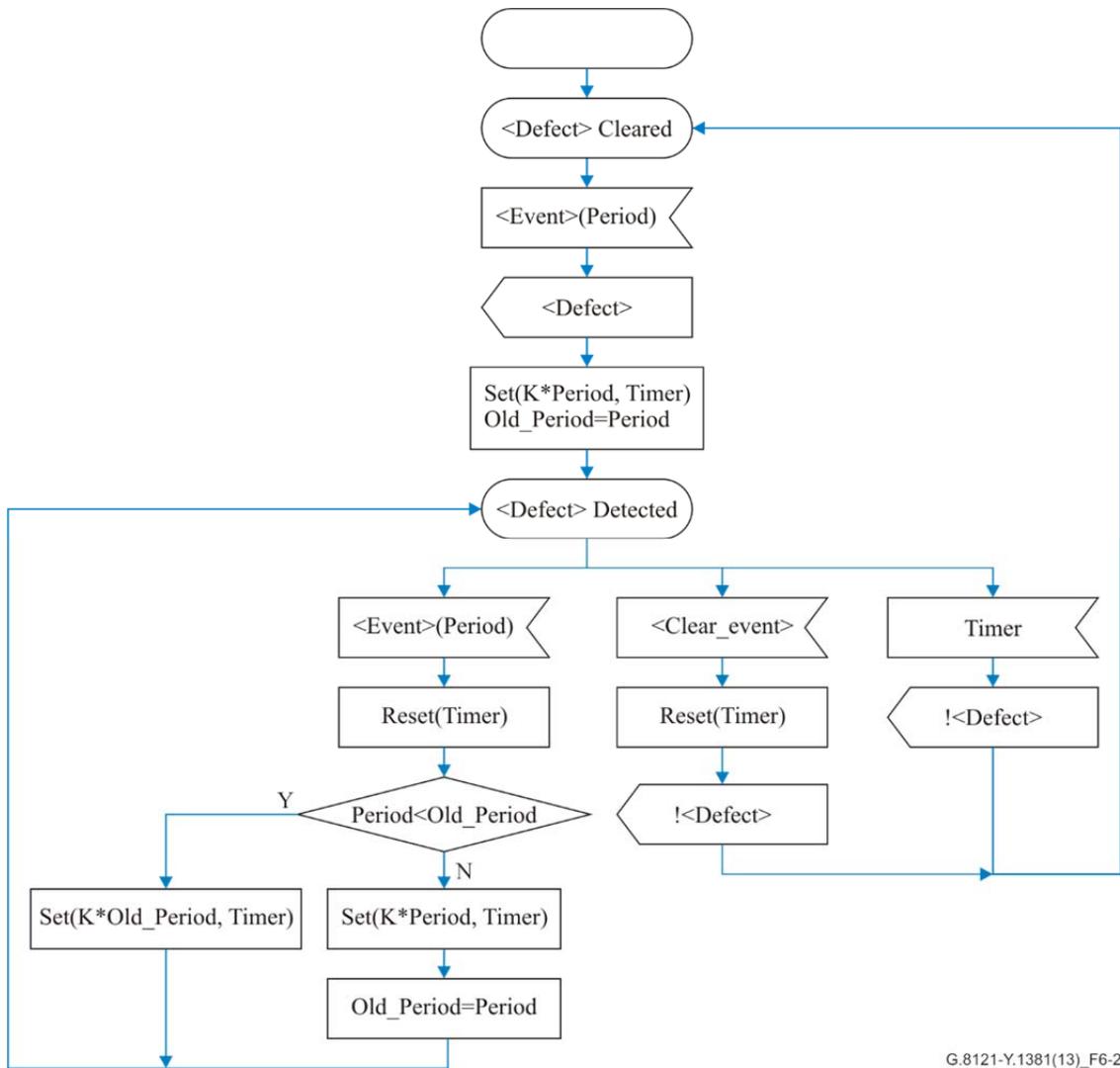
**Figure 6-1 – dLOC detection and clearance process**

**6.1.2.1 Loss of continuity defect (dLOC)**

The loss of continuity defect is calculated at the MT layer. It monitors the presence of continuity in MT trails.

Its detection and clearance are defined in Figure 6-1. The 'period' in Figure 6-1 is set to  $K \cdot MI\_CC\_Period$ , where  $MI\_CC\_Period$  corresponds to the configured CC period and  $K$  is such that  $3.25 \leq K \leq 3.5$ .

### 6.1.3 Connectivity supervision



G.8121-Y.1381(13)\_F6-2

**Figure 6-2 – Defect detection and clearance process for dMMG, dUNM, dUNP, dUNC, dAIS, dLCK and dCSF**

Figure 6-2 shows a generic state diagram that is used to detect and clear the dMMG, dUNM, dUNP, dUNC, dAIS, dLCK and dCSF (dCSF-LOS, dCSF-FDI, and dCSF-RDI) defects. In this diagram  $\langle \text{Defect} \rangle$  needs to be replaced with the specific defect and  $\langle \text{Event} \rangle$  with the specific event related to this defect. Furthermore, in Figure 6-2  $3.25 \leq K \leq 3.5$ .

Figure 6-2 shows that the Timer is set based on the last received period value, unless an earlier OAM packet triggering  $\langle \text{Event} \rangle$  (and therefore the detection of  $\langle \text{Defect} \rangle$ ) carried a longer period. As a consequence, clearing certain defects may take more time than necessary.

#### 6.1.3.1 Mismatch defect (dMMG)

The mismatch defect detect is calculated at the MT layer. It monitors the connectivity in a maintenance entity group (MEG).

Its detection and clearance are defined in Figure 6-2. The  $\langle \text{Defect} \rangle$  in Figure 6-2 is dMMG. The  $\langle \text{Event} \rangle$  in Figure 6-2 is the unexpectedMEG event and the "Period" is the period carried in the CV

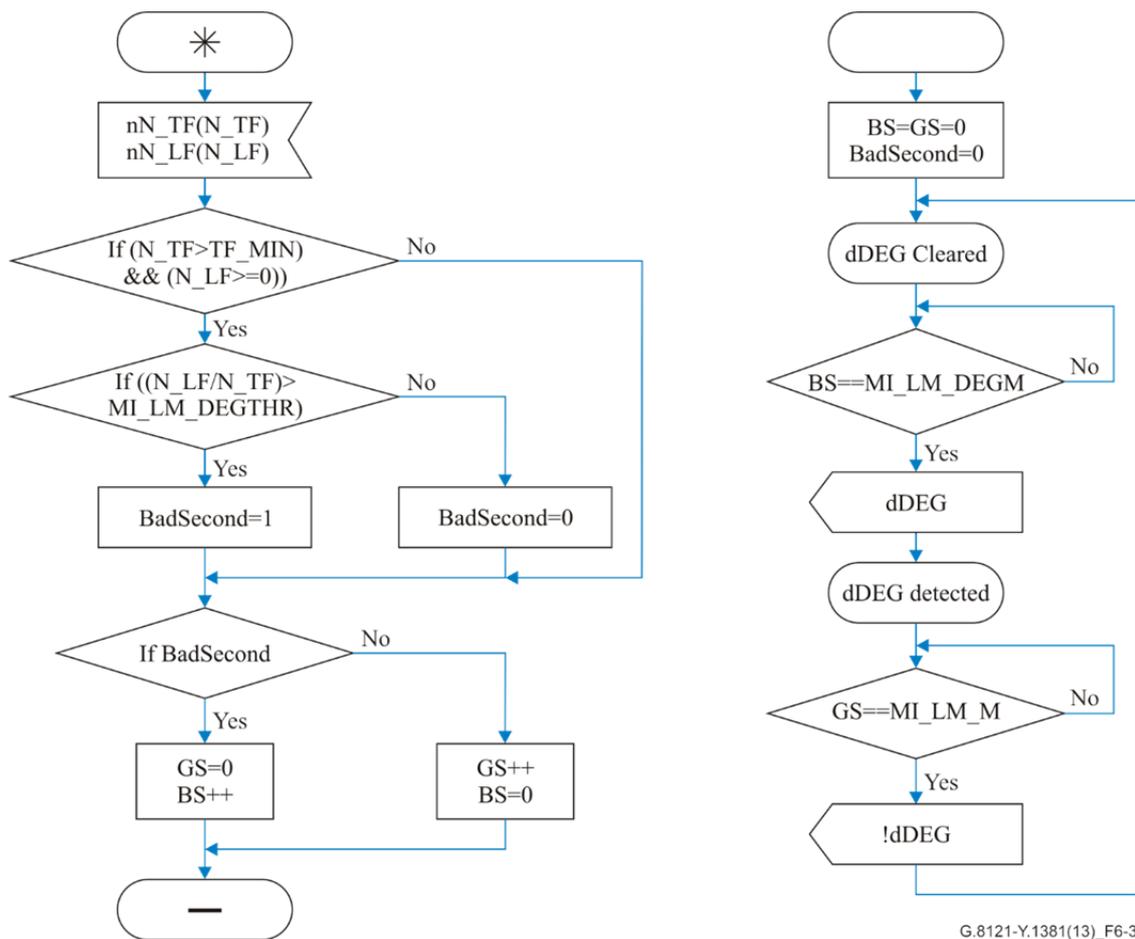
packet that triggered the event, unless an earlier CV packet triggering an unexpectedMEG event carried a greater period.

### 6.1.3.2 Unexpected MEP defect (dUNM)

The unexpected MEP defect is calculated at the MT layer. It monitors the connectivity in a maintenance entity group.

Its detection and clearance are defined in Figure 6-2. The <Defect> in Figure 6-2 is dUNM. The <Event> in Figure 6-2 is the unexpectedMEP event and the "Period" is the period carried in the CV packet that triggered the event, unless an earlier CV packet triggering an unexpectedMEP event carried a greater period.

### 6.1.3.3 Degraded signal defect (dDEG)



**Figure 6-3 – dDEG detection and clearance process**

The degraded signal defect is calculated at the MT layer. It monitors the connectivity of an MT trail.

Its detection and clearance are defined in Figure 6-3.

Every second the state machine receives the 1 second counters for near-end received and transmitted frames and it determines whether the second was a bad second (BS). The defect is detected if there are MI\_LM\_DEGM consecutive bad seconds and cleared if there are MI\_LM\_M consecutive good seconds.

In order to declare a bad second the number of transmitted frames must exceed a threshold (TF\_MIN). If this is true, then a bad second is declared if either the frame loss is negative (i.e., there

are more frames received than transmitted) or the frame loss ratio (lost frames/transmitted frames) is greater than MI\_LM\_DEGTHR.

#### **6.1.4 Protocol supervision**

##### **6.1.4.1 Unexpected periodicity defect (dUNP)**

The unexpected periodicity defect is calculated at the MT layer. It detects the configuration of different periodicities at different MEPs belonging to the same MEG.

Its detection and clearance are defined in Figure 6-2. The <Defect> in Figure 6-2 is dUNP. The <Event> in Figure 6-2 is the unexpectedPeriod event and the "Period" is the period carried in the CC-V packet that triggered the event, unless an earlier CC-V packet triggering an unexpectedPeriod event carried a greater period.

##### **6.1.4.2 Unexpected CoS defect (dUNC)**

The unexpected CoS defect is detected at the MT layer. It detects the configuration error of different CoS at different MEPs belonging to the same MEG.

Its detection and clearance are defined Figure 6-2.

The <Defect> in Figure 6-2 is dUNC. The <Event> in Figure 6-2 is the unexpectedCoS event and the "Period" is the period associated with the CC-V packet that triggered the event, unless an earlier CC-V packet triggering an unexpectedCoS event carried a greater period.

##### **6.1.4.3 Protection protocol supervision**

For further study.

#### **6.1.5 Maintenance signal supervision**

##### **6.1.5.1 Remote defect indicator defect (dRDI)**

The remote defect indicator defect is detected at the MT layer. It monitors the presence of the RDI maintenance signal.

dRDI is detected on receipt of the RDI=1 event and cleared on receipt of the RDI=0 event.

##### **6.1.5.2 Alarm indicate signal defect (dAIS)**

The alarm indicate signal defect is detected at the MT layer. It monitors the presence of the AIS maintenance signal.

Its detection and clearance are defined Figure 6-2. The <Defect> in Figure 6-2 is dAIS. The <Event> in Figure 6-2 is the AIS event and the "Period" is the period associated with the AIS packet unless an earlier AIS packet is associated with a greater period.

##### **6.1.5.3 Locked defect (dLCK)**

The locked defect is detected at the MT layer. It monitors the presence of the locked maintenance signal.

Its detection and clearance are defined in Figure 6-2. The <Defect> in Figure 6-2 is dLCK. The <Event> in Figure 6-2 is the LCK event and the "Period" is the period associated with the LCK packet unless an earlier LCK packet is associated with a greater period.

##### **6.1.5.4 Client signal fail defect (dCSF)**

The CSF (CSF-LOS, CSF-FDI, and CSF-RDI) defect is detected at the MT layer. It monitors the presence of the CSF maintenance signal.

Its detection and clearance conditions are defined in Figure 6-2. The <Defect> in Figure 6-2 is dCSF-LOS, dCSF-FDI, or dCSF-RDI. The <Event> in Figure 6-2 is the CSF event and the "Period"

is the period associated with the CSF packet unless an earlier CSF packet is associated with a greater period.

The <Clear\_event> in Figure 6-2 is the CSF event which indicates detect clearance indication (DCI).

## **6.2 Consequent actions**

For generic consequent actions, see [ITU-T G.806]. For the specific consequent actions applicable to MPLS-TP, refer to the specific atomic functions.

## **6.3 Defect correlations**

For the defect correlations, see the specific atomic functions.

## **6.4 Performance filters**

For further study.

## **7 Information flow across reference points**

Information flow for MPLS-TP functions is defined in clause 9. A generic description of information flow is defined in clause 7 of [ITU-T G.806].

## **8 MPLS-TP processes**

This clause defines the specific processes for the MPLS-TP network. Generic processes are defined in clause 8 [ITU-T G.806].

### **8.1 G-ACh process**

#### **8.1.1 Overview**

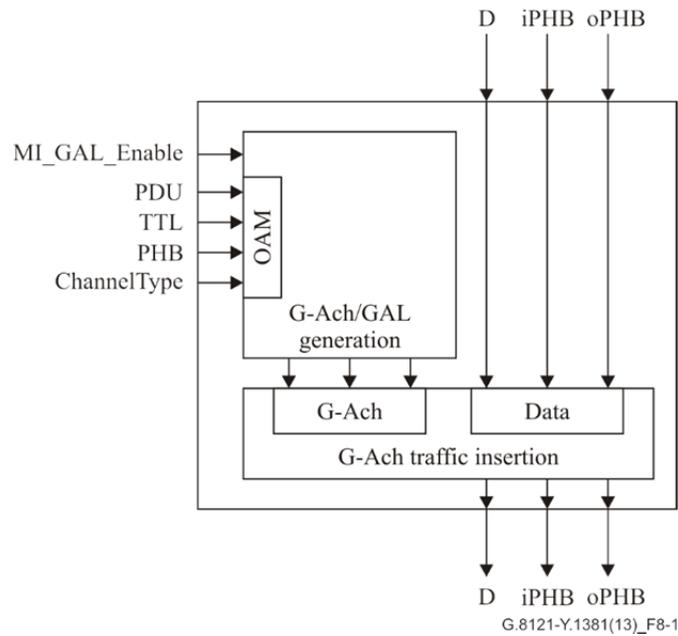
In order to ensure proper operational control, MPLS-TP network elements exchange OAM packets that strictly follow the same path as user traffic packets; that is, OAM packets are subject to the exact same forwarding schemes (e.g., fate sharing) as user traffic packets. These OAM packets can be distinguished from user traffic packets by using the G-ACh and GAL constructs.

The G-ACh is a generic associated control channel mechanism for "Sections", label switched paths (LSPs) and pseudowires (PWs), over which OAM and other control messages can be exchanged. The GAL is a label based exception mechanism to alert label edge routers/label switch routers (LERs/LSRs) of the presence of an associated channel header (ACH) after the bottom of the stack.

The format of GAL and ACH is described in [IETF RFC 5586].

#### **8.1.2 G-ACh insertion process**

Figure 8-1 describes the G-ACh insertion process.



**Figure 8-1 – G-ACh insertion process**

The G-ACh insertion process encapsulates OAM packets and multiplexes them with data packets. The data packets are passed through unchanged, while OAM packets are encapsulated as follows:

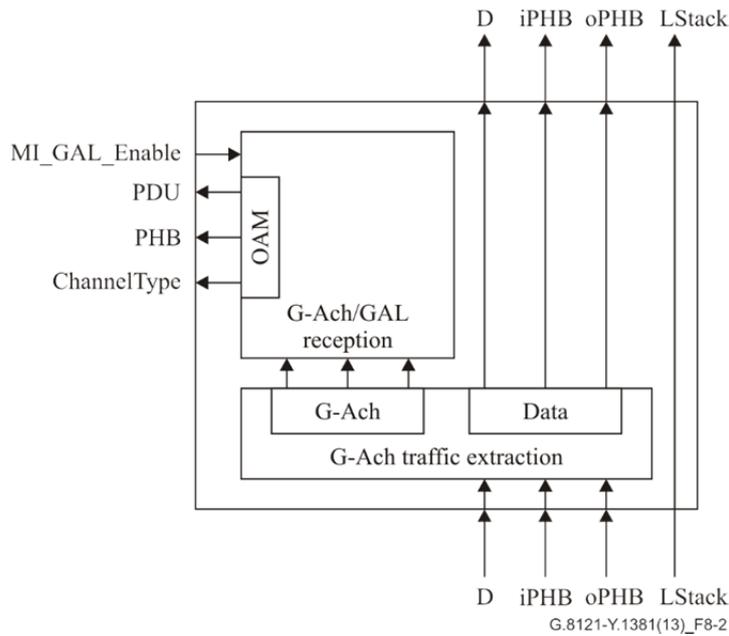
A G-ACh header is prepended to the OAM PDU, with the channel type set to a specified value. If MI\_GAL\_Enable is true, the process then further prepends a G-ACh label (GAL) as described in [IETF RFC 5586]. If the TTL signal is not specified, the TTL field in the MI\_CI\_D is set to 255; otherwise it is set to the value in the TTL signal. If a GAL is inserted, the TTL field in the GAL LSE is set as defined in [IETF RFC 5586].

NOTE 1 – Certain OAM packets can be addressed to an MIP and thus need to be inserted with a specific TTL to ensure that the TTL expires at the target MIP. OAM packets addressed to an MEP have the TTL set to 255.

NOTE 2 – MI\_GAL\_Enable must be set to true on LSPs and to false on PWs. Setting it to true for PWs is for further study.

### 8.1.3 G-ACh extraction process

Figure 8-2 describes the G-ACh extraction process.



**Figure 8-2 – G-ACh extraction process**

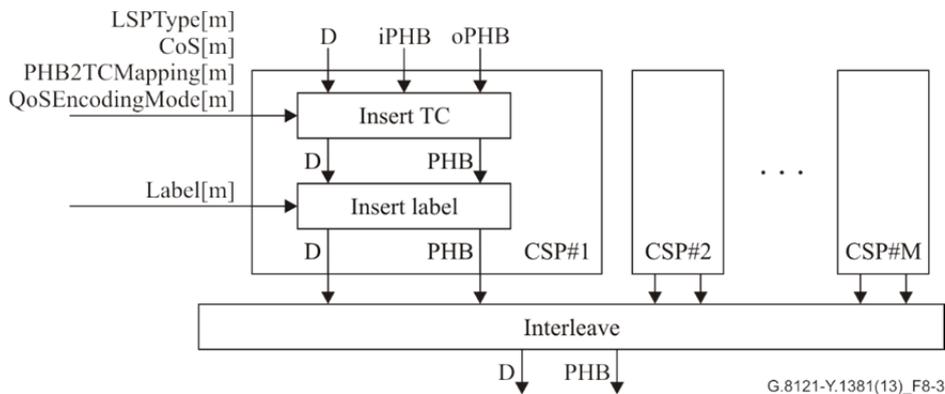
The G-ACh traffic unit will be extracted if it includes GAL and ACh in incoming data when MI\_GAL\_Enable is set.

## 8.2 TC/Label processes

The TC/Label processes can operate in two modes, as described in clause 7.3 of [ITU-T G.8110.1]. Mode 1 is the default mode and is mandatory. Mode 2 is optional and is only used for section monitoring. The mode is set as MI\_Mode by the EMF; it is expected that the EMF sets this automatically to the appropriate value depending on whether the MEP is monitoring a section or a PW/LSP. It is not expected that this MI is exposed to the user.

NOTE – MI\_Mode is always set to Mode 1 when the TC/Label process is within an MT/MT\_A function, since an MPLS section is the lowest monitoring level and is only applicable to monitoring directly over the (non-MPLS) server layer. In a Server/MT\_A function, the TC/Label process may operate in Mode 1 (if there is no section MEP) or Mode 2 (if there is a section MEP).

### 8.2.1 TC/Label source processes



**Figure 8-3 – TC/Label source processes in Mode 1**

Figure 8-3 shows the TC/Label source processes when MI\_Mode is set to Mode 1. These processes are performed on a frame-per-frame basis.

**Client-specific processes:** The function supports M ( $M \leq 2^N - 16$ , with  $N = 20$  for MPLS label) client-specific processes (CSP#1 to CSP#M), each connected to a single MPLS-TP connection point. CSP#m ( $1 \leq m \leq M$ ) is active when Label[m] has a value in the range 16 to  $2^N - 1$ .

**TC insertion process:** Insert the TC field encoding the per hop behaviour (PHB) information according to the following rules:

- If LSPTYPE[m] = L-LSP, the DP information is encoded into the TC field according to [ITU-T G.8110.1] and CoS[m].
- If LSPTYPE[m] = E-LSP, the PHB information is encoded into the TC field according to the 1:1 mapping configured in the PHB2TCMapping[m].

NOTE – E-LSP and L-LSP are referred to [ITU-T G.8110.1].

The PHB information to map into the TC field is selected according to the following rules:

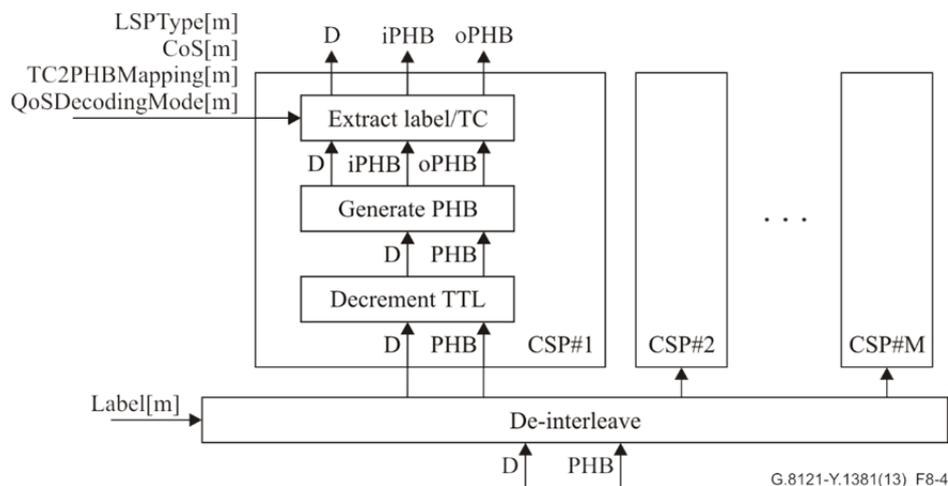
- If QoSEncodingMode[m] = A, the iPHB information is mapped into the TC field.
- If QoSEncodingMode[m] = B, the oPHB information is mapped into the TC field.

**Label insertion process:** Insert the 20-bit MPLS Label field with the value provided via Label[m].

**Interleave process:** Interleave the MPLS-TP traffic units from the client-specific processes into a single stream.

When MI\_Mode is set to Mode 2, the TC/Label source process simply deletes the TTL and S fields from each MPLS traffic unit.

### 8.2.2 TC/Label sink processes



**Figure 8-4 – TC/Label sink processes in Mode 1**

Figure 8-4 shows the TC/Label sink processes when MI\_Mode is set to Mode 1. These processes are performed on a frame-per-frame basis.

**De-interleave process:** De-interleave the MPLS-TP traffic units and forwards each of their client-specific process #m based on the value in the Label field of the traffic unit. The relation between the CSP and MPLS label value is provided by Label[1..M].

Traffic units received with a label value identifying a non-active CSP are dropped.

**Client-specific processes:** The function supports M ( $M \leq 2^N - 16$ , with  $N = 20$  for MPLS label) client-specific processes (CSP#1 to CSP#M), each connected to a single MPLS-TP connection point. CSP#m ( $1 \leq m \leq M$ ) is active when Label[m] has a value in the range 16 to  $2^N - 1$ .

**Label and TC extraction process:** Extract the MPLS label and the TC fields from the traffic unit.

**TTL decrement process:** Decrements the TTL; if the MPLS-TP CP is not a TCP and the decremented TTL is less than or equal to zero, the traffic unit is dropped silently.

NOTE – MIPs and MEPs compound functions are connected to the Server/MT\_A (or MT/MT\_A) functions via an MPLS-TP TCP.

**PHB generation process:** Processes the TC field.

The iPHB signal is generated according to the following rules:

- If LSPTYPE[m] = L-LSP, the CoS information is equal to the CoS[m] while the DP information is decoded from the TC field according to [ITU-T G.8110.1] and the CoS[m].
- If LSPTYPE[m] = E-LSP, the PHB information is decoded from the TC field according to the 1:1 mapping configured in the TC2PHBMapping[m].

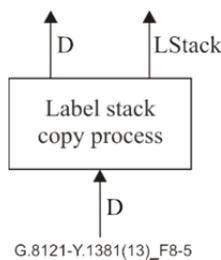
NOTE – E-LSP and L-LSP are referred to [ITU-T G.8110.1].

The CI\_oPHB is generated according to the following rule:

- If QoSDecodingMode = A, the oPHB is equal to the generated iPHB.
- If QoSDecodingMode = B, the oPHB is equal to the received PHB.

When MI\_Mode is set to Mode 2, the TC/Label sink process simply inserts a TTL and an S field on to each MPLS traffic unit, with the values set to 254 and 0 respectively.

### 8.2.3 Label stack copy process



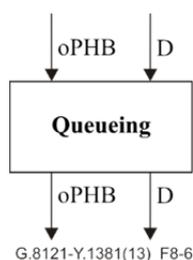
**Figure 8-5 – Label stack copy process**

Figure 8-5 shows the label stack copy process. It passes through the CI\_D unchanged and copies from the CI\_D traffic unit the complete label stack.

### 8.3 Queueing process

The queueing process buffers received MPLS packets for output according to the CI\_oPHB. Figure 8-6 shows the queueing process. The details of queueing process implementation are out of the scope of this Recommendation.

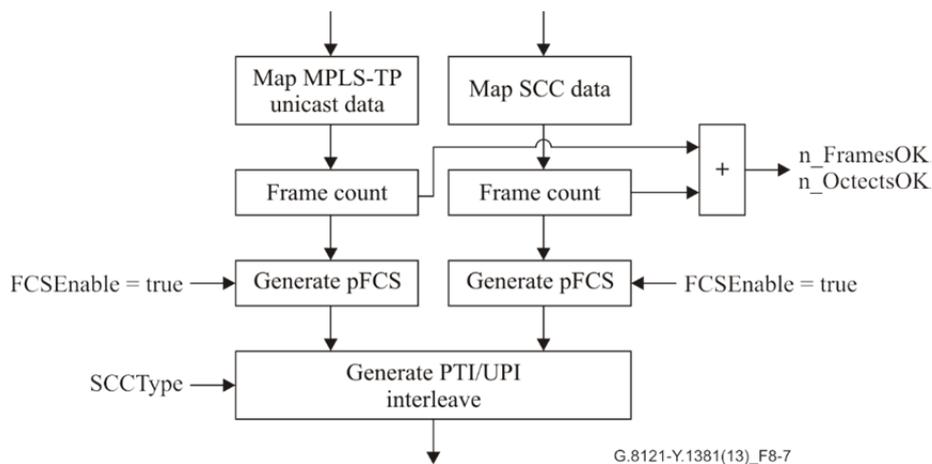
The queueing process is also responsible for dropping frames if their rate at the MT\_CI is higher than the <Srv>\_AI\_D can accommodate. Performance monitor counters are for further study.



**Figure 8-6 – Queueing process**

## 8.4 MPLS-TP-specific GFP-F processes

### 8.4.1 MPLS-TP-specific GFP-F source processes



**Figure 8-7 – MPLS-TP-specific GFP-F source process**

Figure 8-7 shows the MPLS-TP-specific GFP-F source processes. These processes are performed on a frame-per-frame basis.

**Mapping of MPLS-TP data:** The MPLS-TP packet is inserted into the client payload information field of the GFP frame as defined in clause 7.6 of [ITU-T G.7041]. One MPLS-TP packet results in one GFP frame.

**Mapping of SCC data:** The SCC frame is inserted into the client payload information field of the GFP frame as defined in clause 7 of [ITU-T G.7041]. One SCC packet results in one GFP frame.

**Frame count:** It counts the number of frames ( $n\_FramesOK$ ) and octets ( $n\_OctetsOK$ ) that pass through.

**pFCS generation:** See clause 8.5.4.1.1 of [ITU-T G.806]. GFP FCS is always enabled ( $FCSEnable=true$ ).

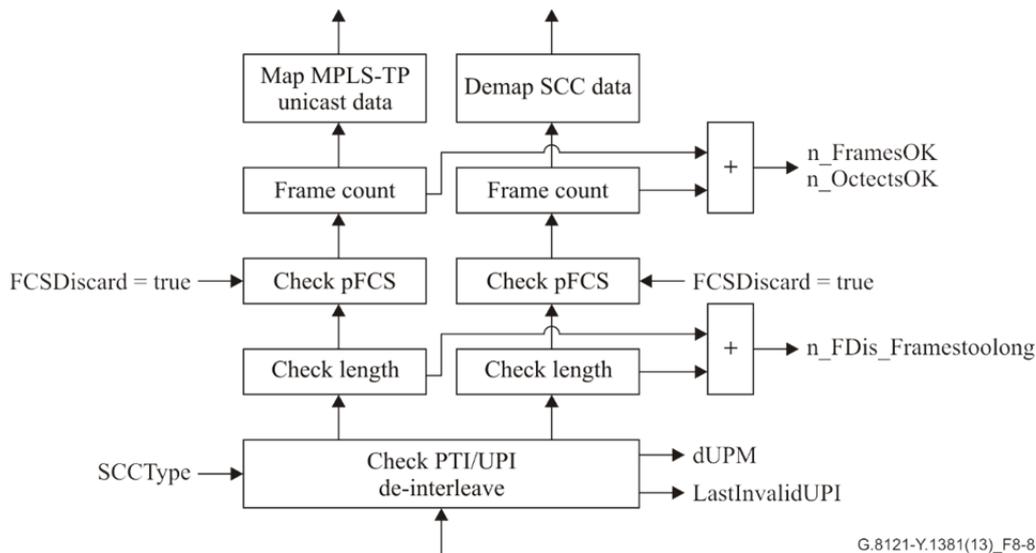
**Generate PTI and UPI, interleave:** The PTI field of the GFP type header is set fixed to "000". The UPI field of the GFP type header is set to:

- the MPLS UPI (as defined in Table 6-3 of [ITU-T G.7041]), for frames coming from the Map MPLS-TP data process;
- the SCC UPI according to the SCC type for frames coming from the Map SCC data process.

The frames are then interleaved to form a single stream.

NOTE 2 – GFP client management frames are not defined for MPLS-TP over GFP-F mapping.

## 8.4.2 MPLS-TP-specific GFP-F sink processes



**Figure 8-8 – MPLS-TP-specific GFP-F sink process**

Figure 8-8 shows the MPLS-TP-specific GFP-F sink processes. These processes are performed on a frame-per-frame basis.

**Check PTI and UPI, de-interleave:** GFP frames with an accepted PTI (AcPTI, see clause 8.5.1.1 of [ITU-T G.806]) of "000" are client data frames. All GFP frames with an accepted PTI (AcPTI, see clause 8.5.1.1 of [ITU-T G.806]) value other than "000" shall be discarded.

The UPI of client data frames is checked to generate dUPM as follows:

- a "valid-UPI frame" is a frame with a UPI that equals either the MPLS UPI (as defined in Table 6-3 of [ITU-T G.7041]) or the SCC UPI according to SCCType. All other frames are "invalid-UPI frames";
- dUPM is raised as soon as one "invalid-UPI frame" is received;
- dUPM is cleared if no "invalid-UPI frames" have been received for the last Tclear seconds.

Tclear is for further study. If dUPM is active, the latest received invalid UPI is available at LastInvalidUPI. If dUPM is not active, LastInvalidUPI is "n/a".

The UPI of client data frames is further used to de-interleave the frames:

- "valid-UPI frames" with UPI equalling the MPLS UPI (as defined in Table 6-3 of [ITU-T G.7041]) are sent towards the "Demap MPLS-TP data" process;
- "valid-UPI frames" with UPI equalling the SCC UPI according to SCCType (as defined in Table 6-3 of [ITU-T G.7041]) are sent towards the "Demap SCC data" process;
- "invalid-UPI frames" are discarded.

**GFP-F frame length:** It checks whether the length of the GFP-F frame is allowed. Frames longer than GFP\_Length bytes are dropped and counted (n\_FramesTooLong).

NOTE – GFP\_Length is for further study.

**pFCS supervision:** See clause 8.5.4.1.2 of [ITU-T G.806]. The discarding of errored frames is always enabled (FCSDiscard=true). If the accepted PFI is 0, the frame is dropped and counted (n\_FDis\_PFI).

**Frame count:** It counts the number of frames (n\_FramesOK) and octets (n\_OctetsOK) that pass through.

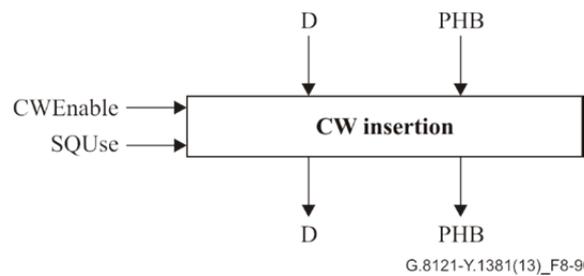
**De-mapping of SCC data:** The SCC packet is extracted from the client payload information field of the GFP frame, as defined in clause 7 of [ITU-T G.7041]. One GFP frame results in one SCC frame.

**De-mapping of unicast MPLS-TP data:** The MPLS-TP packet is extracted from the client payload information field of the GFP frame, as defined in clause 7.6 of [ITU-T G.7041]. One GFP frame results in one MPLS-TP packet.

## 8.5 Control word (CW) processes

This function performs control word (CW) processing as described in [IETF RFC 4448]. The CW is known as the common interworking indicators (CII) in [ITU-T Y.1415].

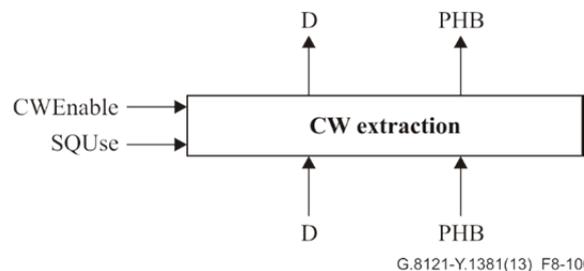
### 8.5.1 CW insertion process



**Figure 8-9 – CW insertion process**

Figure 8-9 shows the CW insertion process. This function should generate and insert the CW as described in [IETF RFC 4448] if the indication CWEnable is true. Otherwise, no insertion should be performed. If the indication SQUse is false, the sequence number field should be set at all zeroes.

### 8.5.2 CW extraction process

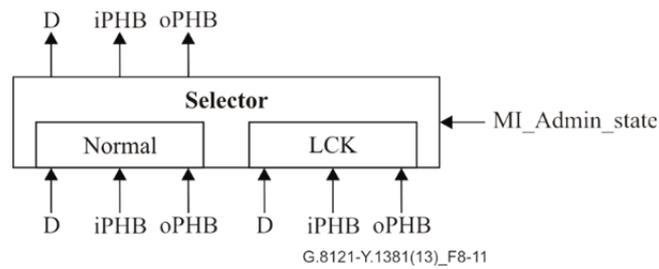


**Figure 8-10 – CW extraction process**

Figure 8-10 shows the CW extraction process. This function should process and remove the CW as described in [IETF RFC 4448], if the indication CWEnable is true. In this case, if the indication SQUse is true, the sequence number field should be processed and out-of-sequence packets dropped (no reordering is performed by this process).

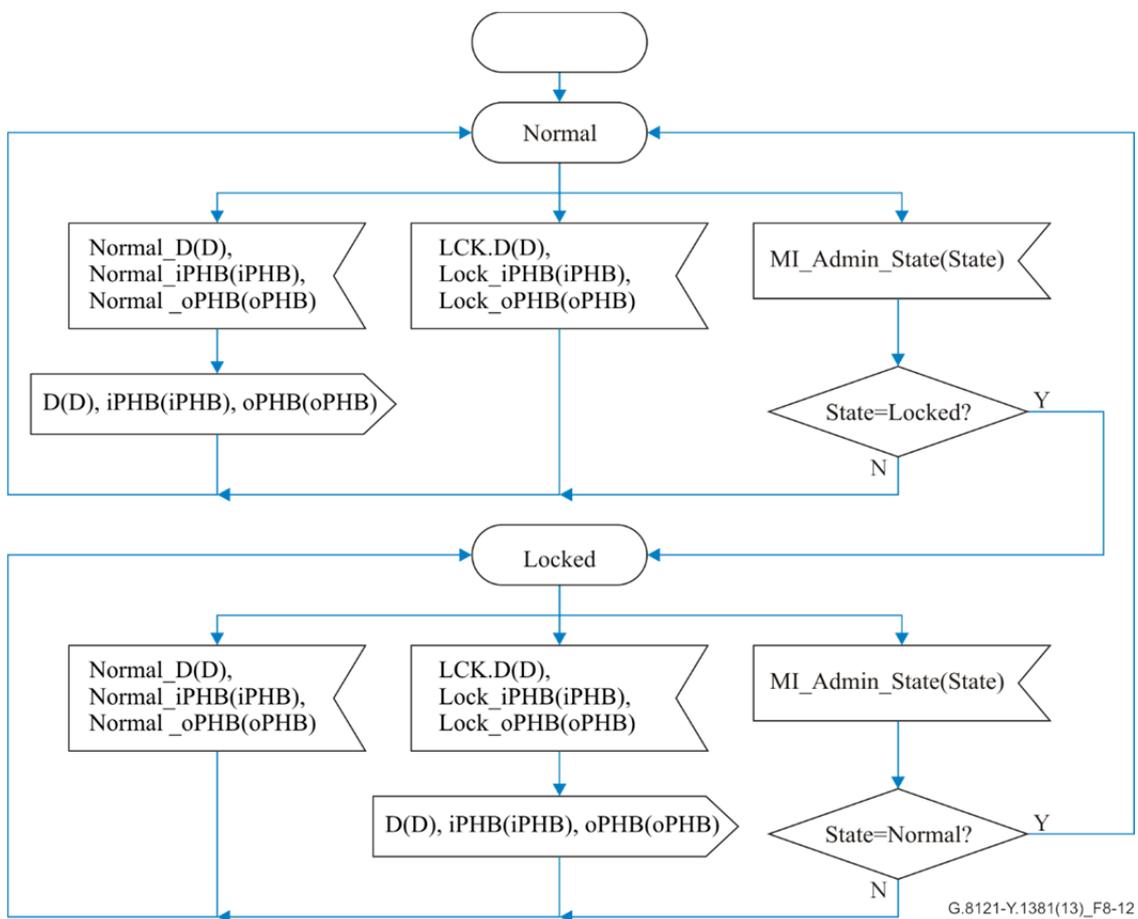
## 8.6 OAM-related processes used by server adaptation functions

### 8.6.1 Selector process



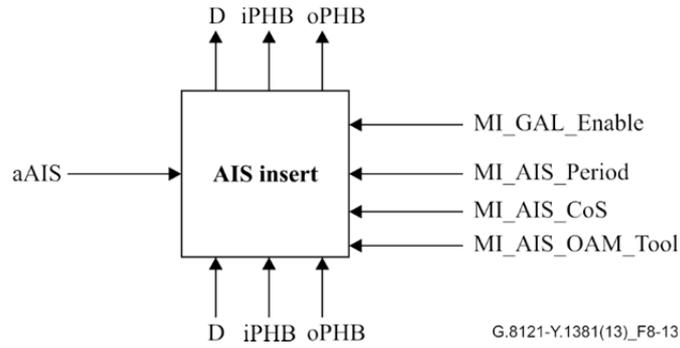
**Figure 8-11 – Selector process**

Figure 8-11 shows the selector process symbol. The selector process selects the valid signal from the input of the normal MT\_CI signal or the MT\_CI LCK signal (as generated by the LCK generation process in 8.6.3). The normal signal is blocked if MI\_Admin\_State is LOCKED. The behaviour is defined in Figure 8-12.



**Figure 8-12 – Selector behaviour**

### 8.6.2 AIS insert process



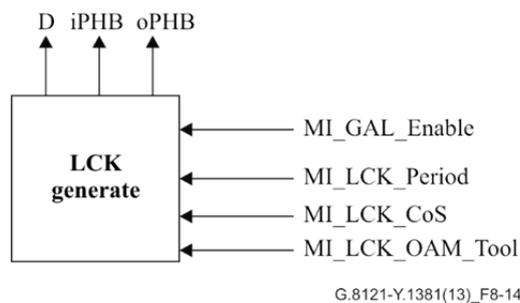
**Figure 8-13 – AIS insert process**

Figure 8-13 shows the AIS insert process symbol. The generated AIS traffic units are inserted in the incoming stream, i.e., the output stream contains the incoming traffic units and the generated AIS traffic units.

The period between consecutive AIS traffic units is determined by the MI\_AIS\_Period parameter. The format of the AIS traffic units is defined by the MI\_AIS\_OAM\_Tool parameter. The generated AIS traffic units are G-ACh encapsulated, as described in clause 8.1 which includes GAL or not depending on MI\_GAL\_Enable.

The value of the MT\_CI\_iPHB and MT\_CI\_oPHB signals associated with the generated AIS traffic units is the PHB with the lowest drop precedence within the CoS defined by the MI\_AIS\_CoS input parameter. As described in [IETF RFC 6371], AIS packets are transmitted with the "minimum loss probability PHB".

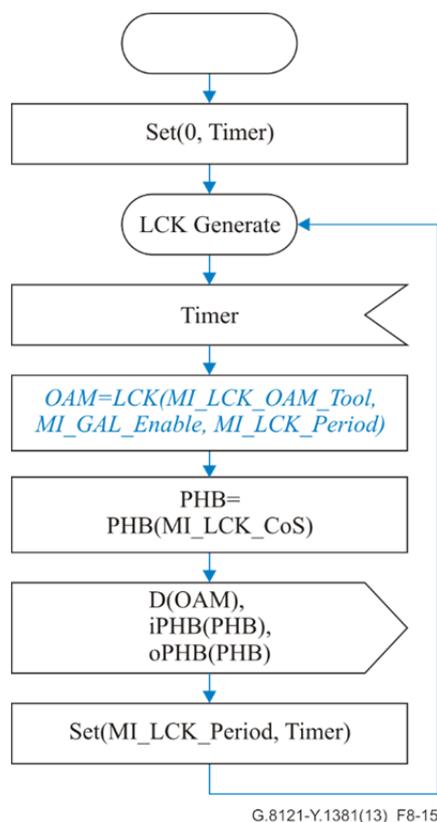
### 8.6.3 LCK generate process



**Figure 8-14 – LCK generation process**

Figure 8-14 shows the LCK<sup>3</sup> generate process symbol. The LCK generation process generates MT\_CI traffic units where the MT\_CI\_D signal contains the LCK signal. Figure 8-15 defines the behaviour of the LCK generation process.

<sup>3</sup> IETF uses the term LKR for this function.



**Figure 8-15 – LCK generation behaviour**

The LCK generation process continuously generates LCK traffic units. The period between consecutive LCK traffic units is determined by the MI\_LCK\_Period parameter.

The LCK(LCK\_OAM\_Tool, GAL\_Enable, Period) function generates an LCK traffic unit, whose format is defined by the LCK\_OAM\_Tool parameter, that encodes the period information defined by the value of the Period parameter. The generated traffic unit is G-ACh encapsulated, as described in clause 8.1, with or without the GAL depending on the GAL\_Enable parameter.

The value of the MT\_CI\_iPHB and MT\_CI\_oPHB signal associated with the generated LCK traffic units is the PHB with the lowest drop precedence within the CoS defined by the MI\_LCK\_CoS input parameter. The PHB(MI\_LCK\_CoS) function generates such PHB information. As described in [IETF RFC 6371], LCK packets are transmitted with the "minimum loss probability PHB".

## **8.7 OAM-related processes used by adaptation functions**

### **8.7.1 MCC and SCC mapping and de-mapping**

As defined in [ITU-T G.7712], an embedded communication channel (ECC) provides a logical operations channel between NEs that can be utilized by various applications. An MCC is an ECC dedicated for management plane communications. A signalling communication channel (SCC) is an ECC dedicated for control plane communications.

The MCC mapping and de-mapping processes are provided to support the MT to MCC adaptation function for accessing the MCC. The SCC mapping and de-mapping processes are provided to support the MT to SCC adaptation function for accessing the SCC. The mapping and de-mapping processes for MCC are very similar to those of the SCC. In the following description of this subclause and clause 8.7.2, the term ECC will be used, which applies to both MCC and SCC.

### 8.7.1.1 ECC mapping

The ECC mapping process is associated with the MT/MCC\_A\_So and MT/SCC\_A\_So functions, which are described in clauses 10.2.2.1 and 10.2.1.1 respectively.

This process shall map the incoming ECC packet into a G-ACh encapsulated ECC traffic unit (i.e., MT\_AI\_D traffic units carrying an ECC packet).

The ECC traffic units generated by this process are encapsulated into the G\_ACh, as defined in [IETF RFC 5718], using or not using the GAL depending on the MI\_GAL\_Enable configuration parameters. The value of the MT\_AI\_PHB associated with the generated ECC traffic units is defined by the MI\_ECC\_CoS input parameter.

### 8.7.1.2 ECC demapping

The ECC demapping process is associated with the MT/MCC\_A\_Sk and MT/SCC\_A\_Sk functions, which are described in clauses 10.2.2.2 and 10.2.1.2 respectively.

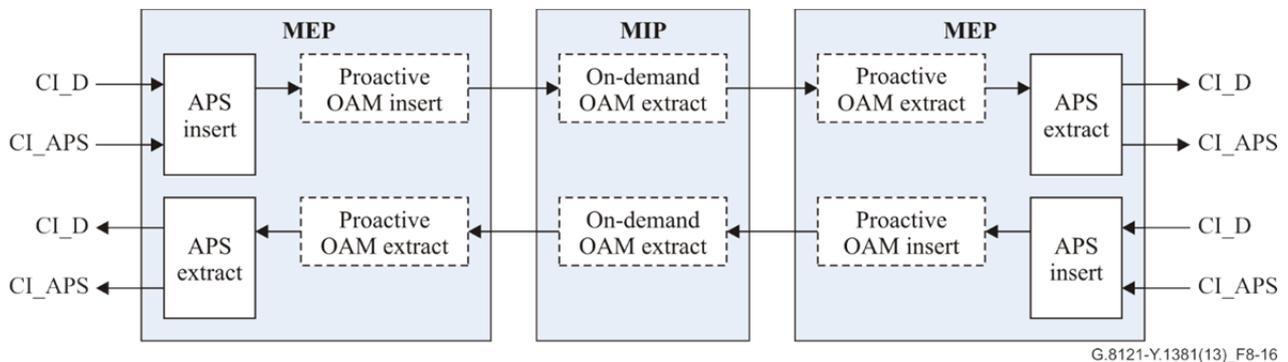
This process shall extract the ECC packet from the G-ACh encapsulated ECC traffic unit (i.e., MT\_AI\_D traffic units carrying ECC packets).

The criteria for selecting ECC traffic units are based on the values of the fields within the MT\_AI\_D signal:

- GAL included to the MT\_AI\_D if GAL usage is enabled via MI\_GAL\_Enable.
- The channel type of G-ACh indicates an MCC packet (in MT/MCC\_A\_Sk) or an SCC packet (in MT/SCC\_A\_Sk), as defined in [IETF RFC 5718].

### 8.7.2 APS insert and extract processes

Figure 8-16 shows a protocol-neutral abstract model of the different processes inside MEPs and MIPs that are involved in the automatic protection switching (APS)<sup>4</sup> function.

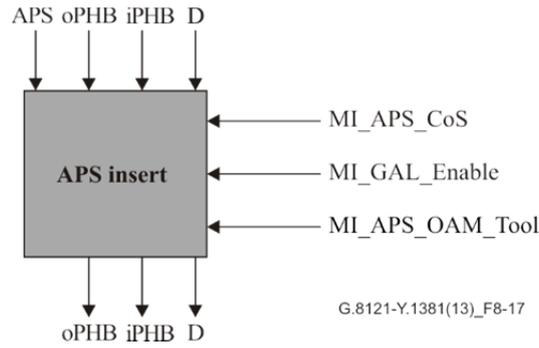


**Figure 8-16 – Overview of the processes involved with APS function**

APS insert and extract processes are located in the MT/MT\_Adaptation function. CI\_APS signal carries APS-specific information which is for further study. APS traffic units are inserted into and extracted from the stream of MT\_CI\_D traffic units.

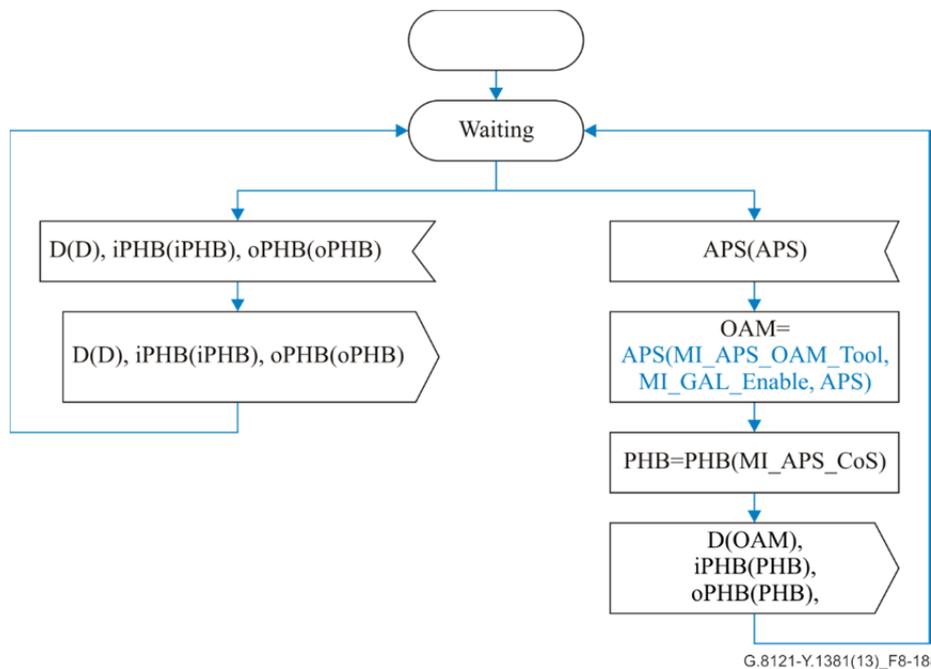
<sup>4</sup> IETF uses the term PCS for this function in [b-IETF RFC 6378].

### 8.7.2.1 APS insert process



**Figure 8-17 – APS insert process**

Figure 8-17 shows the APS insert process and Figure 8-18 defines the behaviour. The resulting APS traffic unit is inserted into the stream of incoming traffic units, i.e., the outgoing stream consists of the incoming traffic units and the inserted APS traffic units.

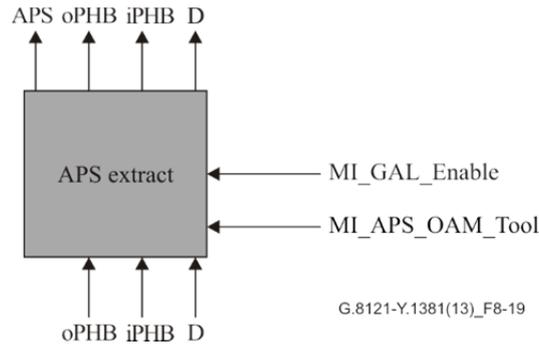


**Figure 8-18 – APS insert behaviour**

The APS(APS\_OAM\_Tool, GAL\_Enable, APS) function generates an APS traffic unit, whose format is defined by the APS\_OAM\_Tool parameter, that encodes the APS information defined by the value of the APS parameter. The generated traffic unit is G-ACh encapsulated, as described in clause 8.1, with or without the GAL depending on the GAL\_Enable parameter.

The value of the MT\_CI\_iPHB and MT\_CI\_oPHB signals associated with the generated APS traffic units are the PHB with the lowest drop precedence within the CoS defined by the MI\_APS\_CoS input parameter. The PHB(MI\_APS\_CoS) function generates such PHB information.

### 8.7.2.2 APS extract process



**Figure 8-19 – APS extract process**

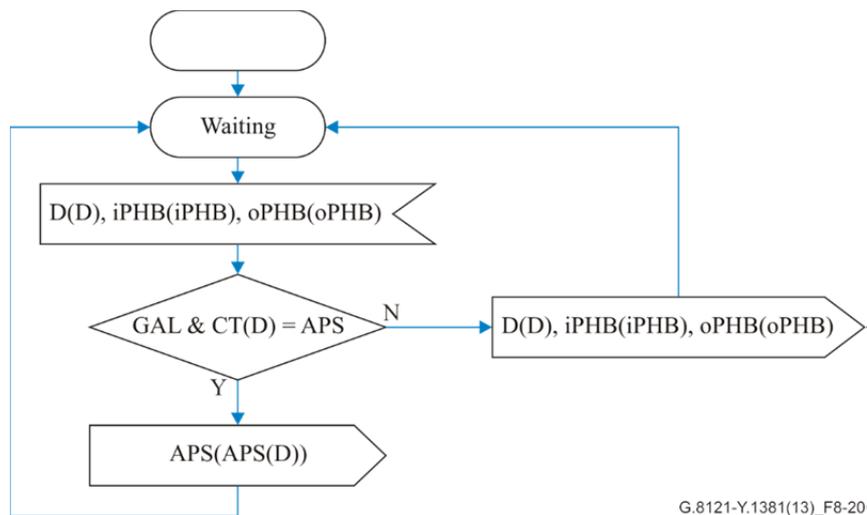
The APS extract process extracts MT\_CI\_APS signals from the incoming stream of MT\_CI traffic units.

The MT\_CI\_APS is the APS-specific information contained in the received traffic unit. All other traffic units will be transparently forwarded.

The criteria for filtering are based on the values of the fields within the MT\_CI\_D signal:

- GAL included to the MT\_CI\_D if GAL usage is enabled via MI\_GAL\_Enable.
- OAM type that is defined in the channel type of G-ACh indicates APS.

This is defined in Figure 8-20. The function APS(D) extracts the APS-specific information from the received traffic unit.

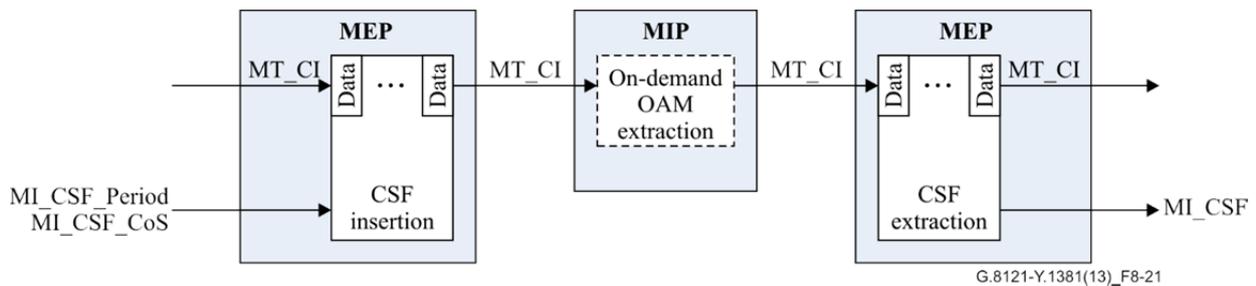


**Figure 8-20 – APS extract behaviour**

### 8.7.3 CSF insert and extract processes

Figure 8-21 shows the different processes inside MEPs and MIPs that are involved in the CSF<sup>5</sup> protocol.

<sup>5</sup> IETF uses the term "Client Failure Indication" for this function in [IETF RFC 6371].

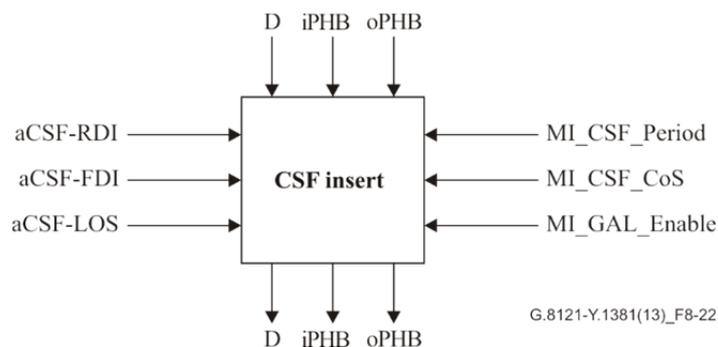


**Figure 8-21 – Overview of processes involved with CSF protocol**

The MPLS-TP client signal fail function (MT-CSF) is used by an MEP to propagate to a peer MEP the detection of a failure or defect event in an MPLS-TP client signal when the client itself does not support appropriate fault or defect detection or propagation mechanisms, such as MT-CC or MT-AIS. The MT-CSF messages propagate in the direction from MPLS-TP MEP function detecting the failure or defect event to the MPLS-TP sink-adaptation function associated with the peer MEP.

MT-CSF generation is located at MT/Client\_A\_So to insert CSF traffic unit and Proactive OAM insertion is located at MT\_TT.

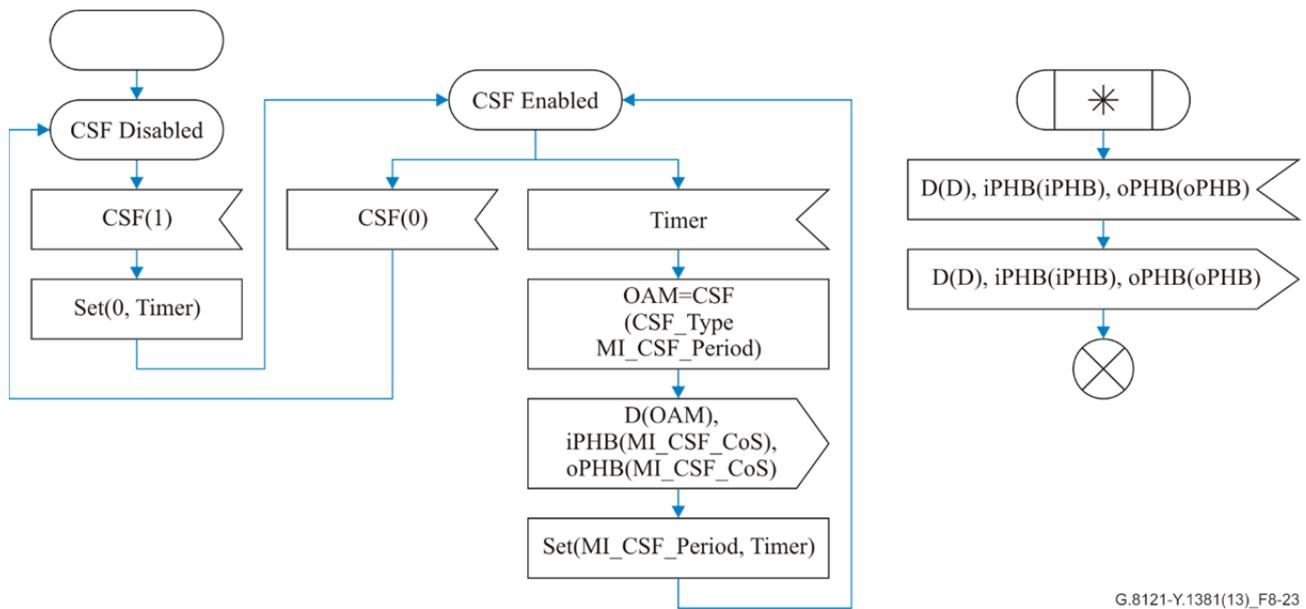
### 8.7.3.1 CSF insert process



**Figure 8-22 – CSF insert process**

The CSF insert process is located at MT/Client\_A\_So as part of CSF generation. Figure 8-22 shows the CSF insert process symbol and Figure 8-23 defines the behaviour. If the aCSF signal is true, the CSF insert process periodically generates MT\_CI traffic units where the MT\_CI\_D signal contains the CSF signal until the aCSF signal is false. The generated CSF traffic units are inserted in the incoming stream, i.e., the output stream contains the incoming traffic units and the generated CSF traffic unit that contains the G-ACh traffic unit as described in clause 8.1, which includes the GAL or not depending on MI\_GAL\_Enable. The period between consecutive CSF traffic units is determined by the MI\_CSF\_Period parameter.

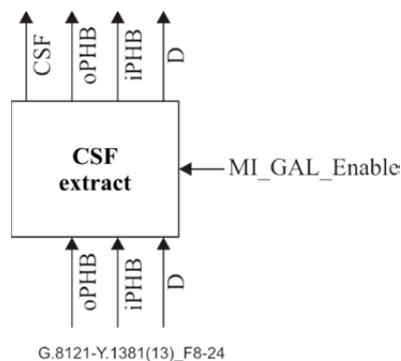
The specific CSF traffic unit is for further study.



**Figure 8-23 – CSF insert behaviour**

NOTE – Generation of CSF(0) and CSF(1) events, as well as determination of CSF type is for further study.

### 8.7.3.2 CSF extract process



**Figure 8-24 – CSF extract process**

The CSF extract process is located at MT/Client\_A\_sk and extracts MT-CSF from MI\_AI\_D. Figure 8-24 shows the CSF extract process symbol.

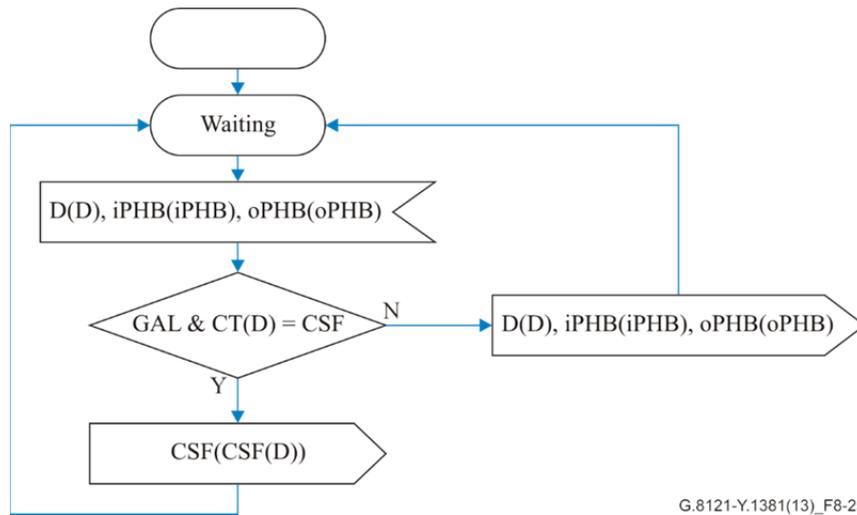
The encoding of the MT\_CI\_D signal for CSF frames is for further study.

The criteria for filtering are based on the values of the fields within the MT\_CI\_D signal:

- GAL included to the MT\_CI\_D if GAL usage is enabled via MI\_GAL\_Enable.
- OAM type that is defined in the channel type of G-ACh indicates CSF.

This behaviour is defined in Figure 8-25. The function CSF(D) extracts the CSF-specific information from the received traffic unit.

NOTE – The G-ACh process is carried out as defined in clause 8.1. The CSF traffic unit in MT\_CI\_D is forwarded to the CSF extract process.



G.8121-Y.1381(13)\_F8-25

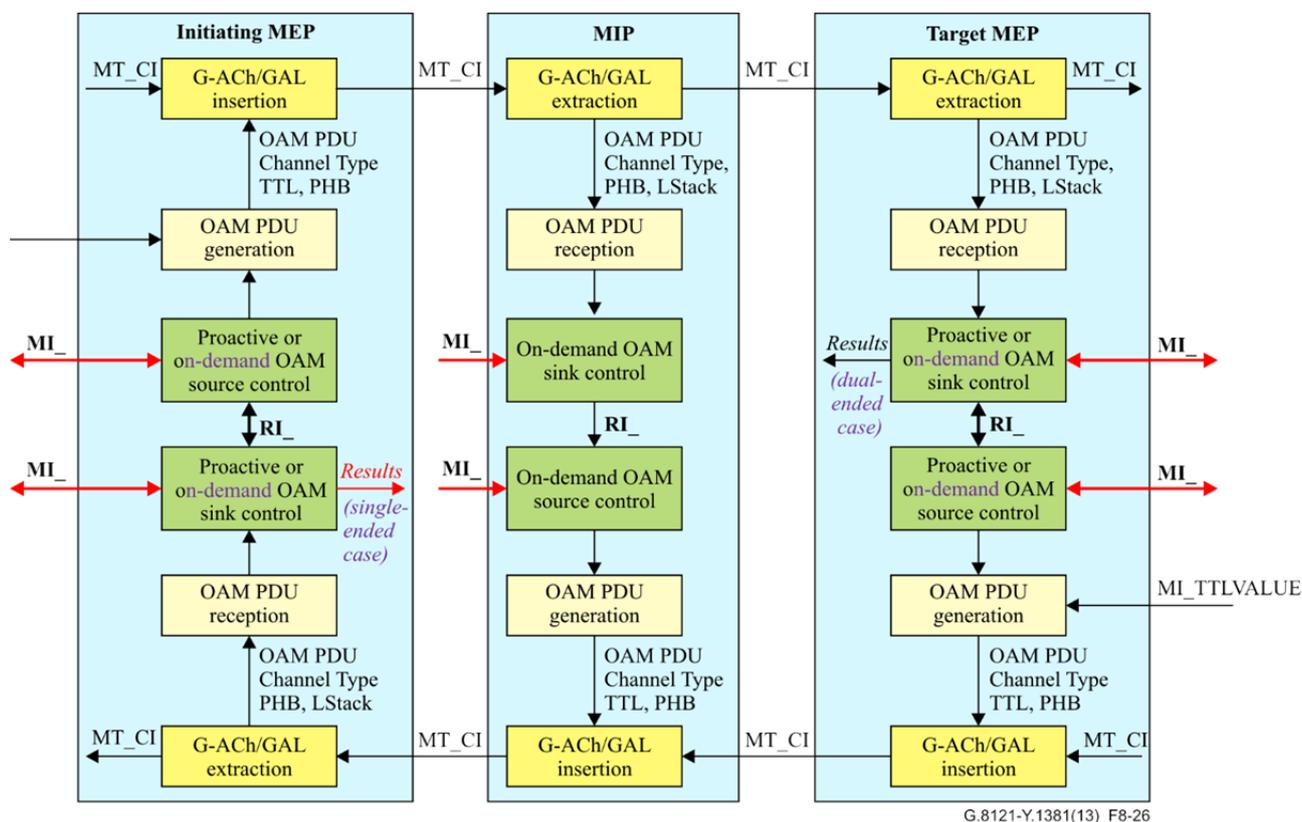
**Figure 8-25 – CSF extract behaviour**

### 8.8 Proactive and on-demand OAM-related processes

As described in [IETF RFC 6371], OAM functions are categorized as proactive and on-demand and these OAM functionalities provide the different interfaces.

OAM functions can be also categorised as single-ended and dual-ended. Single-ended functions are those in which an initiating MEP sends OAM PDUs to a target MEP, which processes it and sends a response OAM PDU back to the initiating MEP. The results of the function are available only on the initiating MEP. Dual-ended functions are those in which an initiating MEP sends OAM PDUs to a target MEP, which processes it and does not send a response. The results of the function are available only on the target MEP. Dual-ended functions are typically deployed in pairs, one in each direction.

Figure 8-26 shows an OAM protocol-neutral abstract model of the different processes inside MEPs and MIPs that are involved in performing single-ended proactive or on-demand OAM functions. In the case of dual-ended functions, the model is equivalent to the top half of the diagram only, and the results are reported by the OAM sink control process on the target MEP.



**Figure 8-26 – Overview of the processes involved with proactive or on-demand OAM functions**

NOTE – The MT\_CI signals at the input of the G-ACh insertion process and at the output of the G-ACh extraction process are not input/output signals of the initiation/target MEPs but signals which are internal to these MEPs.

The processes shown in Figure 8-26 are described further below, with the exception of the G-ACh insertion and extraction processes which are described in clause 8.1.

The relevant management information (MI\_) and remote information (RI\_) used by these processes depends on the OAM function to be performed and is defined in the following subclauses.

The detailed specification of all the OAM processes, including further process decomposition and the interface between them, is OAM protocol specific and therefore outside the scope of this Recommendation.

### OAM control processes:

The four OAM control processes (that is, the proactive OAM source control process, proactive OAM sink control process, on-demand OAM source control process and on-demand OAM sink control process) perform all the OAM control procedures (e.g., they maintain the necessary state machine) that are required for a specific OAM protocol. The proactive OAM source and sink control processes operate within the MT\_TT\_So and MT\_TT\_Sk atomic functions respectively.

Similarly, the on-demand OAM source and sink control processes operate within the MTDe\_TT\_So or MTDi\_TT\_So and the MTDe\_TT\_Sk or MTDi\_TT\_Sk atomic functions respectively.

All four processes consist of a number of OAM protocol-specific control subprocesses, each relating to a different OAM function. The details of these subprocesses are outside the scope of this Recommendation.

The OAM source control process within the initiating MEP (proactive or on-demand, as appropriate) requests the OAM PDU generation process to generate OAM request PDUs toward the

target MEP on the basis of the local state machine and the relevant management information (MI<sub>l</sub>). This supports both single-ended and dual-ended OAM transactions.

In the case of a dual-ended OAM transaction, the appropriate OAM sink control process within the target MEP (proactive or on-demand) or within the target MIP (on-demand only) reports the dual-ended OAM results on the basis of the OAM request PDUs received by the OAM PDU reception process.

In the case of single-ended OAM transactions, the following actions are taken:

- The OAM sink control process within the target MEP or MIP provides the local OAM source control process with the relevant remote information (RI<sub>r</sub>) to generate a reply to the OAM request PDU received by the local PDU reception process.
- The OAM source control process within the target MEP or MIP requests the OAM PDU generation process to generate OAM reply PDUs towards the initiating MEP based on the information it receives from the local OAM sink control process via the relevant remote information (RI<sub>r</sub>).
- The OAM sink control process within the initiating MEP reports the unidirectional or bidirectional OAM results based on the OAM reply PDUs received by the local OAM PDU reception process.

#### **OAM PDU generation process:**

The OAM PDU generation process builds, when instructed by its control process, the required OAM PDU and passes it to the G-ACh insertion process, defined in clause 8.1, for insertion within the MPLS-TP CI traffic flow. It also passes the following information elements that are required by the G-ACh insertion process: the PHB associated with the OAM packet (on the basis of the CoS instruction received by the control process); the ACh channel type that identifies the OAM PDU and the TTL value which it is either the TTL distance to an MIP (for OAM PDUs targeted to an MIP and properly requested by the control process) or the default value as configured via MI\_TTLValue.

The OAM PDU generation process consists of a number of OAM protocol-specific PDU generation subprocesses (one for each PDU type) and a subprocess that multiplexes all the PDUs generated by these OAM protocol-specific PDU generation subprocesses into a single stream of OAM PDUs, which is sent to the G-ACh insertion process along with the appropriate ACh channel type. The details of these subprocesses are outside the scope of this Recommendation.

#### **OAM PDU reception process:**

The OAM PDU reception process receives an OAM PDU, together with the ACh channel type value identifying the PDU type, the associated PHB, and the label stack (LStack) data, from the G-ACh process and passes the relevant information to its control process.

The OAM PDU reception process consists of a number of OAM protocol-specific PDU reception subprocesses (one for each PDU type) and a subprocess that demultiplexes OAM PDUs received from the G-ACh extraction process towards these OAM protocol-specific PDU reception subprocesses based on the ACh channel type. The details of these subprocesses are outside the scope of this Recommendation.

### **8.8.1 Proactive continuity check and connectivity verification (CC/CV)**

As described in [IETF RFC 6371], both CC and CV OAM functions are based on the proactive generation of OAM packets by the source MEP that are processed by the peer sink MEP(s).

The source MEP generates CC/CV OAM packets if it is enabled via management information. As described in [IETF RFC 6371], the CC/CV OAM packets are generated at a regular rate which is

configured by the operator via the MI\_CC\_Period. These packets are also transmitted using PHB which is configured via MI\_CC\_CoS (and that is typically the "minimum loss probability PHB").

In order to perform connectivity verification, the generated CC/CV packets also includes a globally unique source MEP identifier: the transmitted value is configured via protocol-specific management information on the source MEP while the expected value is configured via different protocol-specific management information on the sink MEP.

The sink MEP always processes received CC/CV OAM packets and detects the following CC/CV defects, as defined in clause 6.1:

- dLOC
- dUNC
- dMMG
- dUNM
- dUNP

CC/CV OAM packets pass transparently through MIPs as described in [IETF RFC 6371].

The EMF can retrieve from the sink MEP the latest CC/CV OAM packet which caused a defect condition via the MI\_Get\_SvdCC command: the CC/CV OAM packet is returned to the EMF via the MI\_SvdCC.

### **8.8.2 Remote defect indication (RDI)**

As in [IETF RFC 6371], in the case of co-routed and associated bidirectional transport paths, RDI is associated with proactive CC/CV, and the RDI indicator can be piggy-backed onto the CC/CV packet.

RDI information is carried in the CC/CV packets based upon the RI\_CC/CV\_RDI input. It is extracted in the CC/CV reception process.

In the case of unidirectional transport paths, the RDI-related OAM process is for further study.

### **8.8.3 On-demand connectivity verification (CV)**

As described in [IETF RFC 6371], on-demand CV OAM functions are based on the on-demand generation of OAM packets by the source MEP, that are processed and responded to by the peer sink MIP(s) or MEP(s).

The source MEP generates on-demand CV OAM packets when requested via protocol-specific MI signals. The results of the on-demand CV operation are returned by the source MEP using additional protocol-specific management information.

### **8.8.4 Proactive packet loss measurement (LMp)**

As described in [IETF RFC 6371], proactive LM is performed by periodically sending LM OAM packets from the initiating MEP to the target MEP and by receiving LM OAM packets from the target MEP on a co-routed bidirectional connection. Each MEP performs measurements of its transmitted and received user data packets (TxFCI and RxFCI). These measurements are then correlated in real time with the target MEP in the ME to derive the impact of packet loss on a number of performance metrics for the ME in the MEG.

For single-ended measurement:

- The initiating MEP generates proactive LM OAM request packets if MI\_LMp\_Enable is true. These packets are generated at the rate configured via the MI\_LMp\_Period and, as described in [IETF RFC 6371], with the PHB configured via MI\_LMp\_CoS that yields the lowest drop precedence within the measured PHB scheduling class, in order to maximize the reliability of measurement within the traffic class. The local value of transmitted user

data packets (TxFCI) is inserted within the LM OAM packet by the OAM PDU generation process.

- The target MEP replies to the LM OAM packets if it is enabled. The local value of the received user data packets (RxFCI) at the time the proactive LM OAM request packet has been received is passed by the OAM PDU reception process to the proactive OAM sink control process, then to the proactive OAM source control process (via RI\_OAM\_Info) and inserted by the OAM PDU generation process within the transmitted proactive LM OAM reply: the actual behaviour on how this information is passed is OAM protocol specific and therefore outside the scope of this Recommendation. The OAM PDU generation process also inserts the local value of the transmitted user data packets (TxFCI) in the reverse direction within the transmitted proactive LM OAM reply.
- The initiating MEP processes the received proactive LM OAM reply packet, together with the local value of the received used data packets (RxFCI) in the reverse direction at the time this OAM packet is received, as passed by the OAM PDU reception process, and generates LM results.
- Depending on the LMP OAM tool that is used, the LM results can be either calculated by the proactive OAM sink control process or by the proactive OAM source control process. In the latter case, the proactive OAM sink control process passes the required information in the received LM OAM reply to the proactive OAM source control process via the RI\_LMRp and receives the LM results back via the RI\_LMp\_Result. In both cases, the proactive OAM sink control process passes the LM results to the relevant performance monitoring processes within the MT\_TT\_Sk atomic function for reporting to the EMF that is described in [b-ITU-T G.8151].

For dual-ended measurement:

- The initiating MEP generates proactive LM OAM request packets if it is enabled to do so via management information. These packets are generated at the rate configured via the MI\_LMp\_Period and, as described in [IETF RFC 6371], with the PHB configured via MI\_LMp\_CoS that yields the lowest drop precedence within the measured PHB scheduling class, in order to maximize the reliability of measurement within the traffic class. The local value of transmitted user data packets (TxFCI) is inserted within the LM OAM packet by the OAM PDU generation process.
- The target MEP receives LM OAM packets if it is enabled to do so via management information. The local value of the received user data packets (RxFCI) at the time the proactive LM OAM request packet has been received is passed by the OAM PDU reception process to the proactive OAM sink control process and generates LM results.

Proactive LM OAM packets pass transparently through MIPs as described in [IETF RFC 6371].

### **8.8.5 On-demand packet loss measurement (LMO)**

As described in [IETF RFC 6371], on-demand packet loss measurement (LMO) is performed by the command that sends LM OAM packets from the initiating MEP to the target MEP and by receiving LM OAM packets from the target MEP on a co-routed bidirectional connection. The initiating MEP performs measurements of its transmitted and received user data packets (TxFCI and RxFCI). These measurements are then correlated in real time with values received from the target MEP in the ME to derive the impact of packet loss for the ME in the MEG.

For single-ended measurement:

- The initiating MEP generates on-demand LM OAM request packets when enabled via management information. These packets are generated with the PHB configured via management information that yields the lowest drop precedence within the measured PHB scheduling class, in order to maximize the reliability of measurement within the traffic

class. The local value of transmitted user data packets (TxFCI) is inserted within the LM OAM packet by the OAM PDU generation process.

- The target MEP replies to the LM OAM packets if enabled via management information. The local value of the received user data packets (RxFCI) at the time the on-demand LM OAM request packet has been received is passed by the OAM PDU reception process to the on-demand OAM sink control process, then to the on-demand OAM source control process (via RI\_OAM\_Info) and inserted by the OAM PDU generation process within the transmitted on-demand LM OAM reply: the actual behaviour on how this information is passed is OAM protocol specific and therefore outside the scope of this Recommendation. The OAM PDU generation process also inserts the local value of the transmitted user data packets (TxFCI) in the reverse direction within the transmitted on-demand LM OAM reply.
- The initiating MEP processes the received on-demand LM OAM reply packet, together with the local value of the received used data packets (RxFCI) in the reverse direction at the time that this OAM packet is received, as passed by the OAM PDU reception process, and it generates LM results.
- Depending on the LMo OAM tool that is used, the LM results can be either calculated by the on-demand OAM sink control process or by the on-demand OAM source control process. In both cases, the LM results are reported to the EMF by the MTDe\_TT\_So by the protocol-specific management information.

For dual-ended measurement:

- The initiating MEP generates on-demand LM OAM request packets when enabled via management information. These packets are generated at the rate configured via the MI\_LMo\_Period and, as described in [IETF RFC 6371], with the PHB configured via MI\_LMo\_CoS that yields the lowest drop precedence within the measured PHB scheduling class, in order to maximize the reliability of measurement within the traffic class. The local value of transmitted user data packets (TxFCI) is inserted within the LM OAM packet by the OAM PDU generation process.
- The target MEP receives LM OAM packets when enabled via management information. The local value of the received user data packets (RxFCI) at the time the on-demand LM OAM request packet has been received is passed by the OAM PDU reception process to the on-demand OAM sink control process and generates LM results.

On demand LM OAM packets pass transparently through MIPs as described in [IETF RFC 6371].

### **8.8.6 Proactive packet delay measurement (DMp)**

As described in [IETF RFC 6371], proactive packet delay measurement (DMp) is performed by periodically sending DM OAM packets from the initiating MEP to the target MEP and by receiving DM OAM packets from the target MEP on a co-routed bidirectional connection. Each MEP records its transmitted and received time stamps. The time stamps from the initiating and target MEPs are then correlated to derive a number of performance metrics relating to delay for the ME in the MEG.

For single-ended measurement:

- The initiating MEP generates proactive DM OAM request packets if MI\_DMp\_Enable is true. These packets are generated at the rate configured via the MI\_DMp\_Period and, as described in [IETF RFC 6371], with the PHB configured via MI\_DMp\_CoS that yields the lowest drop precedence within the measured PHB scheduling class, in order to maximize the reliability of measurement within the traffic class. The local value of a transmitted time stamp (TxTimeStampf) is inserted within the DM OAM packet by the OAM PDU generation process.
- The target MEP replies to the DM OAM packets if enabled by management information. The local value of the received time stamp (RxTimeStampl) at the time the proactive DM

OAM request packet has been received is passed by the OAM PDU reception process to the proactive OAM sink control process, then to the proactive OAM source control process (via RI\_OAM\_Info) and inserted by the OAM PDU generation process within the transmitted proactive DM OAM reply: the actual behaviour on how this information is passed is OAM protocol specific and therefore outside the scope of this Recommendation. The OAM PDU generation process also inserts the local value of the transmitted time stamp (TxTimeStampf) in the reverse direction within the transmitted proactive DM OAM reply.

- The initiating MEP processes the received proactive DM OAM reply packet, together with the local value of the received time stamp (RxTimeStampl) in the reverse direction at the time this OAM packet is received, as passed by the OAM PDU reception process, and it generates DM results.
- Depending on the DMp OAM tool that is used, the DM results can be either calculated by the proactive OAM sink control process or by the proactive OAM source control process. In the latter case, the proactive OAM sink control process passes the required information in the received DM OAM reply to the proactive OAM source control process via the RI\_DMp and receives the DM results back via the RI\_DMp\_Result. In both cases, the proactive OAM sink control process passes the DM results to the relevant performance monitoring processes within the MT\_TT\_Sk atomic function for reporting to the EMF.

For dual-ended measurement:

- The initiating MEP generates proactive DM OAM request packets if enabled by management information. These packets are generated at the rate configured via the MI\_DMp\_Period and, as described in [IETF RFC 6371], with the PHB configured via MI\_DMp\_CoS that yields the lowest drop precedence within the measured PHB scheduling class, in order to maximize the reliability of measurement within the traffic class. The local value of transmitted time stamp (TxTimeStampl) is inserted within the DM OAM packet by the OAM PDU generation process.
- The target MEP receives DM OAM packets if enabled by management information. The local value of the received time stamp (RxTimeStampl) at the time the proactive DM OAM request packet has been received is passed by OAM PDU reception process to the proactive OAM sink control process which generates DM results.

Proactive DM OAM packets pass transparently through MIPs as described in [IETF RFC 6371].

### **8.8.7 On-demand packet delay measurement (DMo)**

As described in [IETF RFC 6371], on-demand packet delay measurement (DMo) is performed by the command that sends DM OAM packets from the initiating MEP to the target MEP and by receiving DM OAM packets from the target MEP on a co-routed bidirectional connection. Each MEP records its transmitted and received time stamps. The time stamps from the initiating and target MEPs are then correlated to derive a number of performance metrics relating to delay for the ME in the MEG.

For single-ended measurement:

- The initiating MEP generates on-demand DM OAM request packets if MI\_DMp\_Enable is true. These packets are generated with the PHB configured via MI\_DMp\_CoS that yields the lowest drop precedence within the measured PHB scheduling class, in order to maximize the reliability of measurement within the traffic class. The local value of a transmitted time stamp (TxTimeStampf) is inserted within the DM OAM packet by the OAM PDU generation process. The target MEP replies to the PM OAM packets if the MI\_PMo\_Enable is true. The local value of the received time stamp (RxTimeStampl) at the time the on-demand PM OAM request packet has been received is passed by the OAM PDU reception process to the on-demand OAM sink control process, then to the on-demand OAM source control process (via RI\_OAM\_Info) and inserted by the OAM PDU

generation process within the transmitted on-demand DM OAM reply: the actual behaviour on how this information is passed is OAM protocol specific and therefore outside the scope of this Recommendation.

- The initiating MEP processes the received on-demand DM OAM reply packet, together with the local value of the received time stamp (RxTimestamp) in the reverse direction at the time this OAM packet is received, as passed by the OAM PDU reception process, and it generates DM results.
- Depending on the DMO OAM tool that is used, the DM results can be either calculated by the on-demand OAM sink control process or by the on-demand OAM source control process. In both cases, the DM results are reported to the EMF by the MTDe\_TT\_So by the protocol-specific management information.

For dual-ended measurement:

- The initiating MEP generates on-demand DM OAM request packets if MI\_DMo\_Enable is true. These packets are generated at the rate configured via the MI\_DMo\_Period and, as described in [IETF RFC 6371], with the PHB configured via MI\_DMo\_CoS that yields the lowest drop precedence within the measured PHB scheduling class, in order to maximize the reliability of measurement within the traffic class. The local value of a transmitted time stamp (TxTimestamp) is inserted within the DM OAM packet by the OAM PDU generation process.
- The target MEP receives DM OAM packets if the MI\_DMo\_Enable is true. The local value of the received time stamp (RxTimestamp) at the time the on-demand DM OAM request packet has been received is passed by the OAM PDU reception process to the on-demand OAM sink control process and it generates DM results.

On-demand DM OAM packets pass transparently through MIPs as described in [IETF RFC 6371].

### **8.8.8 Throughput test**

For a dual-ended throughput test:

- As described in [IETF RFC 6371], out of service on demand throughput estimation can be performed by sending OAM test packets at an increasing rate (up to the theoretical maximum), computing the percentage of OAM test packets received and reporting the rate at which OAM test packets begin to drop. In general, this rate is dependent on the OAM test packet size.
- The source MEP starts generating test packets when requested via protocol-specific management information and continues generating these packets at the configured period until requested to stop; at this time the number of sent packets is reported via protocol-specific management information.
- The sink MEP, when enabled via protocol-specific management information, starts processing the received OAM test packets until the test is terminated; at this time, the calculated test results are reported.

For a single-ended throughput test:

- For further study.

### **8.8.9 Route tracing (RT)**

For further study.

### **8.8.10 LCK/AIS reception**

As described in [IETF RFC 6371], when an MEP detects a signal fail condition or it is locked, it may transmit AIS or lock messages at the client layer, respectively. These are transmitted by the AIS insert process (see clause 8.6.2) or the LCK generate process (see clause 8.6.3), and are

received by the LCK/AIS reception subprocess in the Proactive OAM sink control process. The receipt of an AIS or lock message triggers the dAIS or dLCK defects respectively, as described in clause 6.1.

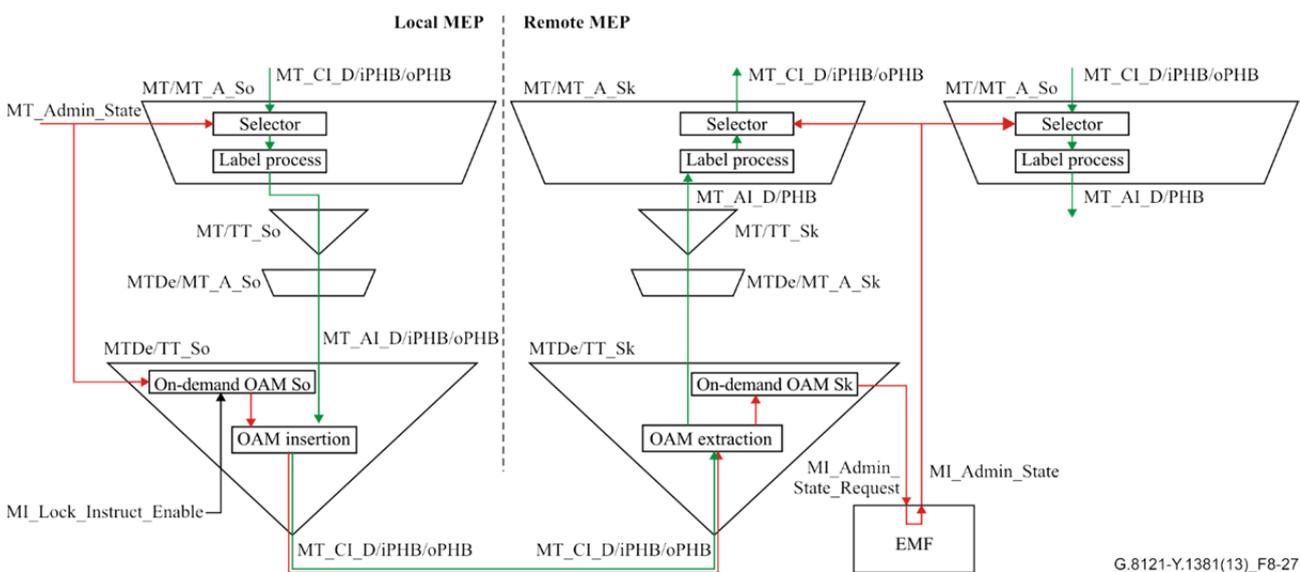
### 8.8.11 Lock instruct processes

As described in [IETF RFC 6371], when an MEP is administratively locked, it puts the local MPLS-TP trail into a locked state and, if enabled by the MI\_Lock\_Instruct\_Enable, it also starts transmitting a lock instruct message to its peer MEP. The locking of the trail at the local MEP is performed by the selector process defined in clause 8.6.1; the transmission of lock instruct (LKI) messages is performed by the on-demand OAM process.

On receiving an LKI message, the peer MEP must also lock the path.

The lock instruct message is received by the on-demand OAM process and the request is signalled to the EMF via MI\_Admin\_State\_Request. The EMF should then combine this remote request with any local request from the user, and set MI\_Admin\_State accordingly in the corresponding MT/MT\_A\_So and MT/MT\_A\_Sk processes.

The figure below illustrates how setting MI\_Admin\_State on the local MEP both locks the local selector processes (the local selector in the MT/MT\_A\_Sk is not shown), and triggers the generation of LKI messages to the remote MEP that cause the two remote selector processes to also be locked.



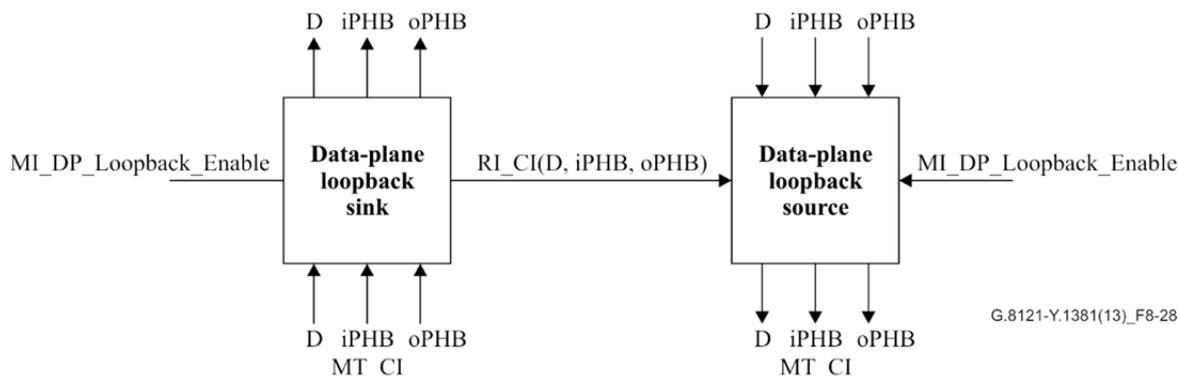
**Figure 8-27 – Overview of the processes involved with lock instruct**

NOTE 1 – It is important that the remote MEP is locked as soon as possible after receiving the first lock instruct message; therefore the EMF must handle the MI\_Admin\_State\_Request with high priority to ensure it is reflected in MI\_Admin\_State as quickly as possible.

NOTE 2 – The EMF must combine any local lock request from the user with the received MI\_Admin\_State\_Request, when setting MI\_Admin\_State in the MT/MT\_A\_So and MT/MT\_A\_Sk functions. However, when setting MI\_Admin\_State in the MTDe\_TT\_So function, the EMF must only do so based on a local request from the user. This is to prevent a deadlock situation where receipt of a lock instruct message causes transmission of a lock instruct message in the opposite direction.

### 8.9 Data-plane loopback processes

As described in [IETF RFC 6435], the data-plane loopback process control looping back of all traffic (i.e., both data and OAM frames), under management control. An overview is shown in Figure 8-28.



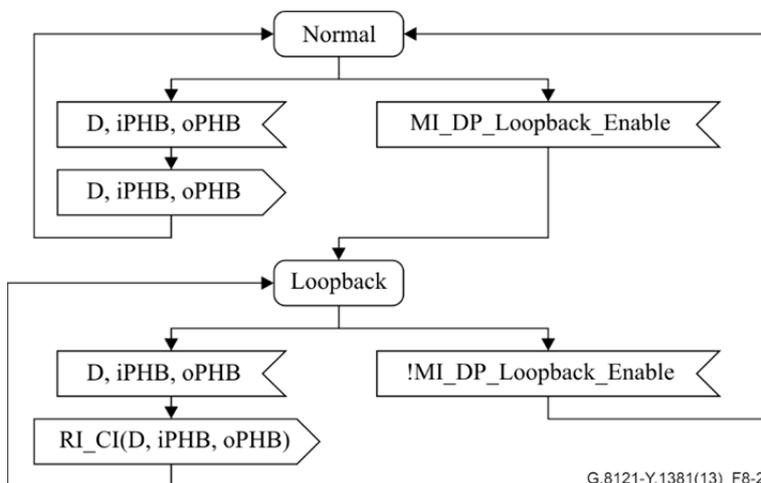
G.8121-Y.1381(13)\_F8-28

**Figure 8-28 – Overview of the processes involved with data-plane loopback**

NOTE – The data-plane loopback processes are included in the MEP diagnostic trail termination function (MTDe\_TT, see clause 9.4.1.1) and the MIP diagnostic trail termination function (MTDi\_TT, see clause 9.4.2.1). Data-plane loopback may also be enabled on a transport path at a point where there is no MEP or MIP; the location of the data-plane loopback processes in this case is for further study.

### 8.9.1 Data-plane loopback sink processes

The data-plane loopback sink process is described by Figure 8-29.



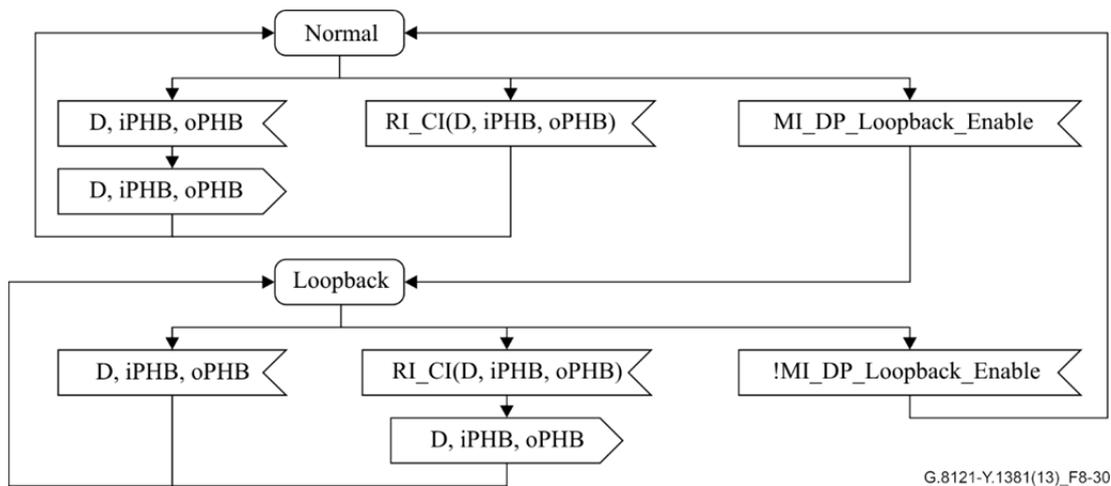
G.8121-Y.1381(13)\_F8-29

**Figure 8-29 – Data-plane loopback sink process**

In Normal state, data traffic passes through the process unmodified; in Loopback state, it is intercepted and sent to the data-plane loopback source process via RI\_CI.

### 8.9.2 Data-plane loopback source processes

The data-plane loopback source process is described by Figure 8-30.



**Figure 8-30 – Data-plane loopback source process**

In Normal state, data traffic passes through the process unmodified; in Loopback state, data traffic is dropped, and data received via RI\_CI from the data-plane loopback sink process is transmitted instead.

## 9 MPLS-TP layer functions

Figure 9 illustrates the MPLS-TP layer network and server and client layer adaptation functions. The information crossing the MPLS-TP connection point (MT\_CP) is referred to as the MPLS-TP characteristic information (MT\_CI), and consists of:

- MT\_CI\_D (containing the 'S' bit, TTL, and MPLS payload)
- MT\_CI\_iPHB
- MT\_CI\_oPHB
- MT\_CI\_SSF
- MT\_CI\_APS
- MT\_CI\_LStack

These are described in clause 6.1.2 of [ITU-T G.8110.1] except for MT\_CI\_LStack, which contains a copy of the complete label stack received from the network.

The information crossing the MPLS-TP access point (MT\_AP) is referred to as the MPLS-TP adapted information (MT\_AI), and consists of:

- MT\_AI\_D (containing the 'S' bit and MPLS payload)
- MT\_AI\_PHB
- MT\_AI\_TSF
- MT\_AI\_TSD
- MT\_AI\_AIS
- MT\_AI\_LStack

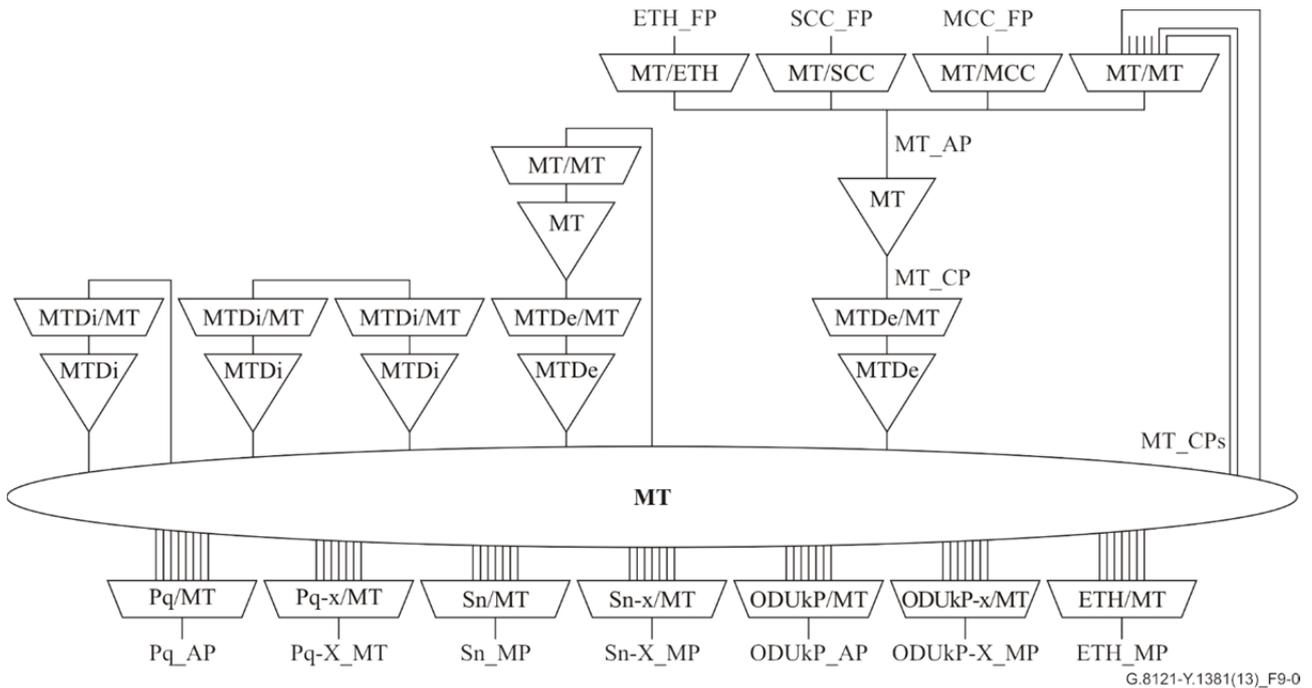
These are described in clause 6.1.1 of ITU-T [G.8110.1] except for MT\_AI\_LStack, which is as above, and MT\_AI\_AIS, which indicates an alarm indication signal (AIS) condition.

The information crossing the MPLS-TP diagnostic access points (MTDe\_AP and MTDi\_AP) is referred to as MPLS-TP diagnostic adapted information (MTDe\_AI and MTDi\_AI respectively). These both consist of the same signals, with the same definitions as MT\_CI.

NOTE – MTDe\_AI and MTDi\_AI are not the same as MT\_AI. In particular, MTDe\_AI\_D and MTDi\_AI\_D contain a TTL field (whereas MT\_AI\_D does not); and MTDe\_AI and MTDi\_AI contain separate iPHB and oPHB signals (whereas MT\_AI contains a single PHB signal).

The MPLS-TP layer network provides embedded hierarchy via the label stacking mechanism. This is represented in the model by MPLS-TP tunnel sublayers, which contain MT\_TT and MT/MT\_A functions. The figure shows a generic example for the connection of the MPLS-TP tunnel functions. It is not required to connect them via an MT\_C function; they can be directly inserted without a connection function. It is noted that this Recommendation only defines Ethernet for the client of MPLS-TP as an MT/ETH adaptation function.

This mechanism (MPLS-TP tunnel sublayers) is also used when sublayer (tandem connection) monitoring is required.



**Figure 9-0 – MPLS-TP atomic functions**

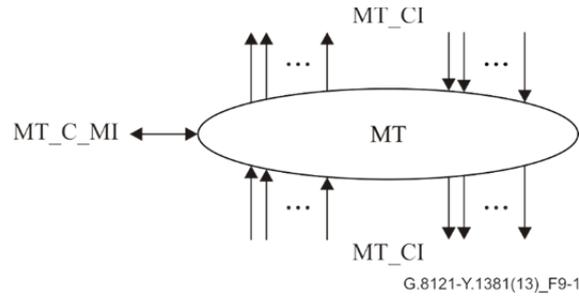
### 9.1 Connection functions (MT\_C)

MT\_C is the function that assigns MPLS packets at its input ports to MPLS-TP packets at its output ports.

The MT\_C connection process is a unidirectional function as illustrated in Figure 9-1. The signal formats at the input and output ports of the function are similar, differing only in the logical sequence of the MPLS-TP packets. As the process does not affect the nature of the characteristic information of the signal, the reference point on either side of the MT\_C function is the same, as illustrated in Figure 9-1.

Incoming MPLS-TP packets at the MT\_CP are assigned to available outgoing MPLS-TP capacity at the MT\_CP.

**Symbol:**



**Figure 9-1 – MT\_C symbol**

**Interfaces:**

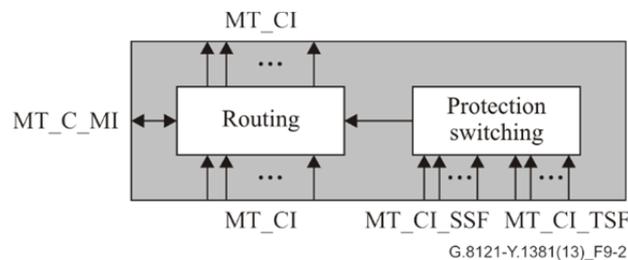
**Table 9-1 – MT\_C input and output signals**

Input(s)	Output(s)
Per MT_CP, $n \times$ for the function: MT_CI_D MT_CI_iPHB MT_CI_oPHB MT_CI_SSF MT_AI_TSF Per input and output connection point: <i>for further study</i> Per matrix connection: MT_C_MI_ConnectionType MT_C_MI_Return_CP_ID MT_C_MI_ConnectionPortIds Per SNC protection group: <i>for further study</i>	Per MT_CP, $m \times$ per function: MT_CI_D MT_CI_iPHB MT_CI_oPHB MT_CI_SSF

**Processes:**

In the MT\_C function MPLS-TP characteristic information is routed between input (termination) connection points ((T)CPs) and output (T)CPs by means of matrix connections. (T)CPs may be allocated within a protection group.

NOTE – Neither the number of input/output signals to the connection function nor the connectivity is specified in this Recommendation. This is a property of individual network elements.



**Figure 9-2 – MT\_C process diagram**

*Routing process:*

This process passes all the traffic units received from a specific input to the corresponding output according to the matrix connection between the specified input and output.

Each (matrix) connection in the MT\_C function shall be characterized by the:

Type of connection (MI_ConnectionType):	Unprotected, protected
Traffic direction (MI_Return_CP_ID):	Unidirectional if NULL, otherwise it identifies the CP of the return connection (Note)
Input and output connection points (MI_ConnectionPortIDs):	Set of connection point identifiers
NOTE – Bidirectional LSPs are supported by associating two unidirectional LSPs in the data plane, as per [ITU-T G.8110.1].	

– *Protection switching process:*

For further study.

**Performance monitoring:** None.

**Defects:** None.

**Consequent actions:** If an output of this function is not connected to one of its inputs, the connection function shall send no traffic units and SSF = false to the output.

**Defect correlations:** None.

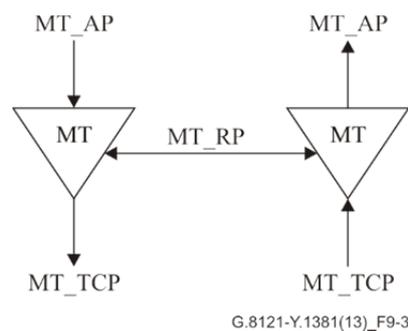
### 9.1.1 Subnetwork connection protection process

For further study.

## 9.2 Termination functions

### 9.2.1 MPLS-TP trail termination function (MT\_TT)

The bidirectional MPLS-TP trail termination (MT\_TT) function terminates the MPLS-TP OAM to determine the status of the MPLS-TP (sub)layer trail. The MT\_TT function is performed by a co-located pair of the MPLS-TP trail termination source (MT\_TT\_So) and sink (MT\_TT\_Sk) functions as shown in Figure 9-3.



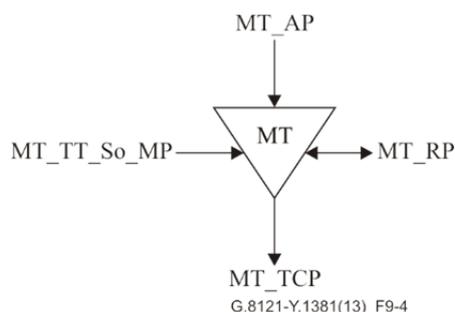
**Figure 9-3 – MT\_TT**

#### 9.2.1.1 MPLS-TP trail termination source function (MT\_TT\_So)

The MT\_TT\_So function determines and inserts the TTL value in the shim header TTL field and adds MPLS-TP OAM for proactive monitoring to the MT\_AI signal at its MT\_AP.

**Symbol:**

The MT\_TT\_So function symbol is shown in Figure 9-4.



**Figure 9-4 – MT\_TT\_So function**

**Interfaces:**

**Table 9-2 – MT\_TT\_So inputs and outputs**

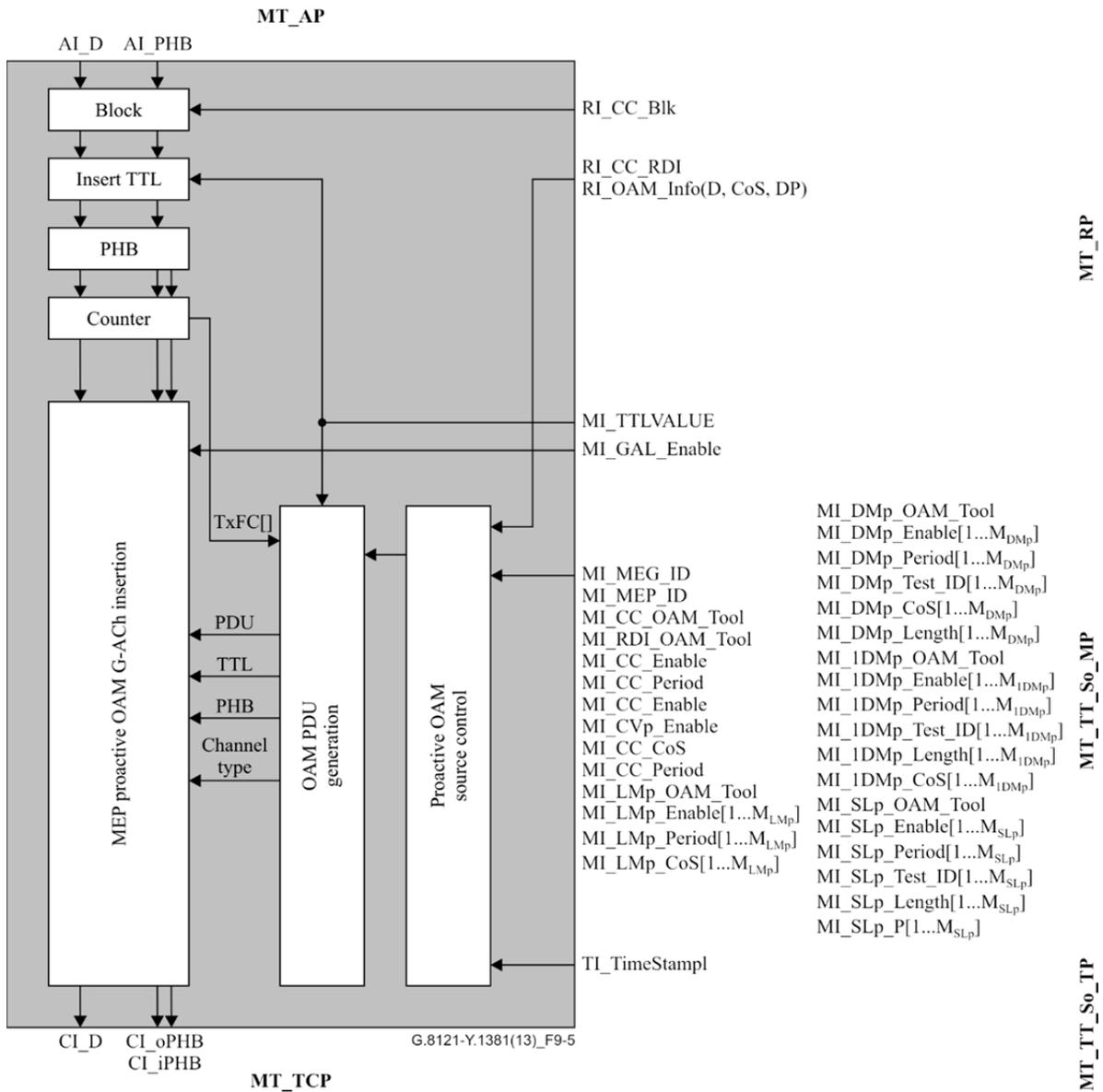
Input(s)	Output(s)
<p><b>MT_AP:</b>            MT_AI_D            MT_AI_PHB             MT_AI_LStack  <b>MT_RP:</b>            MT_RI_CC_RDI            MT_RI_CC_BlK            MT_RI_OAM_Info(D,CoS,DP)   <b>MT_TT_So_MP:</b>            MT_TT_So_MI_GAL_Enable            MT_TT_So_MI_TTLVALUE            MT_TT_So_MI_MEG_ID            MT_TT_So_MI_MEP_ID             MT_TT_So_MI_CC_OAM_Tool            MT_TT_So_MI_RDI_OAM_Tool            MT_TT_So_MI_CC_Enable (Note)            MT_TT_So_MI_CVp_Enable (Note)            MT_TT_So_MI_CC_CoS             MT_TT_So_MI_CC_Period             MT_TT_So_MI_LMp_OAM_Tool            MT_TT_So_MI_LMp_Enable[1...M<sub>LMp</sub>]            MT_TT_So_MI_LMp_Period[1...M<sub>LMp</sub>]            MT_TT_So_MI_LMp_CoS[1...M<sub>LMp</sub>]             MT_TT_So_MI_DMp_OAM_Tool            MT_TT_So_MI_DMp_Enable[1...M<sub>DMp</sub>]            MT_TT_So_MI_DMp_Period[1...M<sub>DMp</sub>]            MT_TT_So_MI_DMp_Test_ID[1...M<sub>DMp</sub>]            MT_TT_So_MI_DMp_CoS[1...M<sub>DMp</sub>]            MT_TT_So_MI_DMp_Length[1...M<sub>DMp</sub>]</p>	<p><b>MT_TCP:</b>            MT_CI_D            MT_CI_oPHB            MT_CI_iPHB            MT_CI_LStack  <b>MT_RP:</b></p>

**Table 9-2 – MT\_TT\_So inputs and outputs**

Input(s)	Output(s)
<p>MT_TT_So_MI_1DMp_OAM_Tool            MT_TT_So_MI_1DMp_Enable[1...M<sub>1DMp</sub>]            MT_TT_So_MI_1DMp_Period[1...M<sub>1DMp</sub>]            MT_TT_So_MI_1DMp_Test_ID[1...M<sub>1DMp</sub>]            MT_TT_So_MI_1DMp_Length[1...M<sub>1DMp</sub>]            MT_TT_So_MI_1DMp_CoS[1...M<sub>1DMp</sub>]</p> <p>MT_TT_So_MI_SLp_OAM_Tool            MT_TT_So_MI_SLp_Enable[1...M<sub>SLp</sub>]            MT_TT_So_MI_SLp_Period[1...M<sub>SLp</sub>]            MT_TT_So_MI_SLp_Test_ID[1...M<sub>SLp</sub>]            MT_TT_So_MI_SLp_Length[1...M<sub>SLp</sub>]            MT_TT_So_MI_SLp_CoS[1...M<sub>SLp</sub>]</p> <p><b>MT_TP:</b>            MT_TT_So_TI_TimeStampI</p>	
<p>NOTE – MI_CC_Enable and MI_CVp_Enable are used to enable CC and CV functions respectively.            The possible combinations are:</p> <ul style="list-style-type: none"> <li>– no CC function and no CV function: MI_CC_Enable = false and MI_CVp_Enable = false</li> <li>– CC-only function: MI_CC_Enable = true and MI_CVp_Enable = false</li> <li>– CC and CV functions: MI_CC_Enable = true and MI_CVp_Enable = true.</li> </ul>	

**Processes:**

The processes associated with the MT\_TT\_So function are depicted in Figure 9-5.



**Figure 9-5 – MT\_TT\_So process diagram**

NOTE 1 – The interface between proactive OAM control and OAM PDU generation is protocol specific.

NOTE 2 – Note that the parameters and values in the MT\_TT\_So\_MI\_XX\_OAM\_Tool are outside the scope of this Recommendation.

**Block:** When RI\_CC\_BlK is raised, the block process will discard all AI\_D traffic units it receives. If RI\_CC\_BlK is cleared, the received AI\_D traffic units will be passed to the output port.

**Insert TTL:** The time-to-live value is inserted in the outer shim header's TTL field within the MT\_AI traffic unit.

**PHB:** The AI\_PHB signal is assigned to both the CI\_iPHB and CI\_oPHB signals at the MT\_TCP reference point.

**Counter:** For further study.

**MEP proactive OAM G-ACh insertion:** See clause 8.1.

**OAM PDU generation:** See clause 8.8.

**Proactive OAM source control:** See clause 8.8.

**Defects:** None.

**Consequent actions:** None.

**Defect correlations:** None.

**Performance monitoring:** None.

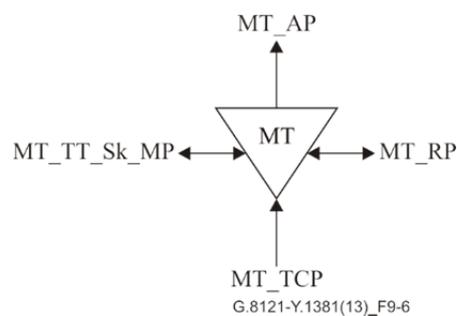
### 9.2.1.2 MPLS-TP trail termination sink function (MT\_TT\_Sk)

The MT\_TT\_Sk function reports the state of the MPLS-TP trail (network connection). It extracts MPLS-TP trail OAM for proactive monitoring from the MPLS-TP signal at its MT\_TCP, detects defects, counts during 1-second periods errors and defects to feed performance monitoring when connected and forwards the defect information as backward indications to the companion MT\_TT\_So function.

NOTE – The MT\_TT\_Sk function extracts and processes one level of MPLS-TP OAM irrespective of the presence of more levels.

#### Symbol:

The MT\_TT\_Sk function symbol is shown in Figure 9-6.



**Figure 9-6 – MT\_TT\_Sk function**

**Interfaces:**

**Table 9-3 – MT\_TT\_Sk inputs and outputs**

Input(s)	Output(s)
<p><b>MT_TCP:</b>            MT_CI_D            MT_CI_iPHB            MT_CI_oPHB            MT_CI_SSF            MT_CI_Lstack</p> <p><b>MT_RP:</b></p> <p><b>MT_TT_Sk_MP:</b>            MT_TT_Sk_MI_GAL_Enable            MT_TT_Sk_MI_MEG_ID            MT_TT_Sk_MI_PeerMEP_ID            MT_TT_Sk_MI_CC_OAM_Tool            MT_TT_Sk_MI_RDI_OAM_Tool</p> <p>MT_TT_Sk_MI_CC_Enable (Note)            MT_TT_Sk_MI_CVp_Enable (Note)</p> <p>MT_TT_Sk_MI_CC_Period            MT_TT_Sk_MI_CC_CoS</p> <p>MT_TT_Sk_MI_Get_SvdCC</p> <p>MT_TT_Sk_MI_LMp_OAM_Tool            MT_TT_Sk_MI_LMp_Enable[1... M<sub>LMp</sub>]            MT_TT_Sk_MI_LMp_CoS[1... M<sub>LMp</sub>]            MT_TT_Sk_MI_LM_DEGM            MT_TT_Sk_MI_LM_M            MT_TT_Sk_MI_LM_DEGTHR            MT_TT_Sk_MI_LM_TFMIN</p> <p>MT_TT_Sk_MI_DMp_OAM_Tool            MT_TT_Sk_MI_DMp_Enable[1... M<sub>DMp</sub>]            MT_TT_Sk_MI_DMp_CoS[1... M<sub>DMp</sub>]</p> <p>MT_TT_Sk_MI_1DMp_OAM_Tool            MT_TT_Sk_MI_1DMp_Enable[1... M<sub>1DMp</sub>]            MT_TT_Sk_MI_1DMp_Test_ID[1... M<sub>1DMp</sub>]            MT_TT_Sk_MI_SLp_OAM_Tool            MT_TT_Sk_MI_SLp_Enable[1... M<sub>SLp</sub>]            MT_TT_Sk_MI_SLp_CoS[1... M<sub>SLp</sub>]</p> <p>MT_TT_Sk_MI_AIS_OAM_Tool</p>	<p><b>MT_AP:</b>            MT_AI_D            MT_AI_PHB            MT_AI_TSF            MT_AI_TSD            MT_AI_AIS</p> <p>MT_AI_LStack</p> <p><b>MT_RP:</b>            MT_RI_CC_RDI            MT_RI_CC_BlK</p> <p>MT_RI_OAM_Info(D,CoS,DP)</p> <p><b>MT_TT_Sk_MP:</b>            MT_TT_Sk_MI_SvdCC            MT_TT_Sk_MI_cSSF            MT_TT_Sk_MI_cLCK            MT_TT_Sk_MI_cLOC            MT_TT_Sk_MI_cMMG            MT_TT_Sk_MI_cUNM            MT_TT_Sk_MI_cUNP</p> <p>MT_TT_Sk_MI_cUNC</p> <p>MT_TT_Sk_MI_cDEG            MT_TT_Sk_MI_cRDI            MT_TT_Sk_MI_pN_LF[1...P]            MT_TT_Sk_MI_pN_TF[1...P]            MT_TT_Sk_MI_pF_LF[1...P]            MT_TT_Sk_MI_pF_TF[1...P]            MT_TT_Sk_MI_pF_DS            MT_TT_Sk_MI_pN_DS            MT_TT_Sk_MI_pB_FD[1...P]            MT_TT_Sk_MI_pB_FDV[1...P]            MT_TT_Sk_MI_pN_FD[1...P]            MT_TT_Sk_MI_pN_FDV[1...P]            MT_TT_Sk_MI_pF_FD[1...P]            MT_TT_Sk_MI_pF_FDV[1...P]</p>



NOTE – The parameters and values in the MT\_TT\_Sk\_MI\_XX\_OAM\_Tool are outside the scope of this Recommendation.

**Extract TTL:** The time-to-live value is extracted from the outer shim header's TTL field within the MT\_CI traffic unit.

**Block:** When the aBlock consequent action is asserted, this process drops all traffic units arriving at its input.

**PHB:** The CI\_oPHB signal is assigned to the AI\_PHB signal at the reference point MT\_AP.

Note that the CI\_iPHB signal is not used by any of the processes in the function.

**Counter:** For further study.

**MEP proactive OAM G-ACh extraction:** See clause 8.1.3. The process of CI\_D is made according to the following rule:

```
if ( ((MI_GAL_Enable && MT-label(D) == GAL) ||
      (!MI_GAL_Enable && 1stNibble(D) == 0b0001)) &&
      (Packet_Type(D) == Proactive_OAM) )
    forward to G-ACh port
} else {
    forward to data port
}
```

NOTE – MT-label(D) and 1stNibble(D) are functions which return, respectively, the Label field at the top of the stack within the MPLS payload, the first nibble of the MPLS payload as defined in Figure 6-3 of [ITU-T G.8110.1].

These fields are used to identify G-ACh packets as described in section 4.2.1 of [IETF RFC 5586]. Packet\_Type (D) is a protocol-specific function that determines whether the traffic unit contains a proactive OAM packet.

**OAM PDU reception:** See clause 8.8.

**Proactive OAM sink control:** See clause 8.8.

**Performance counter:** For further study.

#### **Defects generation:**

This function detects and clear the defects (dLOC, dMMG, dUNM, dDEG, dUNP, dUNC, dRDI, dAIS and dLCK), as defined in clause 6.1.

#### **Consequent actions:**

aBLK ← (dMMG or dUNM)

aTSF ← (dLOC and MI\_CC\_Enable) or (dAIS and not(MI\_CC\_Enable)) or (dLCK and not(MI\_CC\_Enable)) or dMMG or dUNM or CI\_SSF

aTSD ← dDEG and (not aTSF)

aAIS ← aTSF

aRDI ← aTSF

#### **defect correlations:**

cLOC ← dLOC and (not dAIS) and (not dLCK) and (not CI\_SSF) and (MI\_CC\_Enable)

cMMG ← dMMG

cUNM ← dUNM

cDEG ← dDEG and (not dAIS) and (not dLCK) and (not CI\_SSF) and (not (dLOC or dMMG or dUNM)) and (MI\_CC\_Enable))

cUNP ← dUNP

cUNC ← dUNC

cRDI ← dRDI and (MI\_CC\_Enable)

cSSF ← CI\_SSF or dAIS

cLCK ← dLCK and (not dAIS)

### Performance monitoring:

For further study.

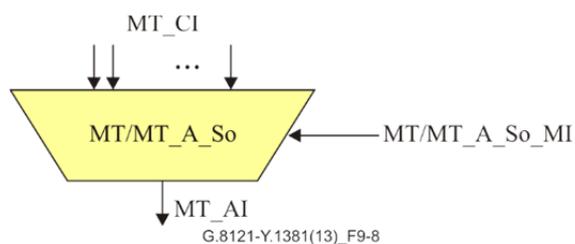
## 9.3 Adaptation functions

### 9.3.1 MPLS-TP to MPLS-TP adaptation function (MT/MT\_A)

#### 9.3.1.1 MPLS-TP to MPLS-TP adaptation source function (MT/MT\_A\_So)

#### Symbol:

The MT/MT\_A\_So function symbol is shown in Figure 9-8. This function maps client MT\_CI traffic units into server MT\_AI traffic units.



**Figure 9-8 – MT/MT\_A\_So function**

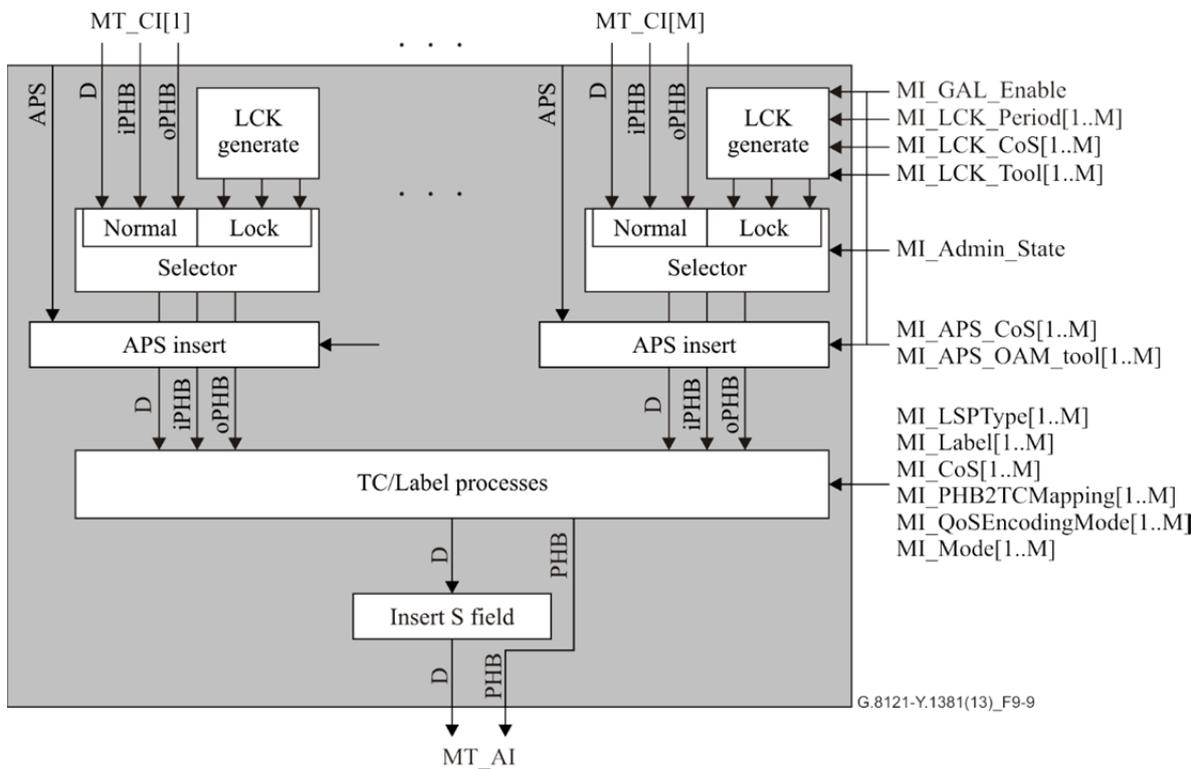
**Interfaces:**

**Table 9-4 – MT/MT\_A\_So interfaces**

Inputs	Outputs
<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB            MT_CI_APS</p> <p><b>MT/MT_A_So_MI:</b>            MT/MT_A_So_MI_Admin_State            MT/MT_A_So_MI_Label[1...M]            MT/MT_A_So_MI_LSPTType[1...M]            MT/MT_A_So_MI_CoS[1...M]            MT/MT_A_So_MI_PHB2TCMapping[1...M]            MT/MT_A_So_MI_QoSEncodingMode[1...M]            MT/MT_A_So_MI_Mode</p> <p>MT/MT_A_So_MI_LCK_Period[1...M]            MT/MT_A_So_MI_LCK_CoS[1...M]            MT/MT_A_So_MI_LCK_OAM_Tool[1...M]            MT/MT_A_So_MI_GAL_Enable[1...M]            MT/MT_A_So_MI_APS_CoS[1...M]            MT/MT_A_So_MI_APS_OAM_Tool[1...M]</p>	<p><b>MT_AP:</b>            MT_AI_Data            MT_AI_PHB</p>

**Processes:**

The processes associated with the MT/MT\_TT\_So function are depicted in Figure 9-9.



**Figure 9-9 – MT/MT\_A\_So process diagram**

– *LCK generate process:*

See clause 8.6.3. Each CP has its LCK generate process.

– *Selector process:*

See clause 8.6.1. The normal CI is blocked if Admin\_State = LOCKED.

– *TC/Label processes:*

See clause 8.2.1.

– *S Field insertion:*

A 1-bit S field set to 0 (not bottom of label stack) is inserted to indicate the client is MPLS.

**Defects:** None.

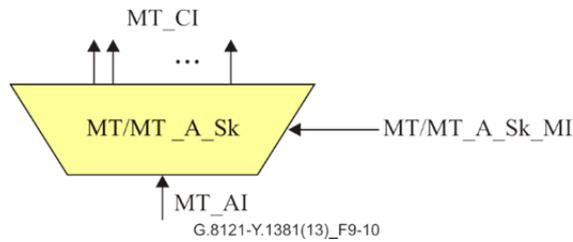
**Consequent actions:** None.

**Defect correlations:** None.

**Performance monitoring:** None.

### 9.3.1.2 MPLS-TP to MPLS-TP adaptation sink function (MT/MT\_A\_Sk)

The MT/MT\_A\_Sk function symbol is shown in Figure 9-10. This function retrieves client MT\_CI traffic units from server MT\_AI traffic units.



**Figure 9-10 – MT/MT\_A\_Sk function**

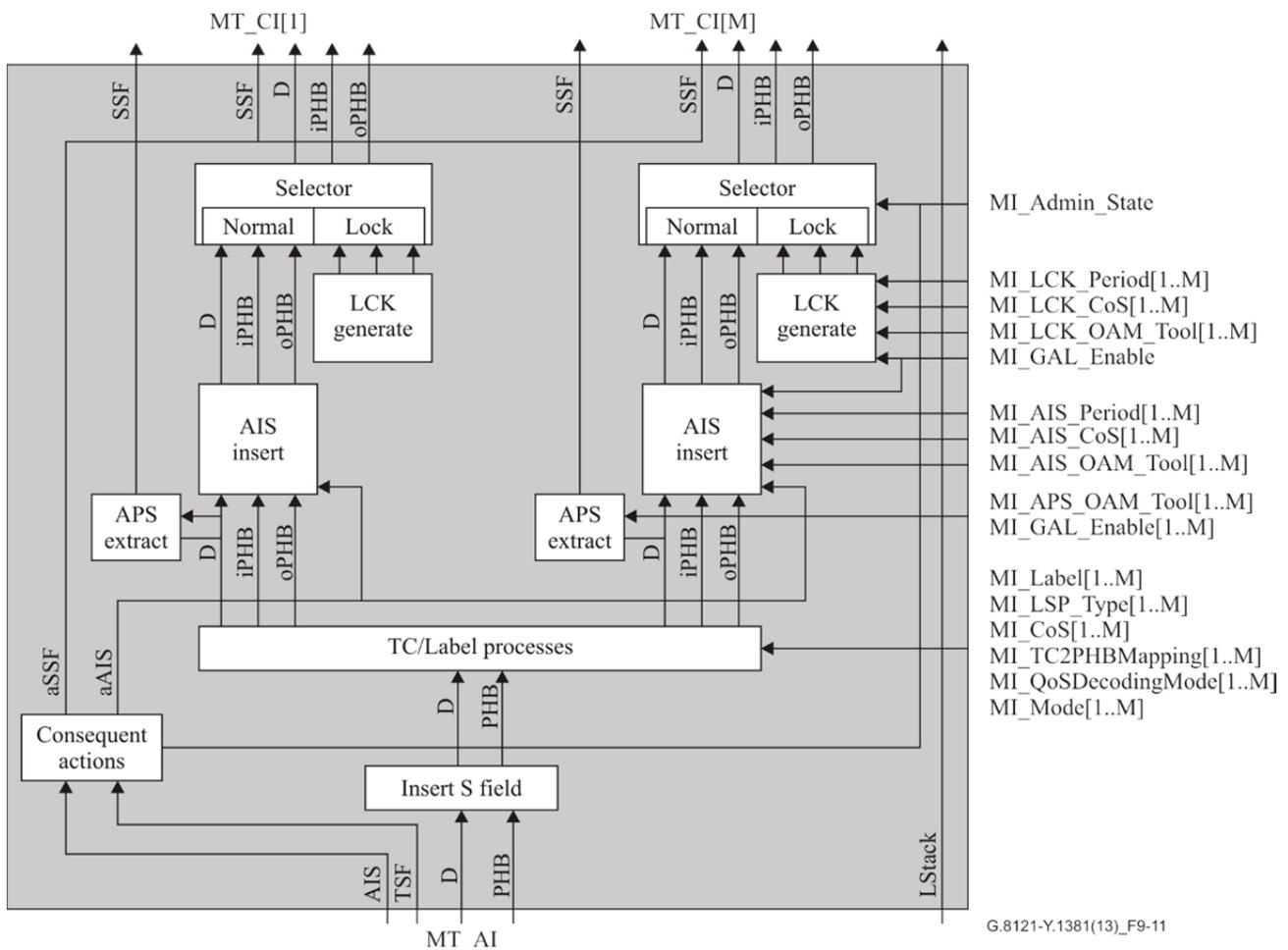
**Interfaces:**

**Table 9-5 – MT/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>MT_AP:</b>            MT_AI_Data            MT_AI_PHB            MT_AI_TSF            MT_AI_AIS            MT_AI_LStack</p> <p><b>MT/MT_A_Sk_MP:</b>            MT/MT_A_Sk_MI_AdminState            MT/MT_A_Sk_MI_Label[1...M]            MT/MT_A_Sk_MI_LSPTType[1...M]            MT/MT_A_Sk_MI_CoS[1...M]            MT/MT_A_Sk_MI_TC2PHBMapping[1...M]            MT/MT_A_Sk_MI_QoSDecodingMode[1...M]            MT/MT_A_Sk_MI_Mode</p> <p>MT/MT_A_Sk_MI_AIS_Period[1...M]            MT/MT_A_Sk_MI_AIS_CoS[1...M]            MT/MT_A_Sk_MI_AIS_OAM_Tool[1...M]            MT/MT_A_Sk_MI_LCK_Period[1...M]            MT/MT_A_Sk_MI_LCK_CoS[1...M]            MT/MT_A_Sk_MI_LCK_OAM_Tool[1...M]            MT/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p> <p>MT/MT_A_Sk_MI_GAL_Enable [1...M]</p>	<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB            MT_CI_SSF            MT_AI_LStack</p>

**Processes:**

The processes associated with the MT/MT\_TT\_Sk function are depicted in Figure 9-11.



**Figure 9-11 – MT/MT\_A\_Sk process diagram**

– *Selector process:*

See clause 8.6.1. The normal CI is blocked if Admin\_State = LOCKED.

– *LCK generate process:*

See clause 8.6.3.

– *AIS process:*

See clause 8.6.2.

– *TC/Label processes:*

See clause 8.2.2.

– *S field extraction:*

Extract and process the 1-bit S field: the retrieved S field should have the value 0 (not bottom of label stack) to indicate the client is MPLS; for such cases the traffic unit is accepted and forwarded (together with the PHB information) after extraction of the S-bit field to the next process. For the case where the S-bit has the value 1, the traffic unit is silently discarded.

**Defects:** None.

**Consequent actions:** The function shall perform the following consequent actions:

aSSF ← AI\_TSF

aAIS ← AI\_AIS

**Defect correlations:** None.

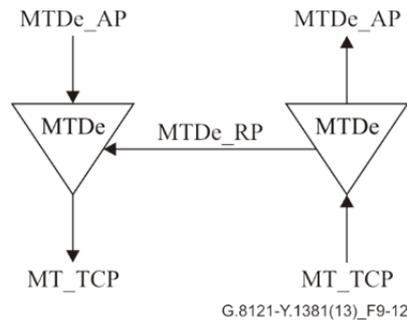
**Performance monitoring:** None.

## 9.4 MT diagnostic functions

### 9.4.1 MT diagnostic functions for MEPs (MTDe)

#### 9.4.1.1 MT diagnostic trail termination functions for MEPs (MTDe\_TT)

The bidirectional MTDe trail termination (MTDe\_TT) function is performed by a co-located pair of MTDe trail termination source (MTDe\_TT\_So) and sink (MTDe\_TT\_Sk) functions as shown in Figure 9-12.

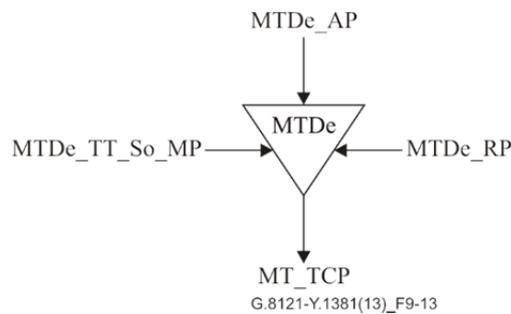


**Figure 9-12 – MTDe\_TT**

#### 9.4.1.1.1 MT diagnostic trail termination source function for MEPs (MTDe\_TT\_So)

##### Symbol

The MTDe\_TT\_So function symbol is shown in Figure 9-13.



**Figure 9-13 – MTDe\_TT\_So symbol**

##### Interfaces

**Table 9-6 – MTDe\_TT\_So interfaces**

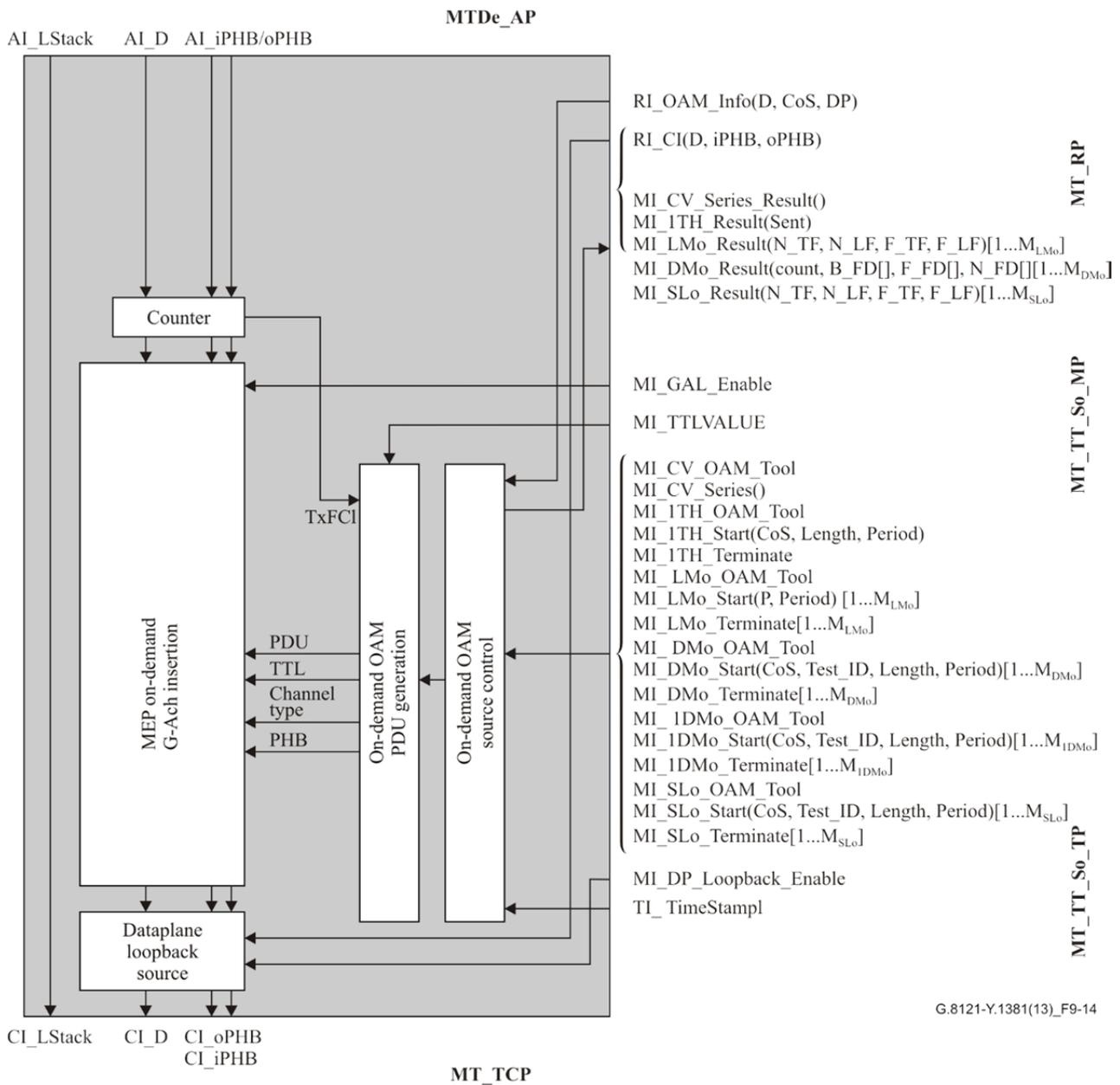
Input(s)	Output(s)
<b>MTDe_AP:</b> MTDe_AI_D MTDe_AI_oPHB MTDe_AI_iPHB MTDe_AI_LStack	<b>MT_TCP:</b> MT_CI_D MT_CI_oPHB MT_CI_iPHB MT_CI_LStack
<b>MT De_RP:</b> MTDe_RI_OAM_Info(D,CoS,DP) MTDe_RI_CI	<b>MTDe_TT_So_MP:</b> MTDe_TT_So_MI_CV_Series_Result [Note]

**Table 9-6 – MTDe\_TT\_So interfaces**

Input(s)	Output(s)
<p><b>MTDe_TT_So_MP:</b>            MTDe_TT_So_MI_GAL_Enable            MTDe_TT_So_MI_TTLVALUE            MTDe_TT_So_MI_CV_OAM_Tool            MTDe_TT_So_MI_CV_Series () [Note]</p> <p>MTDe_TT_So_MI_1TH_OAM_Tool            MTDe_TT_So_MI_1TH_Start                (CoS,Length,Period)            MTDe_TT_So_MI_1TH_Terminate</p> <p>MTDe_TT_So_MI_LMo_OAM_Tool            MTDe_TT_So_MI_LMo_Start(CoS,Period)            [1...M<sub>LMo</sub>]            MTDe_FT_So_MI_LMo_Terminate[1...M<sub>LMo</sub>]</p> <p>MTDe_TT_So_MI_DMo_OAM_Tool            MTDe_TT_So_MI_DMo_Start                (CoS,Test_ID,Length,Period)[1...M<sub>DMo</sub>]            MTDe_TT_So_MI_DMo_Terminate[1...M<sub>DMo</sub>]</p> <p>MTDe_TT_So_MI_1DMo_OAM_Tool            MTDe_TT_So_MI_1DMo_Start                (CoS,Test_ID,Length,Period)[1...M<sub>1DMo</sub>]            MTDe_TT_So_MI_1DMo_Terminate[1...M<sub>1DMo</sub>]</p> <p>MTDe_TT_So_MI_SLo_OAM_Tool            MTDe_TT_So_MI_SLo_Start                (CoS,Test_ID,Length,Period)[1...M<sub>SLo</sub>]            MTDe_TT_So_MI_SLo_Terminate[1...M<sub>SLo</sub>]            MTDe_TT_So_MI_Admin_State            MTDe_TT_So_MI_Lock_Instruct_Enable</p> <p>MTDe_TT_So_MI_DP_Loopback_Enable            MTDe_TT_So_TP:            MTDe_TT_So_TI_TimeStampI</p>	<p>MTDe_TT_So_MI_1TH_Result(Sent)            MTDe_TT_So_MI_LMo_Result(N_TF,N_LF,                F_TF,F_LF)[1...M<sub>LMo</sub>]            MTDe_TT_So_MI_DMo_Result(count,B_FD[],                F_FD[],N_FD[])[1...M<sub>DMo</sub>]            MTDe_TT_So_MI_SLo_Result(N_TF,N_LF,F                TF,F_LF)[1...M<sub>SLo</sub>]</p>
<p>NOTE – The parameters for MI_CV_Series and MI_CV_Series_Result are not within the scope of this Recommendation.</p>	

**Processes**

The processes associated with the MTDe\_TT\_So function are depicted in Figure 9-14.



**Figure 9-14 – MTDe\_TT\_So process**

**MEP on-demand G-ACh insertion:** See clause 8.1.

**OAM PDU generation:** See clause 8.8.

**On-demand OAM source control:** See clause 8.8.

**Data-plane loopback source process:** See clause 8.9.1.

**Defects:** None.

**Consequent actions:** None.

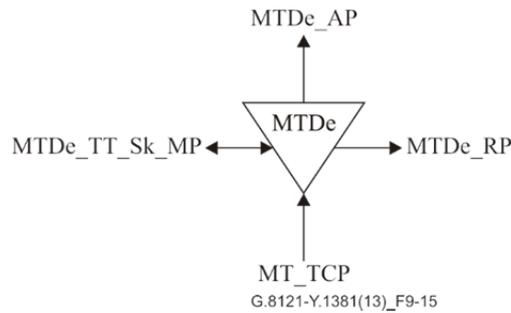
**Defect correlations:** None.

**Performance monitoring:** None.

#### 9.4.1.1.2 MT diagnostic trail termination sink function for MEPs (MTDe\_TT\_Sk)

##### Symbol

The MTDe\_A\_Sk function symbol is shown in Figure 9-15.



**Figure 9-15 – MTDe\_TT\_Sk symbol**

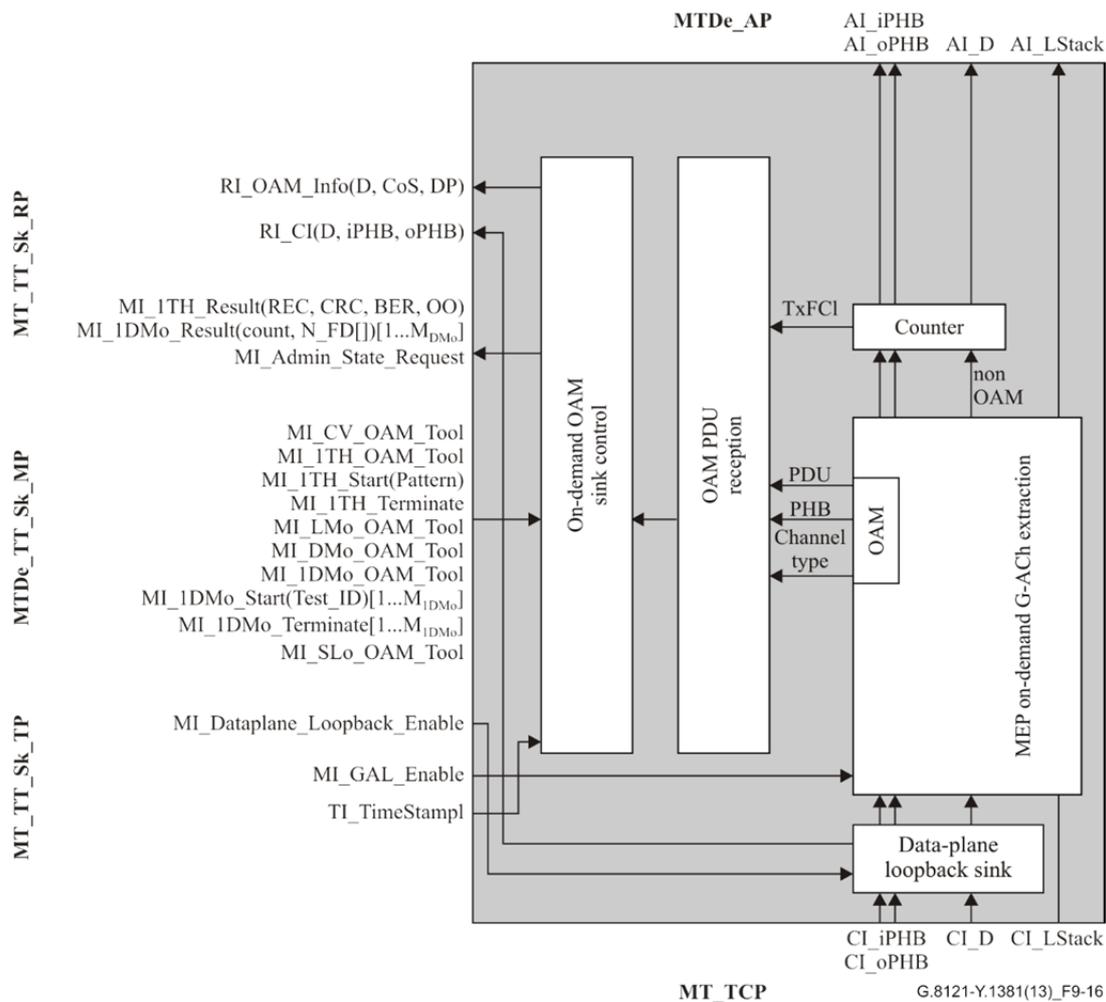
**Interfaces**

**Table 9-7 – MTDe\_TT\_Sk interfaces**

Input(s)	Output(s)
<p><b>MT_TCP:</b>            MT_CI_D            MT_CI_iPHB            MT_CI_oPHB            MT_CI_LStack</p> <p><b>MTDe_TT_Sk_MP:</b>            MTDe_TT_Sk_MI_GAL_Enable</p> <p>MTDe_TT_Sk_MI_CV_OAM_Tool            MTDe_TT_Sk_MI_1TH_OAM_Tool            MTDe_TT_Sk_MI_1TH_Start            MTDe_TT_Sk_MI_1TH_Terminate            MTDe_TT_Sk_MI_LMo_OAM_Tool            MTDe_TT_Sk_MI_DMo_OAM_Tool            MTDe_TT_Sk_MI_1DMo_OAM_Tool            MTDe_TT_Sk_MI_1DMo_Start(Test_ID)            [1...M<sub>1DMo</sub>]            MTDe_TT_Sk_MI_1DMo_Terminate[1...M<sub>1DMo</sub>]            MTDe_TT_Sk_MI_SLo_OAM_Tool            MTDe_TT_Sk_MI_DP_Loopback_Enable</p> <p><b>MTDe_TP:</b>            MTDe_TT_Sk_TI_TimeStampI</p>	<p><b>MTDe_AP:</b>            MTDe_AI_D            MTDe_AI_oPHB            MTDe_AI_iPHB            MTDe_AI_LStack</p> <p><b>MTDe_RP:</b>            MTDe_RI_OAM_Info(D,CoS,DP)</p> <p>MTDe_RI_CI</p> <p><b>MTDe_TT_Sk_MP:</b>            MTDe_TT_Sk_MI_1TH_Result(REC,CRC,BER,O            O)            MTDe_TT_Sk_MI_1DMo_Result(count,N_FD[])            [1...M<sub>1DMo</sub>]            MTDe_TT_Sk_MI_Admin_State_Request</p>

**Processes**

The processes associated with the MTDe\_TT\_Sk function are depicted in Figure 9-16.



**Figure 9-16 – MTDe\_TT\_Sk process**

**MEP on-demand G-ACh extraction:** See clause 8.1. The process of CI\_D is made according to the following rule:

```

if ( ((MI_GAL_Enable && MT-label(D) == GAL) ||
      (!MI_GAL_Enable && 1stNibble(D) == 0b0001)) &&
      (Packet_Type(D) == On-demand_OAM) )
    forward to G-ACh port
} else {
    forward to data port
}

```

NOTE – MT-label(D) and 1stNibble(D) are functions which return respectively, the Label field at the top of the stack within the MPLS payload and the first nibble of the MPLS payload as defined in Figure 6-3 of [ITU-TG.8110.1].

These fields are used to identify G-ACh packets as described in section 4.2.1 of [IETF RFC 5586].

Packet\_Type(D) is a protocol-specific function that determines whether the traffic unit contains an on-demand OAM packet.

**OAM PDU reception:** See clause 8.8.

**On-demand OAM sink control:** See clause 8.8.

**Data-plane loopback sink process:** See clause 8.9.1.

**Defects:** None.  
**Consequent actions:** None.  
**Defect correlations:** None.  
**Performance monitoring:** None.

**9.4.1.2 MTDe to MT adaptation functions (MTDe/MT\_A)**

**9.4.1.2.1 MTDe to MT adaptation source function (MTDe/MT\_A\_So)**

This function consists of input MT\_CI and output MTDe\_AI. The function inside is empty, i.e., the input signals are simply passes to the output.

**9.4.1.2.2 MTDe to MT adaptation sink function (MTDe/MT\_A\_Sk)**

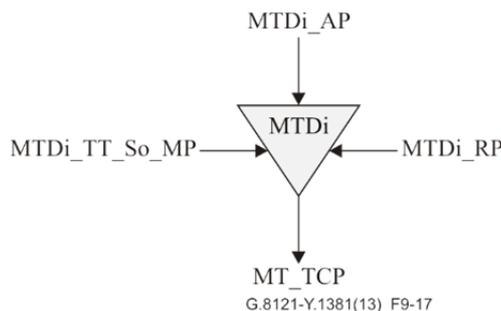
This function consists of input MTDe\_AI and output MT\_CI. The function inside is empty, i.e., the input signals are simply passed to the output.

**9.4.2 MT diagnostic functions for MIPs (MTDi)**

**9.4.2.1 MT diagnostic trail termination functions for MIPs (MTDi\_TT)**

**9.4.2.1.1 MT diagnostic trail termination source function for MIPs (MTDi\_TT\_So) symbol**

The MTDi\_TT\_So function symbol is shown in Figure 9-17.



**Figure 9-17 – MTDi\_TT\_So symbol**

**Interfaces**

**Table 9-8 – MTDi\_TT\_So interfaces**

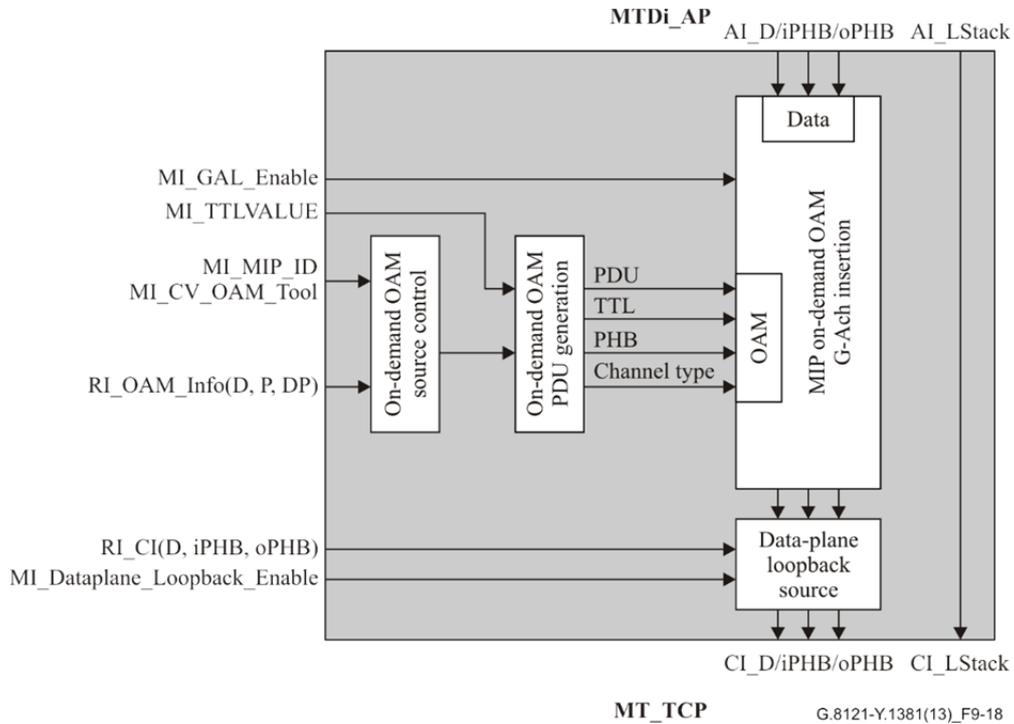
Inputs	Outputs
<p><b>MTDi_AP</b>            MTDi_AI_D            MTDi_AI_iPHB            MTDi_AI_oPHB            MTDi_AI_Lstack</p> <p><b>MTDi_RP</b>            MTDi_RI_OAM_Info (D, CoS, DP)            MTDi_RI_CI</p> <p><b>MTDi_TT_So_MP</b>            MTDi_TT_So_MI_GAL_Enable            MTDi_TT_So_MI_TTLVALUE            MTDi_TT_So_MI_MIP_ID            MTDi TT So MI CV OAM Tool</p>	<p><b>MT_TCP</b>            MT_CI_D,            MT_CI_iPHB,            MT_CI_oPHB,            MT_CI_LStack</p>

**Table 9-8 – MTDi\_TT\_So interfaces**

Inputs	Outputs
MTDi_TT_So_MI_DP_Loopback_Enable	

**Processes**

The processes associated with the MTDi\_TT\_So function are depicted in Figure 9-18.



**Figure 9-18 – MTDi\_TT\_So process**

**MIP on-demand OAM G-ACh insertion:** The MIP OAM insertion process inserts OAM traffic units that are generated in the MTDi\_TT\_So process into the stream of traffic units.

The GAL is used or not used according to the MI\_GAL\_Enable parameter.

**OAM PDU generation:** See clause 8.8.

**On-demand OAM source control:** See clause 8.8.

**Data-plane loopback source process:** See clause 8.9.1.

**Defects:** None.

**Consequent actions:** None.

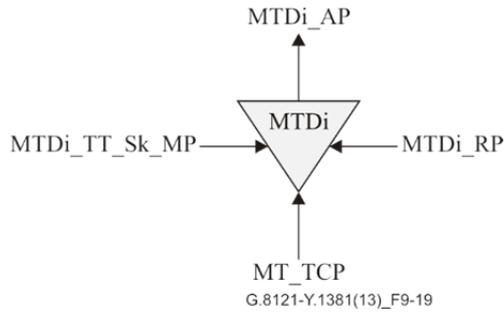
**Defect correlations:** None.

**Performance monitoring:** None.

**9.4.2.1.2 MT diagnostic trail termination sink function for MIPs (MTDi\_TT\_Sk)**

**Symbol**

The MTDi\_TT\_Sk function symbol is shown in Figure 9-19.



**Figure 9-19 – MTDi\_TT\_Sk symbol**

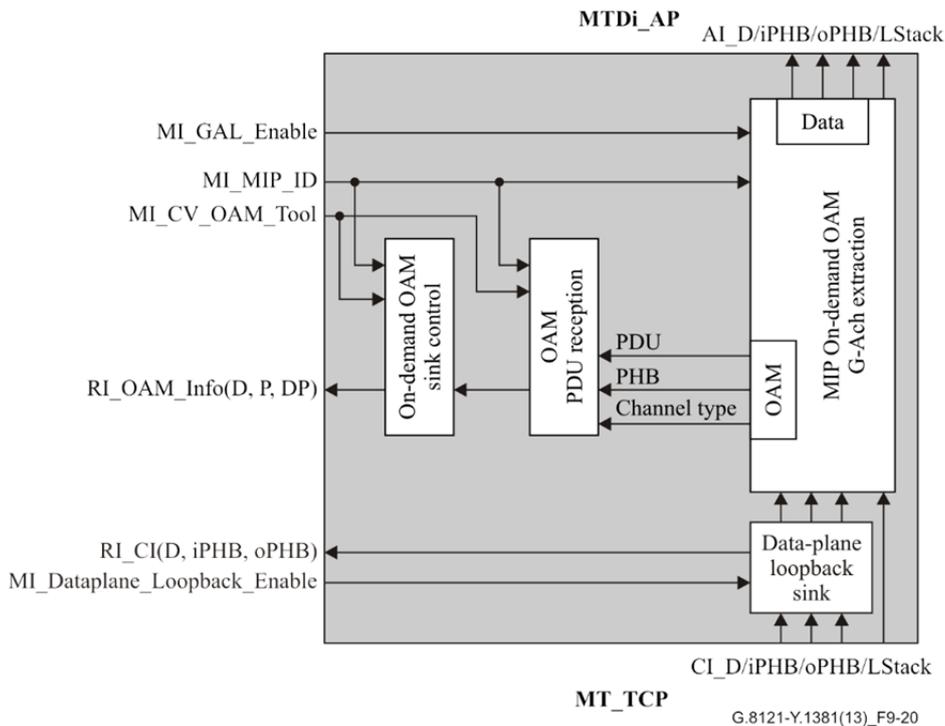
**Interfaces**

**Table 9-9 – MTDi\_TT\_Sk interfaces**

Inputs	Outputs
<b>MT_TCP</b> MT_CI_D MT_CI_iPHB MT_CI_oPHB MT_CI_LStack  <b>MTDi_TT_Sk_MP</b> MTDi_TT_Sk_MI_GAL_Enable MTDi_TT_Sk_MI_MIP_ID MTDi_TT_Sk_MI_CV_OAM_Tool MTDi_TT_Sk_MI_DP_Loopback_Enable	<b>MTDi_AP</b> MTDi_AI_D MTDi_AI_iPHB MTDi_AI_oPHB MTDi_AI_LStack  <b>MTDi_RP</b> MTDi_RI_OAM_Info (D, CoS, DP) MTDi_RI_CI

**Processes**

The processes associated with the MTDi\_TT\_Sk function are depicted in Figure 9-20.



**Figure 9-20 – MTDi\_TT\_Sk process**

**MIP on-demand OAM G-ACh extraction:** The MIP OAM extraction process classifies the OAM traffic units targeted to the MIP to which this MTDi\_TT belongs, as configured by MI\_MIP\_ID, and delivers them to the on-demand OAM PDU reception process. All the other traffic units are delivered to MTDi\_AP. The process is made according to the following rule:

```

if ( (TTL(D) == 0) &&
    ((MI_GAL_Enable && MT-label(D) == GAL) ||
     (!MI_GAL_Enable && 1stNibble(D) == 0b0001)) &&
    (Packet_Type(D) == OnDemandForThisMIP) )
{
    forward to G-Ach port
} else {
    forward to data port
}

```

NOTE – For LSP and pseudowire MIPs, MT-label(D), 1stNibble(D) and TTL(D) are functions which return, respectively, the Label field at the top of the stack within the MPLS payload, the first nibble of the MPLS payload and the TTL field as defined in Figure 6-3 of [ITU-T G.8110.1].

These fields are used to identify G-ACh packets as described in section 4.2.1 of [IETF RFC 5586].

Packet\_Type(D) is a protocol-specific function that determines whether the traffic unit contains an on-demand OAM packet be processed by this MIP.

**OAM PDU reception:** See clause 8.8.

**On-demand OAM sink control:** See clause 8.8.

**Data-plane loopback sink process:** See clause 8.9.2.

**Defects:** None.

**Consequent actions:** None.

**Defect correlations:** None.

**Performance monitoring:** None.

#### 9.4.2.2 MTDi to MT adaptation functions (MTDi/MT\_A)

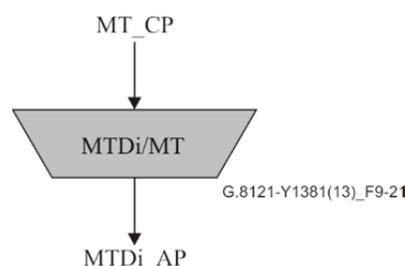
The MTDi/MT adaptation function is an empty function; it is included to satisfy the modelling rules.

The bidirectional MTD/MT adaptation function is performed by a co-located pair of MTDi/MT adaptation source (MTDi/MT\_A\_So) and sink (MTDi/MT\_A\_Sk) functions.

##### 9.4.2.2.1 MTDi to MT adaptation source functions (MTDi/MT\_A\_So)

###### Symbol

The MTDi/MT\_A\_So function symbol is shown in Figure 9-21.



**Figure 9-21 – MTDi/MT\_A\_So symbol**

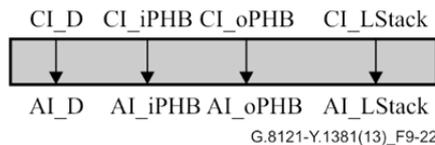
**Interfaces**

**Table 9-10 – MTDi/MT\_A\_So interfaces**

Inputs	Outputs
<b>MT_CP:</b> MT_CI_D MT_CI_iPHB MT_CI_oPHB MT_CI_LStack	<b>MTDi_AP:</b> MTDi_AI_D MTDi_AI_iPHB MTDi_AI_oPHB MTDi_AI_LStack

**Processes**

The processes associated with the MTDi/MT\_Sk function are depicted in Figure 9-22.



G.8121-Y.1381(13)\_F9-22

**Figure 9-22 – MTDi/MT\_A\_So process**

**Defects** None.

**Consequent actions** None.

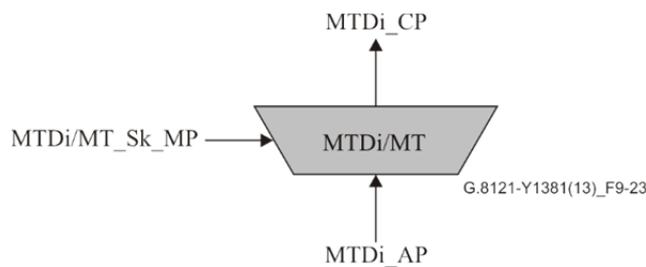
**Defect correlations** None.

**Performance monitoring** None.

**9.4.2.2.2 MTDi to MT adaptation sink function (MTDi/MT\_A\_Sk)**

**Symbol**

The MTDi/MT\_A\_So function symbol is shown in Figure 9-23.



G.8121-Y1381(13)\_F9-23

**Figure 9-23 – MTDi/MT\_A\_Sk symbol**

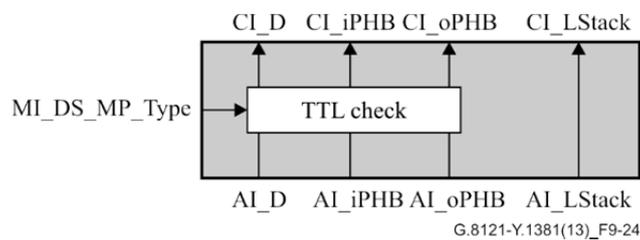
## Interfaces

**Table 9-11 – MTDi/MT\_A\_Sk interfaces**

Inputs	Outputs
<b>MTDi_AP:</b> MTDi_AI_D MTDi_AI_iPHB MTDi_AI_oPHB MTDi_AI_LStack  <b>MTDi/MT_Sk_MP:</b> MIDi/MT_A_MI_DS_MP_Type	<b>MT_CP:</b> MT_CI_D MT_CI_iPHB MT_CI_oPHB MT_CI_LStack

## Processes

The processes associated with the MTDi/MT\_A\_Sk function are depicted in Figure 9-24.



**Figure 9-24 – MTDi/MT\_A\_Sk process**

### *TTL check process:*

For LSP and pseudowire MIPs, the TTL check process examines the TTL field in the MTDi\_AI\_D traffic unit. The TTL check process drops all MPLS-TP packets with TTL = 0 by default (MI\_DS\_MP\_Type set to none).

When MI\_DS\_MP\_Type is set to MIP, the TTL check process drops only user data MPLS-TP packets with TTL = 0 while OAM packets with TTL = 0 are not dropped in this process and forwarded.

When the MI\_DS\_MP\_Type is set to MEP, the TTL check process does not block any MPLS-TP packets with TTL = 0: all MPLS-TP packets with TTL = 0 are forwarded.

NOTE – The MI\_DS\_MP\_Type parameter should be properly configured by the EMF on the basis of the MPLS-TP connection configuration within the node and not exposed to the operator as a configuration parameter of the equipment management interface. Examples of MI\_DS\_MP\_Type configuration are described in Appendix I.

**Defects:** None.

**Consequent actions:** None.

**Defect correlations:** None.

**Performance monitoring:** None.

## 10 MPLS-TP to non-MPLS-TP client adaptation functions

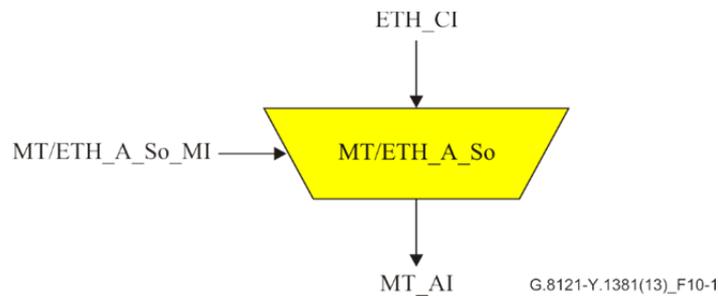
### 10.1 MPLS-TP to ETH adaptation function (MT/ETH\_A)

#### 10.1.1 MPLS-TP to ETH adaptation source function (MT/ETH\_A\_So)

This function maps the ETH\_CI information for transport in an MT\_AI signal.

The information flow and processing of the MT/ETH\_A\_So function is defined with reference to Figure 10-1.

#### Symbol:



**Figure 10-1 – MT/ETH\_A\_So function**

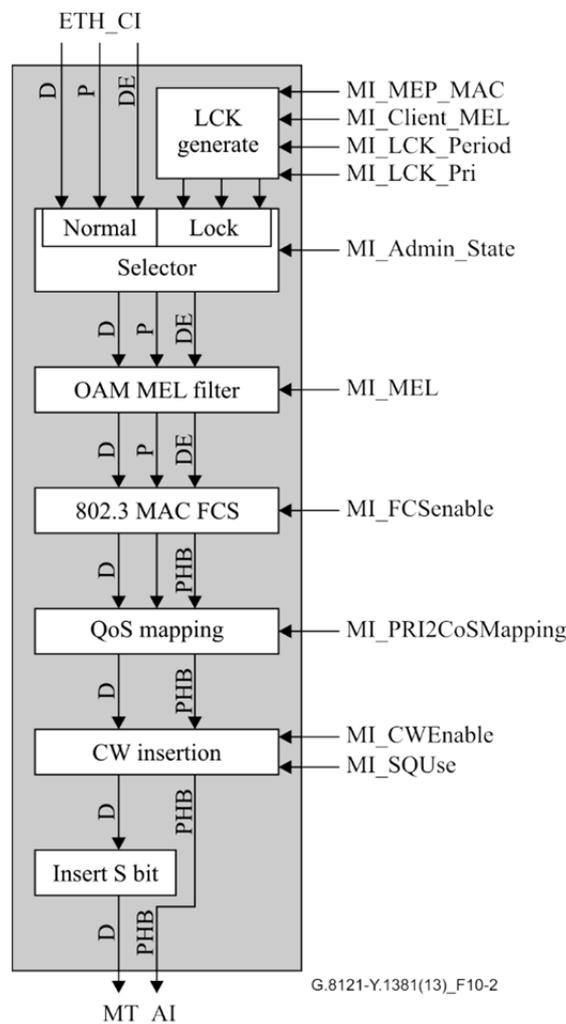
#### Interfaces:

**Table 10-1 – MT/ETH\_A\_So inputs and outputs**

Inputs	Outputs
<p><b>ETH_FP:</b>            ETH_CI_Data            ETH_CI_P            ETH_CI_DE</p> <p><b>MT/ETH_A_So_MP:</b>            MT/ETH_A_So_MI_AdminState            MT/ETH_A_So_MI_FCSEnable            MT/ETH_A_So_MI_CWEnable            MT/ETH_A_So_MI_SQUse            MT/ETH_A_So_MI_PRI2CoSMapping            MT/ETH_A_So_MI_MEP_MAC*            MT/ETH_A_So_MI_Client_MEL*            MT/ETH_A_So_MI_LCK_Period*            MT/ETH_A_So_MI_LCK_Pri*            MT/ETH_A_So_MI_MEL*            * ETH OAM related</p>	<p><b>MT_AP:</b>            MT_AI_Data            MT_AI_PHB</p>

#### Processes:

The processes associated with the MT/ETH\_A\_So function are depicted in Figure 10-2.



**Figure 10-2 – MT/ETH\_A\_So process diagram**

– *LCK generate process:*

See clause 8.1.2 of [ITU-T G.8021].

– *Selector process:*

See clause 8.1.3 of [ITU-T G.8021]. The normal CI is blocked if Admin\_State = LOCKED.

– *OAM MEL filter process:*

See clause 8.1.1 of [ITU-T G.8021].

– *802.3 MAC FCS generation:*

See clause 8.8.1 of [ITU-T G.8021]. MAC FCS generation is optional (see [IETF RFC 4720] and [ITU-T Y.1415]); MAC FCS is generated if MI\_FCSEnable is true.

– *QoS mapping process:*

This process maps the Ethernet-based QoS signals into MPLS-based QoS signals.

The CoS part of the AI\_PHB is generated by the received CI\_P according to the 1:1 mapping configured by the MI\_PRI2CoSMapping.

The DP part of the AI\_PHB is generated by the received CI\_DE according to the following rule:

If CI\_DE = True

```
DP(AI_PHB) = Yellow
Else
DP(AI_PHB) = Green
```

– *CW insertion process:*

See clause 8.5.1.

– *Insert S bit process:*

A 1-bit S field set to 1 (bottom of label stack) is inserted to indicate the client is not MPLS.

**Defects:** None.

**Consequent actions:** None.

**Defect correlations:** None.

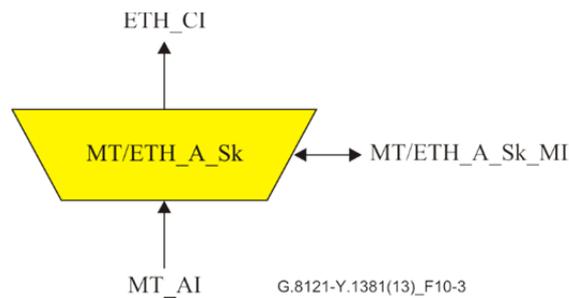
**Performance monitoring:** None.

### 10.1.2 MPLS-TP to ETH adaptation sink function (MT/ETH\_A\_Sk)

This function extracts the ETH\_CI information from an MT\_AI signal.

The information flow and processing of the MT/ETH\_A\_Sk function is defined with reference to Figure 10-3.

**Symbol:**



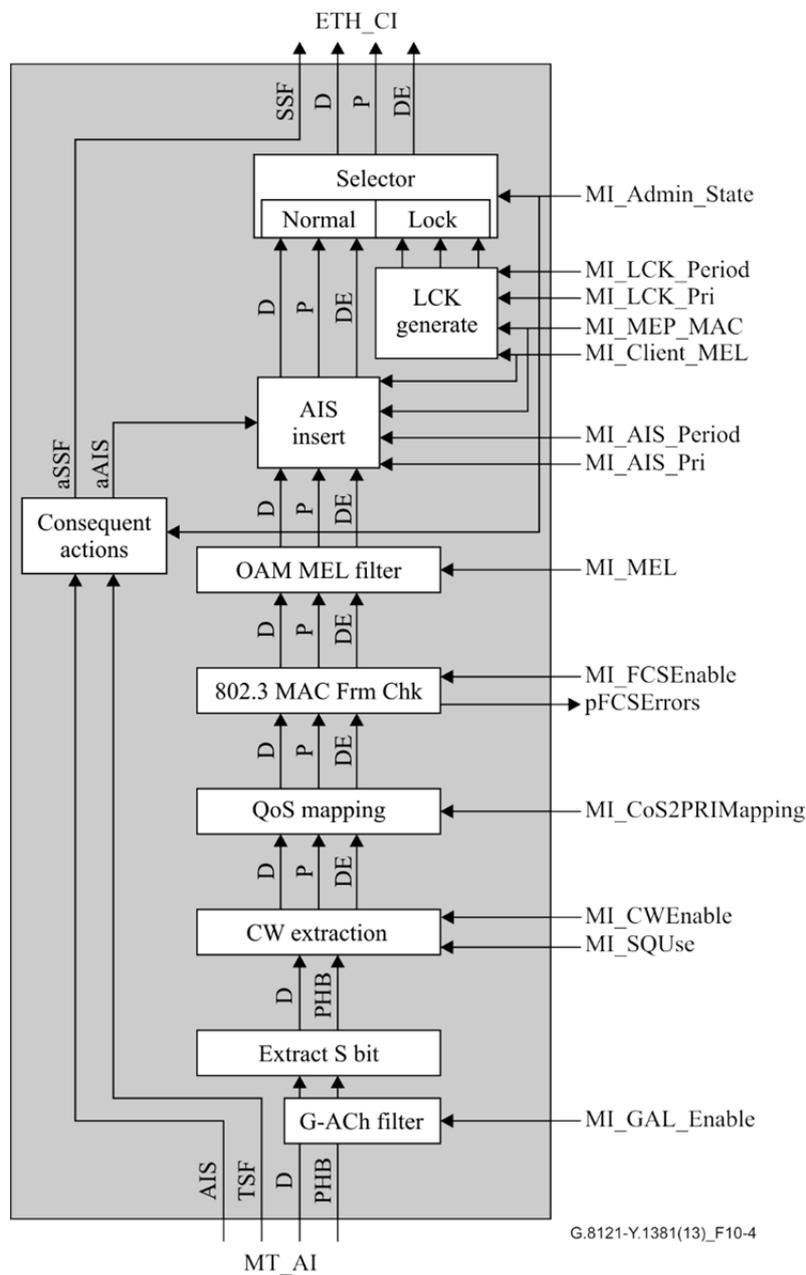
**Figure 10-3 – MT/ETH\_A\_Sk function**

**Interfaces:****Table 10-2 – MT/ETH\_A\_Sk inputs and outputs**

Inputs	Outputs
<p><b>Each MT_AP:</b>  MT_AI_Data  MT_AI_PHB  MT_AI_TSF  MT_AI_AIS</p> <p><b>MT/ETH_A_Sk_MP:</b>  MT/ETH_A_Sk_MI_FCSEnable  MT/ETH_A_Sk_MI_CWEnable  MT/ETH_A_Sk_MI_SQUse  MT/ETH_A_Sk_MI_GAL_Enable  MT/ETH_A_Sk_MI_CoS2PRIMapping</p> <p>MT/ETH_A_Sk_MI_MEL *</p> <p>MT/ETH_A_Sk_MI_Admin_State  MT/ETH_A_Sk_MI_LCK_Period *  MT/ETH_A_Sk_MI_LCK_Pri *  MT/ETH_A_Sk_MI_Client_MEL *  MT/ETH_A_Sk_MI_MEP_MAC *  MT/ETH_A_Sk_MI_AIS_Pri *  MT/ETH_A_Sk_MI_AIS_Period *</p> <p>* ETH OAM related</p>	<p><b>ETH_FP:</b>  ETH_CI_Data  ETH_CI_P  ETH_CI_DE  ETH_CI_SSF</p> <p><b>MT/ETH_A_Sk_MP:</b>  MT/ETH_MI_pFCSErrors</p>

**Processes:**

The processes associated with the MT/ETH\_A\_Sk function are depicted in Figure 10-4.



**Figure 10-4 – MT/ETH\_A\_Sk process diagram**

– *Selector process:*

See clause 8.1.3 of [ITU-T G.8021]. The normal CI is blocked if Admin\_State = LOCKED.

– *LCK generate process:*

See clause 8.1.2 of [ITU-T G.8021].

– *AIS insert process:*

See clause 8.1.4 of [ITU-T G.8021].

– *OAM MEL filter process:*

See clause 8.1.1 of [ITU-T G.8021].

– *"802.3 MAC Frame Check" process:*

See clause 8.9.2 of [ITU-T G.8021]. MAC frame check is optional (see [IETF RFC 4720] and [ITU-T Y.1415]): MAC FCS is checked if MI\_FCSEnable is True.

– *QoS mapping process:*

This process maps the MPLS-based QoS signals into Ethernet-based QoS signals.

The CI\_P is generated by the received PSC part of the AI\_PHB according to the 1:1 mapping configured by the MI\_CoS2PRIMapping.

The CI\_DE is generated by the received DP part of the AI\_PHB according to the following rule

```
If DP(AI_PHB) = Green
    CI_DE = False
Else
    CI_DE = True
```

– *CW extraction process:*

See clause 8.5.2.

– *G-ACh filter process:*

This process removes all the received traffic units which are G-ACh encapsulated, which include GAL or not depending on the MI\_GAL\_Enable.

– *Extract S bit process:*

Extract and process the 1-bit S field: the retrieved S field should have the value 1 (bottom of label stack) to indicate the client is not MPLS: for such cases the traffic unit is accepted and forwarded (together with the PHB information) after extraction of the S-bit field to the next process. For the case where the S-bit has the value 0, the traffic unit is silently discarded.

**Defects:** None.

**Consequent actions:** The function shall perform the following consequent actions:

aSSF ← AI\_TSF and (not MI\_Admin\_State == LOCKED)

aAIS ← AI\_AIS

•**Defect correlations:** None.

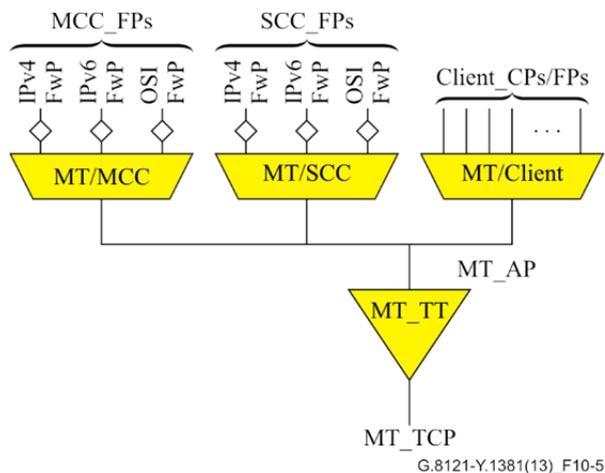
**Performance monitoring:** For further study.

## 10.2 MPLS-TP to SCC and MCC adaptation functions

This clause provides the descriptions of the MPLS-TP adaptation functions for the MPLS-TP MCC and SCC.

Figure 10-5 shows the MPLS-TP adaptation functions providing access to the MCC and SCC. These MT/MCC and MT/SCC adaptation functions are defined in more detail below.

In cases where the client is MPLS-TP, MT/Client will be MT/MT.



**Figure 10-5 – MT/SCC\_A function, MT/MCC\_A function and MT/client\_A function**

### 10.2.1 MT/SCC\_A adaptation function

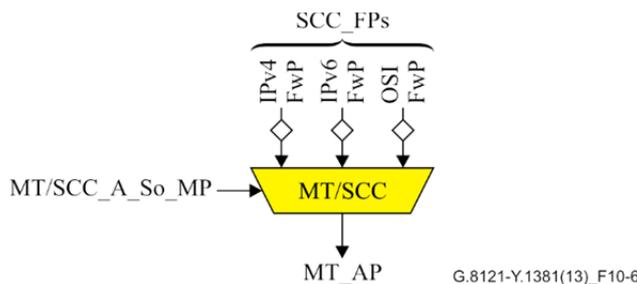
The MT to SCC adaptation function provides access to the SCC for signalling communication. It is used for the scenarios where the SCN utilizes the SCC as defined in [IETF RFC 5718].

#### 10.2.1.1 MT to SCC adaptation source function (MT/SCC\_A\_So function)

The MT/SCC\_A\_So function maps the SCN data into the G-ACh SCC packets as defined in [IETF RFC 5718]. The diamonds in Figure 10-6 represent traffic shaping and conditioning functions that may be needed to prevent the SCC forwarding points from exceeding their committed bandwidth in congestion situations. These traffic shaping and conditioning functions, as well as the related bandwidth management and bandwidth assignment functions are outside the scope of this Recommendation.

The information flow and processing of the MT/SCC\_A\_So functions is defined with reference to Figures 10-6 and 10-7.

#### Symbol



**Figure 10-6 – MT/SCC\_A\_So function**

## Interfaces

**Table 10-3 – MT/SCC\_A\_So inputs and outputs**

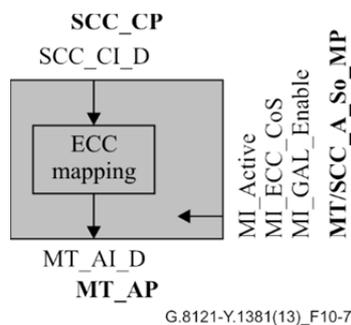
Input(s)	Output(s)
<b>SCC_FP:</b> SCC_CI_D <b>MT/SCC_A_So_MP:</b> MT/SCC_A_So_MI_Active MT/SCC_A_So_MI_ECC_CoS MT/SCC_A_So_MI_GAL_Enable	<b>MT_AP:</b> MT_AI_D MT_AI_PHB

## Processes

### Activation

- The MT/SCC\_A\_So function shall access the access point when it is activated (MI\_Active is true). Otherwise, it shall not access the access point.

The process associated with the MT/SCC\_A\_So function is as depicted in Figure 10-7.



**Figure 10-7 – MT/SCC\_A\_So processes**

**ECC mapping process:** See clause 8.7.1.1.

**Defects:** None.

**Consequent actions:** None.

**Defect correlations:** None.

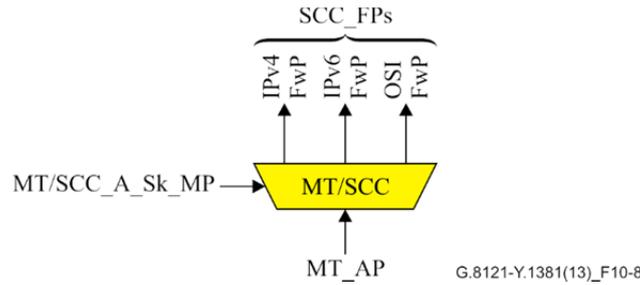
**Performance monitoring:** None.

### 10.2.1.2 MT to SCC adaptation sink function (MT/SCC\_A\_Sk function)

The MT/SCC\_A\_Sk function extracts the SCN from the G-ACh SCC packets as defined in [IETF RFC 5718].

The information flow and processing of the MT/SCC\_A\_Sk functions are defined with reference to Figures 10-8 and 10-9.

**Symbol**



**Figure 10-8 – MT/SCC\_A\_Sk function**

**Interfaces**

**Table 10-4 – MT/SCC\_A\_Sk inputs and outputs**

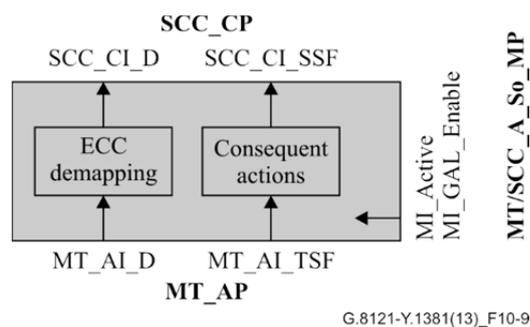
Input(s)	Output(s)
<b>MT_AP:</b> MT_AI_D MT_AI_PHB MT_AI_TSF <b>MT/SCC_A_Sk_MP:</b> MT/SCC_A_Sk_MI_Active MT/SCC_A_Sk_MI_GAL_Enable	<b>SCC_FP:</b> SCC_CI_D SCC_CI_SSF

**Processes**

*Activation*

- The MT/SCC\_A\_Sk function shall access the access point and perform the common and specific processes operation specified below when it is activated (MI\_Active is true). Otherwise, it shall activate the SSF signals at its output (CI\_SSF).

The processes associated with the MT/SCC\_A\_Sk function are depicted in Figure 10-9.



**Figure 10-9 – MT/SCC\_A\_Sk processes**

**ECC de-mapping process:** See clause 8.7.1.2.

**Defects:** None.

**Consequent actions:** The function shall perform the following consequent actions:  
 aSSF ← AI\_TSF or (not MI\_Active)

**Defect correlations:** None.

**Performance monitoring:** None.

### 10.2.2 MT/MCC\_A adaptation function

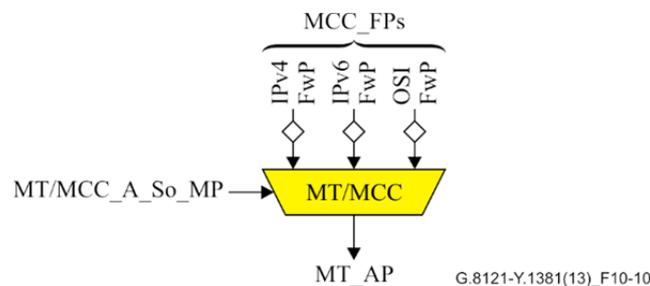
The MT to MCC adaptation function provides access to the MCC for signalling communication. It is used for the scenarios where the MCN utilizes the MCC as defined in [IETF RFC 5718].

#### 10.2.2.1 MT to MCC adaptation source function (MT/MCC\_A\_So function)

The MT/MCC\_A\_So function maps the MCN data into the G-ACh MCC packets as defined in [IETF RFC 5718]. The diamonds in Figure 10-10 represent traffic shaping and conditioning functions that may be needed to prevent the MCC forwarding points from exceeding their committed bandwidth in congestion situations. These traffic shaping and conditioning functions, as well as the related bandwidth management and bandwidth assignment functions are outside the scope of this Recommendation.

The information flow and processing of the MT/MCC\_A\_So functions is defined with reference to Figures 10-10 and 10-11.

#### Symbol



**Figure 10-10 – MT/MCC\_A\_So function**

#### Interfaces

**Table 10-5 – MT/MCC\_A\_So inputs and outputs**

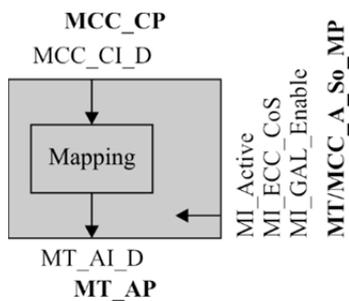
Input(s)	Output(s)
<b>MCC_FP:</b> MCC_CI_D <b>MT/MCC_A_So_MP:</b> MT/MCC_A_So_MI_Active MT/MCC_A_So_MI_ECC_CoS MT/MCC_A_So_MI_GAL_enable	<b>MT_AP:</b> MT_AI_D MT_AI_PHB

#### Processes

##### Activation

- The MT/MCC\_A\_So function shall access the access point when it is activated (MI\_Active is true). Otherwise, it shall not access the access point.

The process associated with the MT/MCC\_A\_So function is as depicted in Figure 10-11.



G.8121-Y.1381(13)\_F10-11

**Figure 10-11 – MT/MCC\_A\_So processes**

**MCC mapping process:** See clause 8.7.1.1.

**Defects:** None.

**Consequent actions:** None.

**Defect correlations:** None.

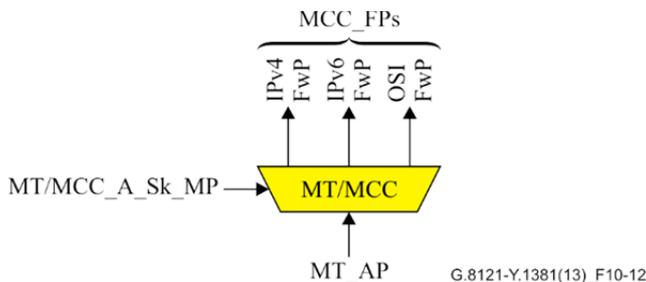
**Performance monitoring:** None.

**10.2.2.2 MT to MCC adaptation source function (MT/SCC\_A\_Sk function)**

The MT/MCC\_A\_Sk function extracts the MCN data from the G-ACh MCC packets as defined in [IETF RFC 5718].

The information flow and processing of the MT/MCC\_A\_Sk functions is defined with reference to Figures 10-12 and 10-13.

**Symbol**



G.8121-Y.1381(13)\_F10-12

**Figure 10-12 – MT/MCC\_A\_Sk function**

**Interfaces**

**Table 10-6 – MT/MCC\_A\_Sk inputs and outputs**

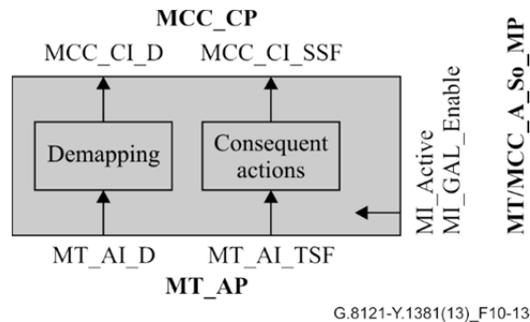
Input(s)	Output(s)
<b>MT_AP:</b> MT_AI_D MT_AI_PHB MT_AI_TSF  <b>MT/MCC_A_Sk_MP:</b> MT/MCC_A_Sk_MI_Active MT/SCC_A_Sk_MI_GAL_Enable	<b>MCC_FP:</b> MCC_CI_D MCC_CI_SSF

**Processes**

*Activation*

- The MT/MCC\_A\_Sk function shall access the access point and perform the common and specific processes operation specified below when it is activated (MI\_Active is true). Otherwise, it shall activate the SSF signals at its output (CI\_SSF).

The processes associated with the MT/MCC\_A\_Sk function are depicted in Figure 10-13.



**Figure 10-13 – MT/MCC\_A\_Sk processes**

**ECC de-mapping process:** See clause 8.7.1.2.

**Defects:** None.

**Consequent actions:** The function shall perform the following consequent actions:  
 $aSSF \leftarrow AI\_TSF \text{ or } (\text{not } MI\_Active)$

**Defect correlations:** None.

**Performance monitoring:** None.

**11 Non-MPLS-TP server to MPLS-TP adaptation functions**

**11.1 SDH to MPLS-TP adaptation function (S/MT\_A)**

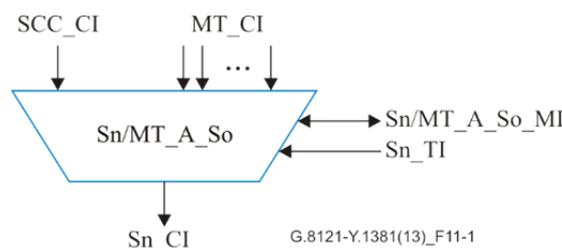
**11.1.1 VC-n to MPLS-TP adaptation functions (Sn/MT\_A; n=3, 3-X, 4, 4-X)**

**11.1.1.1 VC-n to MPLS-TP adaptation source function (Sn/MT\_A\_So)**

This function maps MT\_CI information onto an Sn\_AI signal (n=3, 3-X, 4, 4-X).

Data at the Sn\_AP is a VC-n (n = 3, 3-X, 4, 4-X), with a payload as described in [ITU-T G.707], but with indeterminate POH bytes: J1, B3, G1.

**Symbol:**



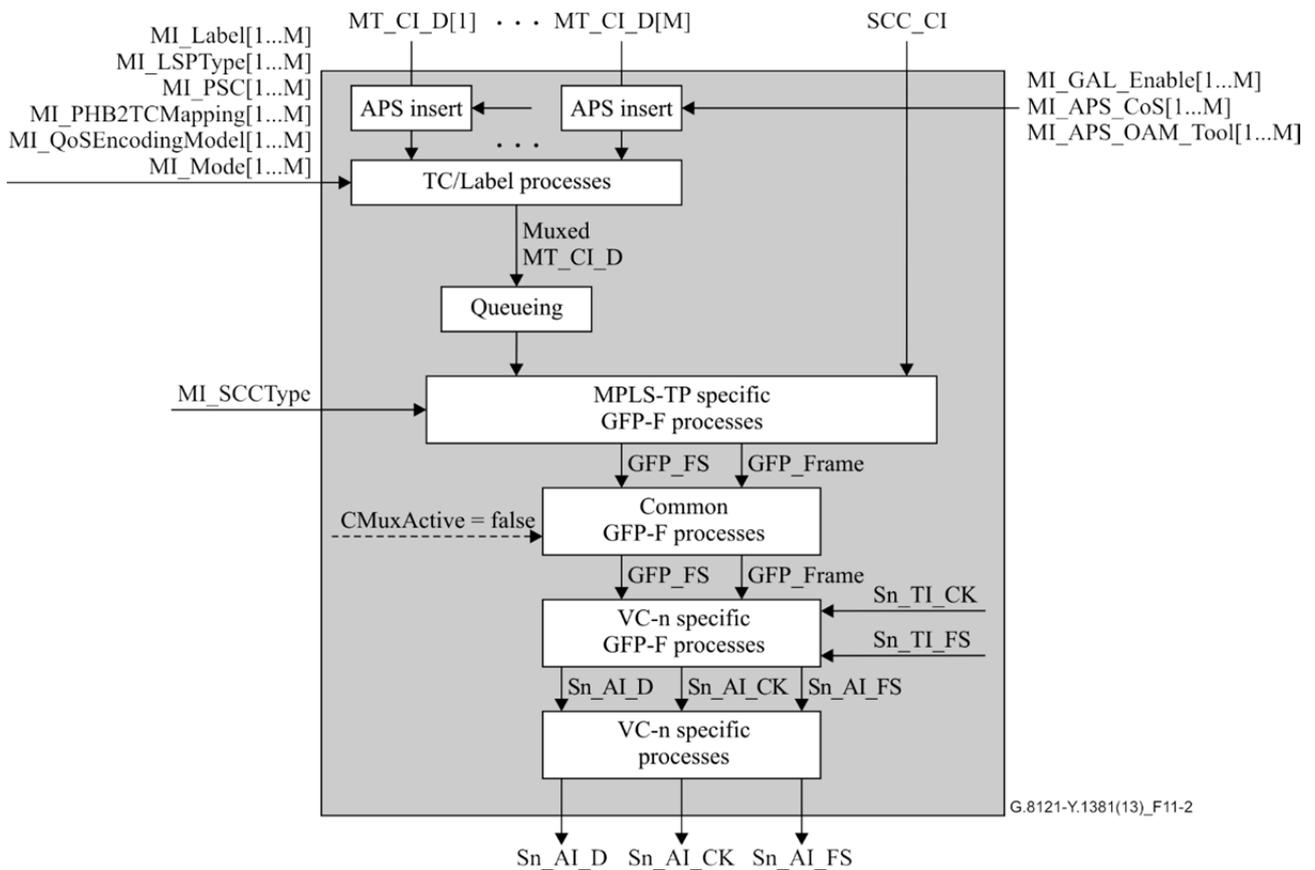
**Figure 11-1 – Sn/MT\_A\_So symbol**

**Interfaces:****Table 11-1 – Sn/MT\_A\_So interfaces**

<b>Inputs</b>	<b>Outputs</b>
<p><b>Each MT_CP:</b>  MT_CI_Data  MT_CI_iPHB  MT_CI_oPHB</p> <p><b>SCC_CP:</b>  SCC_CI_Data</p> <p><b>Sn_TP:</b>  Sn_TI_Clock  Sn_TI_FrameStart</p> <p><b>Sn/MT_A_So_MP:</b>  Sn/MT_A_So_MI_SCCType  Sn/MT_A_So_MI_Label[1...M]  Sn/MT_A_So_MI_LSPTType[1...M]  Sn/MT_A_So_MI_CoS[1...M]  Sn/MT_A_So_PHB2TCMapping[1...M]  Sn/MT_A_So_MI_QoSEncodingMode[1...M]  Sn/MT_A_So_MI_Mode[1...M]</p> <p>Sn/MT_A_So_MI_GAL_Enable[1...M]  Sn/MT_A_So_MI_APS_OAM_CoS[1...M]  Sn/MT_A_So_MI_APS_OAM_Tool[1...M]</p>	<p><b>Sn_AP:</b>  Sn_AI_Data  Sn_AI_Clock  Sn_AI_FrameStart</p>

**Processes:**

A process diagram of this function is shown in Figure 11-2.



**Figure 11-2 – Sn/MT\_A\_So process diagram**

– *TC/Label processes:*

See clause 8.2.1.

– *Queueing process:*

See clause 8.3.

– *MPLS-TP-specific GFP-F source process:*

See clause 8.4.1.

– *Common GFP source process:*

See clause 8.5.3.1 of [ITU-T G.806]. GFP channel multiplexing is not supported (CMuxActive=false).

– *VC-n-specific GFP source process:*

See clause 8.5.2.1 of [ITU-T G.806]. The GFP frames are mapped into the VC-n payload area according to clause 10.6 of [ITU-T G.707].

– *VC-n-specific source process:*

**C2:** Signal label information is derived directly from the adaptation function type. The value for "GFP mapping" in Table 9-11 of [ITU-T G.707] is placed in the C2 byte position.

**H4:** For Sn/MT\_A\_So with n=3, 4, the H4 byte is sourced as all-zeros.

NOTE 1 – For Sn/MT\_A\_So with n=3-X, 4-X, the H4 byte is undefined at the Sn-X\_AP output of this function (as per clause 12 of [ITU-T G.783]).

NOTE 2 – For Sn/MT\_A\_So with n=3, 4, 3-X, 4-X, the K3, F2, F3 bytes are undefined at the Sn-X\_AP output of this function (as per clause 12 of [ITU-T G.783]).

**Defects:** None.

**Consequent actions:** None.

**Defect correlations:** None.

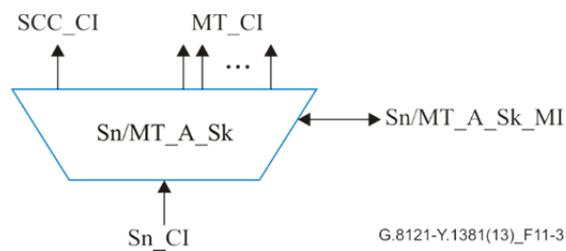
**Performance monitoring:** For further study.

### 11.1.1.2 VC-n to MPLS-TP adaptation sink function (Sn/MT\_A\_Sk)

This function extracts MT\_CI information from the Sn\_AI signal (n=3, 3-X, 4, 4-X), delivering MT\_CI.

Data at the Sn\_AP is a VC-n (n=3, 3-X, 4, 4-X) but with indeterminate POH bytes J1, B3, G1, as per [ITU-T G.707].

**Symbol:**



**Figure 11-3 – Sn/MT\_A\_Sk symbol**

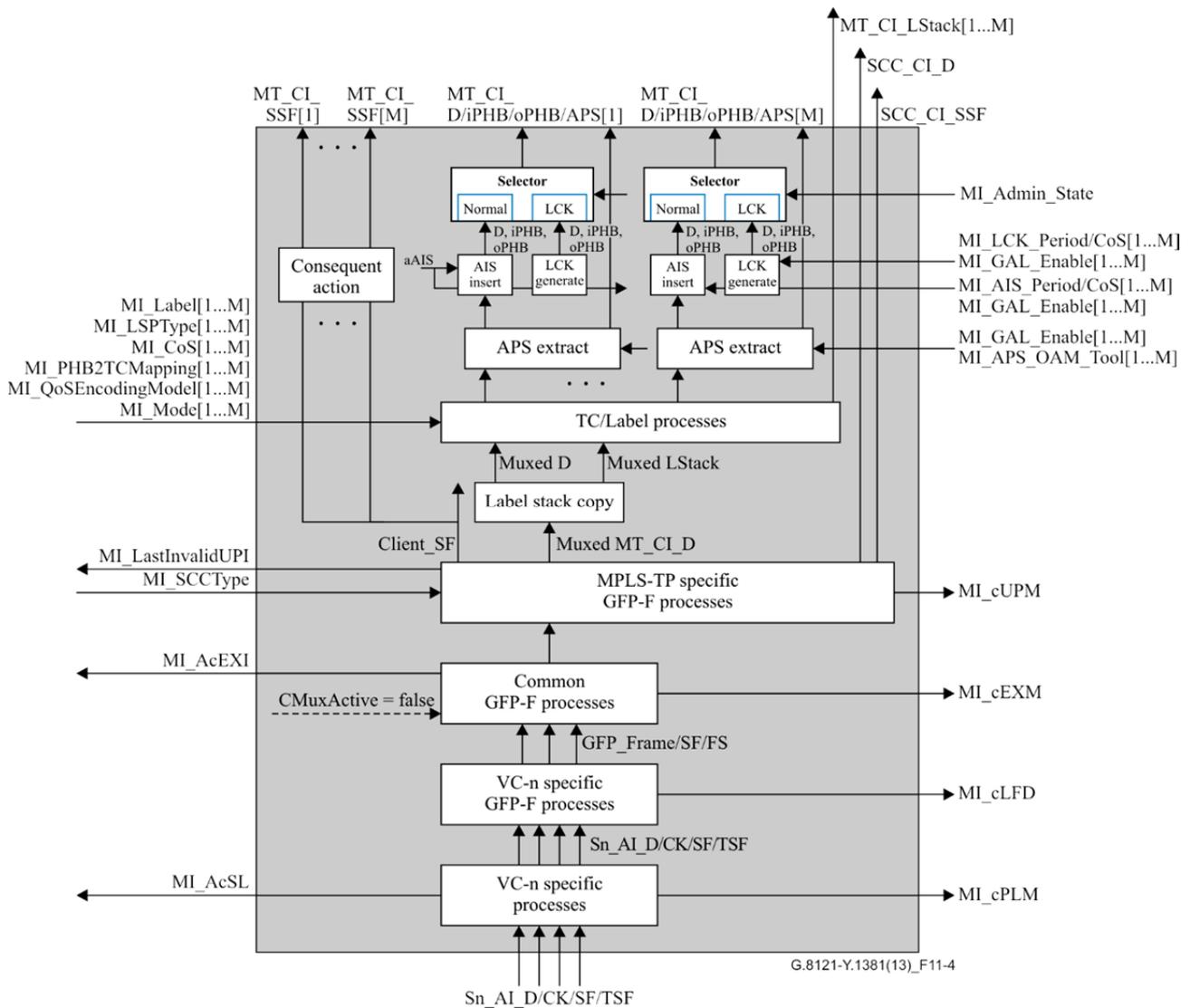
**Interfaces:**

**Table 11-2 – Sn/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>Sn_AP:</b>  Sn_AI_Data  Sn_AI_Clock  Sn_AI_FrameStart  Sn_AI_TSF</p> <p><b>Sn/MT_A_Sk_MP:</b>  Sn/MT_A_Sk_MI_SCCType  Sn/MT_A_Sk_MI_Label[1...M]  Sn/MT_A_Sk_MI_LSPTType[1...M]  Sn/MT_A_Sk_MI_CoS[1...M]  Sn/MT_A_Sk_MI_TC2PHBMapping[1...M]  Sn/MT_A_Sk_MI_QoSDecodingMode[1...M]  Sn/MT_A_Sk_MI_Mode[1...M]</p> <p>Sn/MT_A_Sk_MI_LCK_Period[1...M]  Sn/MT_A_Sk_MI_LCK_CoS[1...M]  Sn/MT_A_Sk_MI_Admin_State  Sn/MT_A_Sk_MI_AIS_Period[1...M]  Sn/MT_A_Sk_MI_AIS_CoS[1...M]  Sn/MT_A_Sk_MI_GAL_enable[1...M]  Sn/MT_A_Sk_MI_LCK_OAM_Tool [1...M]  Sn/MT_A_Sk_MI_AIS_OAM_Tool[1...M]</p> <p>Sn/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p>	<p><b>Each MT_CP:</b>  MT_CI_Data  MT_CI_iPHB  MT_CI_oPHB  MT_CI_SSF  MT_CI_LStack</p> <p><b>SCC_CP:</b>  SCC_CI_Data  SCC_CI_SSF</p> <p><b>Sn/MT_A_Sk_MP:</b>  Sn/MT_A_Sk_MI_AcSL  Sn/MT_A_Sk_MI_AcEXI  Sn/MT_A_Sk_MI_LastValidUPI  Sn/MT_A_Sk_MI_cPLM  Sn/MT_A_Sk_MI_cLFD  Sn/MT_A_Sk_MI_cEXM  Sn/MT_A_Sk_MI_cUPM</p>

**Processes:**

A process diagram of this function is shown in Figure 11-4.



**Figure 11-4 – Sn/MT\_A\_Sk process diagram**

– *Selector generation process:*

See clause 8.6.1. The normal CI is blocked if Admin\_State = LOCKED.

– *AIS insert process:*

See clause 8.6.2. There is a single AIS insert process for each MT.

– *LCK generation process:*

See clause 8.6.3. There is a single LCK insert process for each MT.

– *TC/Label processes:*

See clause 8.2.2.

– *Label stack copy process:*

See clause 8.2.3.

– *MPLS-TP-specific GFP-F sink process:*

See clause 8.4.2.

– *Common GFP sink process:*

See clause 8.5.3.2 of [ITU-T G.806]. GFP channel multiplexing is not supported (CMuxActive=false).

– *VC-n-specific GFP sink process:*

See clause 8.5.2.2 of [ITU-T G.806]. The GFP frames are de-mapped from the VC-n payload area according to clause 10.6 of [ITU-T G.707].

– *VC-n-specific sink process:*

**C2:** The signal label is recovered from the C2 byte as per clause 6.2.4.2 of [ITU-T G.806]. The signal label for "GFP mapping" in Table 9-11 of [ITU-T G.707] shall be expected. The accepted value of the signal label is also available at the Sn/MT\_A\_Sk\_MP.

**Defects:**

dPLM – See clause 6.2.4.2 of [ITU-T G.806].

dLFD – See clause 6.2.5.2 of [ITU-T G.806].

dEXM – See clause 6.2.4.4 of [ITU-T G.806].

dUPM – See clause 8.4.2.

**Consequent actions:** The function shall perform the following consequent actions:

aSSF ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

aAIS ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

**Defect correlations:** The function shall perform the following defect correlations to determine the most probable fault cause (see clause 6.4 of [ITU-T G.806]). This fault cause shall be reported to the EMF.

cPLM ← dPLM and (not AI\_TSF)

cLFD ← dLFD and (not dPLM) and (not AI\_TSF)

cEXM ← dEXM and (not dPLM) and (not dLFD) and (not AI\_TSF)

cUPM ← dUPM and (not dEXM) and (not dPLM) and (not dLFD) and (not AI\_TSF)

**Performance monitoring:** For further study.

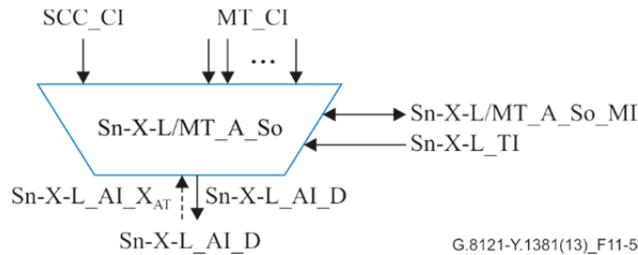
**11.1.2 LCAS-capable VC-n to MPLS-TP adaptation functions (Sn-X-L/MT\_A; n=3, 4)**

**11.1.2.1 LCAS-capable VC-n to MPLS-TP adaptation source function (Sn-X-L/MT\_A\_So)**

This function maps MT\_CI information onto an Sn-X-L\_AI signal (n=3, 4).

Data at the Sn-X-L\_AP is a VC-n-X (n = 3, 4), with a payload as described in [ITU-T G.707], but with indeterminate POH bytes: J1, B3, G1.

**Symbol:**



**Figure 11-5 – Sn-X-L/MT\_A\_So symbol**

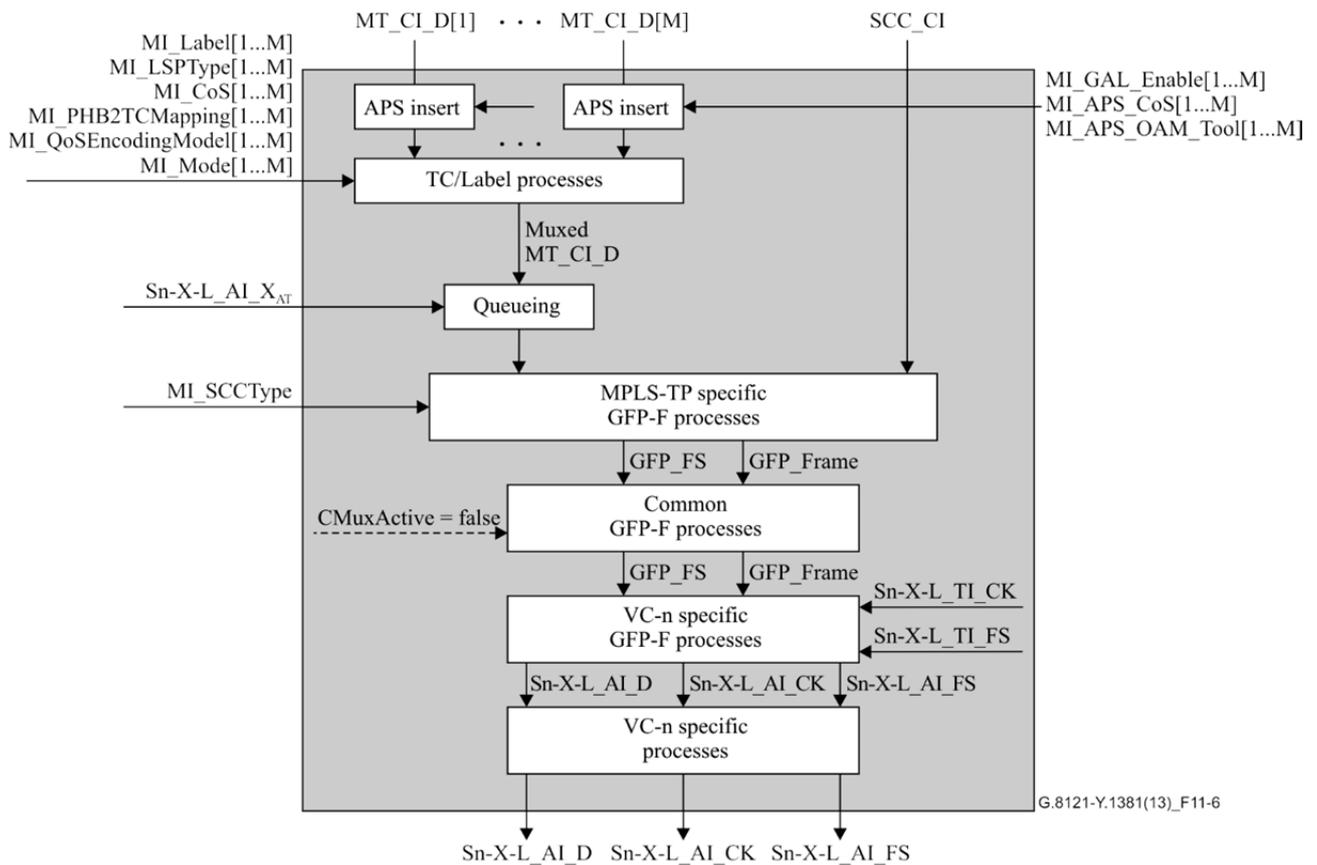
**Interfaces:**

**Table 11-3 – Sn-X-L/MT\_A\_So interfaces**

Inputs	Outputs
<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB  <b>SCC_CP:</b>            SCC_CI_Data  <b>Sn-X-L_AP:</b>            Sn-X-L_AI_XAT  <b>Sn-X-L_TP:</b>            Sn-X-L_TI_Clock            Sn-X-L_TI_FrameStart  <b>Sn-X-L/MT_A_So_MP:</b>            Sn-X-L/MT_A_So_MI_SCCType            Sn-X-L/MT_A_So_MI_Label[1...M]            Sn-X-L/MT_A_So_MI_LSPTType[1...M]            Sn-X-L/MT_A_So_MI_CoS[1...M]            Sn-X-L/MT_A_So_PHB2TCMapping[1...M]            Sn-X-L/MT_A_So_MI_QoSEncodingMode[1...M]            Sn-X-L/MT_A_So_MI_Mode[1...M]</p> <p>Sn-X-L/MT_A_So_MI_GAL_Enable[1...M]            Sn-X-L/MT_A_So_MI_APS_OAM_CoS[1...M]            Sn-X-L/MT_A_So_MI_APS_OAM_Tool[1...M]</p>	<p><b>Sn-X-L_AP:</b>            Sn-X-L_AI_Data            Sn-X-L_AI_Clock            Sn-X-L_AI_FrameStart</p>

**Processes:**

A process diagram of this function is shown in Figure 11-6.



**Figure 11-6 – Sn-X-L/MT\_A\_So process diagram**

The processes have the same definition as in clause 11.1.1.1.

**Defects:** None.

**Consequent actions:** None.

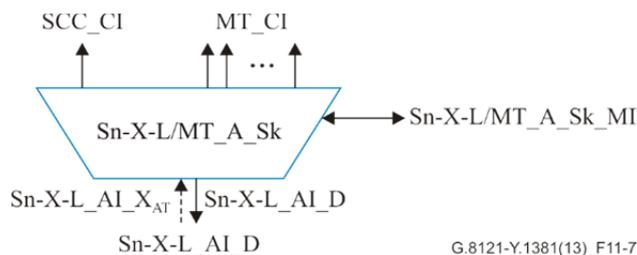
**Defect correlations:** None.

**Performance monitoring:** For further study.

**11.1.2.2 LCAS-capable VC-n to MPLS-TP adaptation sink function (Sn-X-L/MT\_A\_Sk)**

This function extracts MT\_CI information from the Sn-X-L\_AI signal (n=3, 4), delivering MT\_CI. Data at the Sn-X-L\_AP is a VC-n-Xv (n=3, 4) but with indeterminate POH bytes J1, B3, G1, as per [ITU-T G.707].

**Symbol:**



**Figure 11-7 – Sn-X-L/MT\_A\_Sk symbol**

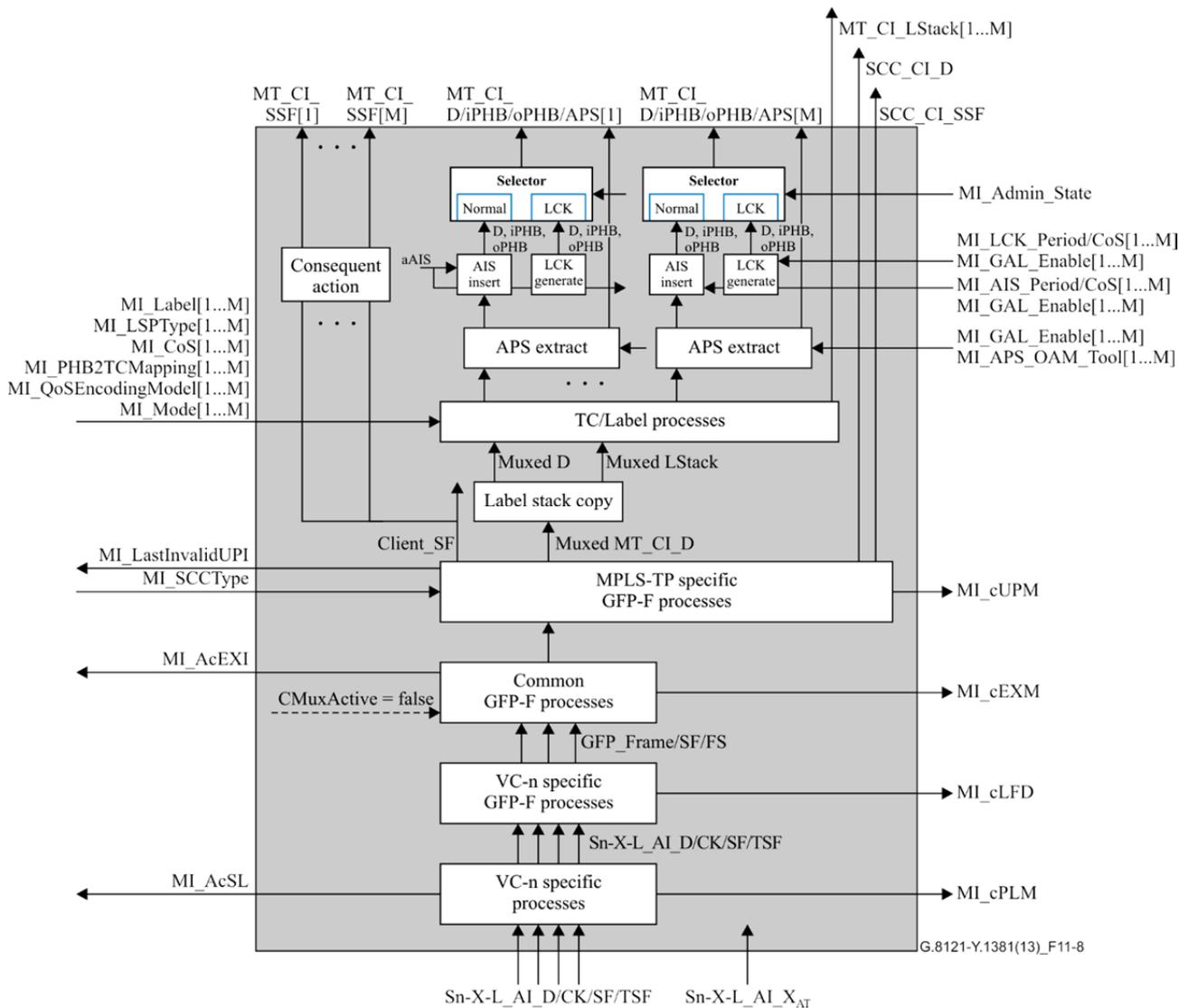
**Interfaces:**

**Table 11-4 – Sn-X-L/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>Sn-X-L_AP:</b>            Sn-X-L_AI_Data            Sn-X-L_AI_Clock            Sn-X-L_AI_FrameStart            Sn-X-L_AI_TSF            Sn-X-L_AI_XAR</p> <p><b>Sn-X-L/MT_A_Sk_MP:</b>            Sn-X-L/MT_A_Sk_MI_SCCType            Sn-X-L/MT_A_Sk_MI_Label[1...M]            Sn-X-L/MT_A_Sk_MI_LSPTType[1...M]            Sn-X-L/MT_A_Sk_MI_CoS[1...M]            Sn-X-L/MT_A_Sk_MI_TC2PHBMapping[1...M]            Sn-X-L/MT_A_Sk_MI_QoSDecodingMode[1...M]            Sn-X-L/MT_A_Sk_MI_Mode[1...M]            Sn-X-L/MT_A_Sk_MI_LCK_Period[1...M]            Sn-X-L/MT_A_Sk_MI_LCK_CoS[1...M]            Sn-X-L/MT_A_Sk_MI_Admin_State            Sn-X-L/MT_A_Sk_MI_AIS_Period[1...M]            Sn-X-L/MT_A_Sk_MI_AIS_CoS [1...M]            Sn-X-L/MT_A_Sk_MI_GAL_Enable [1...M]            Sn-X-L/MT_A_Sk_MI_LCK_OAM_Tool [1...M]            Sn-X-L/MT_A_Sk_MI_AIS_OAM_Tool [1...M]            Sn-X-L/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p>	<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB            MT_CI_SSF            MI_CI_LStack</p> <p><b>SCC_CP:</b>            SCC_CI_Data            SCC_CI_SSF</p> <p><b>Sn-X-L/MT_A_Sk_MP:</b>            Sn-X-L/MT_A_Sk_MI_AcSL            Sn-X-L/MT_A_Sk_MI_AcEXI            Sn-X-L/MT_A_Sk_MI_LastValidUPI            Sn-X-L/MT_A_Sk_MI_cPLM            Sn-X-L/MT_A_Sk_MI_cLFD            Sn-X-L/MT_A_Sk_MI_cEXM            Sn-X-L/MT_A_Sk_MI_cUPM</p>

**Processes:**

A process diagram of this function is shown in Figure 11-8.



**Figure 11-8 – Sn-X-L/MT\_A\_Sk process diagram**

See process diagram and process description in clause 11.1.1.2. The additional Sn-X-L\_AI\_X<sub>AR</sub> interface is not connected to any of the internal processes.

**Defects:**

dPLM – See clause 6.2.4.2 of [ITU-T G.806].

dLFD – See clause 6.2.5.2 of [ITU-T G.806].

dUPM – See clause 8.4.2.

dEXM – See clause 6.2.4.4 of [ITU-T G.806].

**Consequent actions:** The function shall perform the following consequent actions:

aSSF ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

aAIS ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

**Defect correlations:** The function shall perform the following defect correlations to determine the most probable fault cause (see clause 6.4 of [ITU-T G.806]). This fault cause shall be reported to the EMF.

cPLM ← dPLM and (not AI\_TSF)

cLFD ← dLFD and (not dPLM) and (not AI\_TSF)

cEXM ← dEXM and (not dPLM) and (not dLFD) and (not AI\_TSF)

cUPM ← dUPM and (not dEXM) and (not dPLM) and (not dLFD) and (not AI\_TSF)

**Performance monitoring:** For further study.

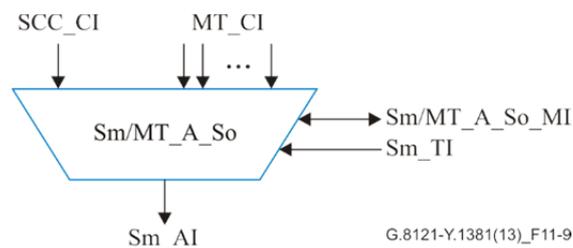
### 11.1.3 VC-m to MPLS-TP adaptation functions (*Sm/MT\_A*; *m=11, 11-X, 12, 12-X*)

#### 11.1.3.1 VC-m to MPLS-TP adaptation source function (*Sm/MT\_A\_So*)

This function maps MT\_CI information onto an Sm\_AI signal (*m=11, 11-X, 12, 12-X*).

Data at the Sm\_AP is a VC-m (*m = 11, 11-X, 12, 12-X*), with a payload as described in [ITU-T G.707], but with indeterminate POH bytes: J2, V5[1-4], V5[8].

**Symbol:**



**Figure 11-9 – Sm/MT\_A\_So symbol**

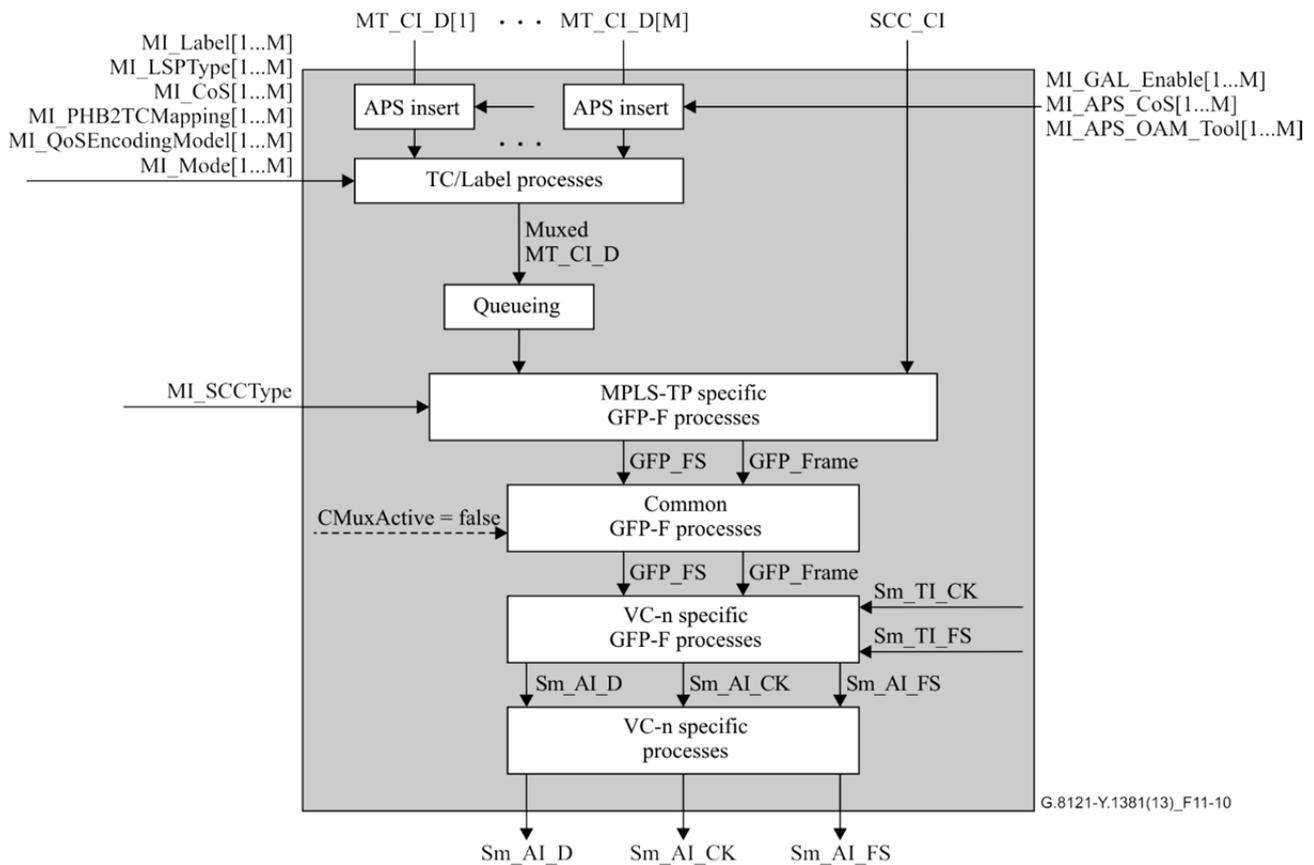
**Interfaces:**

**Table 11-5 – Sm/MT\_A\_So interfaces**

Inputs	Outputs
<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB  <b>SCC_CP:</b>            SCC_CI_Data  <b>Sm_TP:</b>            Sm_TI_Clock            Sm_TI_FrameStart  <b>Sm/MT_A_So_MP:</b>            Sm/MT_A_So_MI_SCCType            Sm/MT_A_So_MI_Label[1...M]            Sm/MT_A_So_MI_LSPTType[1...M]            Sm/MT_A_So_MI_CoS[1...M]            Sm/MT_A_So_PHB2TCMapping[1...M]            Sm/MT_A_So_MI_QoSEncodingMode[1...M]            Sm/MT_A_So_MI_Mode[1...M]</p> <p>Sm/MT_A_So_MI_GAL_Enable[1...M]            Sm/MT_A_So_MI_APS_OAM_CoS[1...M]            Sm/MT_A_So_MI_APS_OAM_Tool[1...M]</p>	<p><b>Sm_AP:</b>            Sm_AI_Data            Sm_AI_Clock            Sm_AI_FrameStart</p>

**Processes:**

A process diagram of this function is shown in Figure 11-10.



**Figure 11-10 – Sm/MT\_A\_So process diagram**

– *TC/Label processes:*

See clause 8.2.1.

– *Queueing process:*

See clause 8.3.

– *MPLS-TP-specific GFP-F source process:*

See clause 8.4.1.

– *Common GFP source process:*

See clause 8.5.3.1 of [ITU-T G.806]. GFP channel multiplexing is not supported (CMuxActive=false).

– *VC-m-specific GFP source process:*

See clause 8.5.2.1 of [ITU-T G.806]. The GFP frames are mapped into the VC-m payload area according to clause 10.6 of [ITU-T G.707].

– *VC-m-specific source process:*

**V5[5-7] and K4[1]:** Signal label information is derived directly from the adaptation function type. The value for "GFP mapping" in Table 9-13 of [ITU-T G.707] is placed in the K4[1] extended signal label field as described in clause 8.2.3.2 of [ITU-T G.783].

**K4[2]:** For Sm/MT\_A\_So with m = 11, 12, the K4[2] bit is sourced as all-zeros.

NOTE 1 – For Sm/MT\_A\_So with m = 11-X, 12-X, the K4[2] bit is undefined at the Sm-X\_AP output of this function (as per clause 13 of [ITU-T G.783]).

NOTE 2 – For Sm/MT\_A\_So with m = 11, 11-X, 12, 12-X, 2, the K4[3-8], V5[1-4] and V5[8] bits are undefined at the Sm-X\_AP output of this function (as per clause 13 of [ITU-T G.783]).

**Defects:** None.

**Consequent actions:** None.

**Defect correlations:** None.

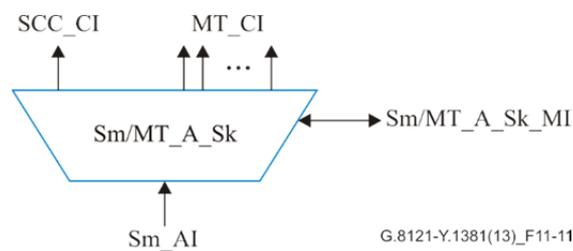
**Performance monitoring:** For further study.

### 11.1.3.2 VC-m to MPLS-TP adaptation sink function (Sm/MT\_A\_Sk)

This function extracts MT\_CI information from the Sm\_AI signal (m=11, 11-X, 12, 12-X), delivering MT\_CI.

Data at the Sm\_AP is a VC-m (m=11, 11-X, 12, 12-X) but with indeterminate POH bytes J2, V5[1-4], V5[8], as per [ITU-T G.707].

**Symbol:**



**Figure 11-11 – Sm/MT\_A\_Sk symbol**

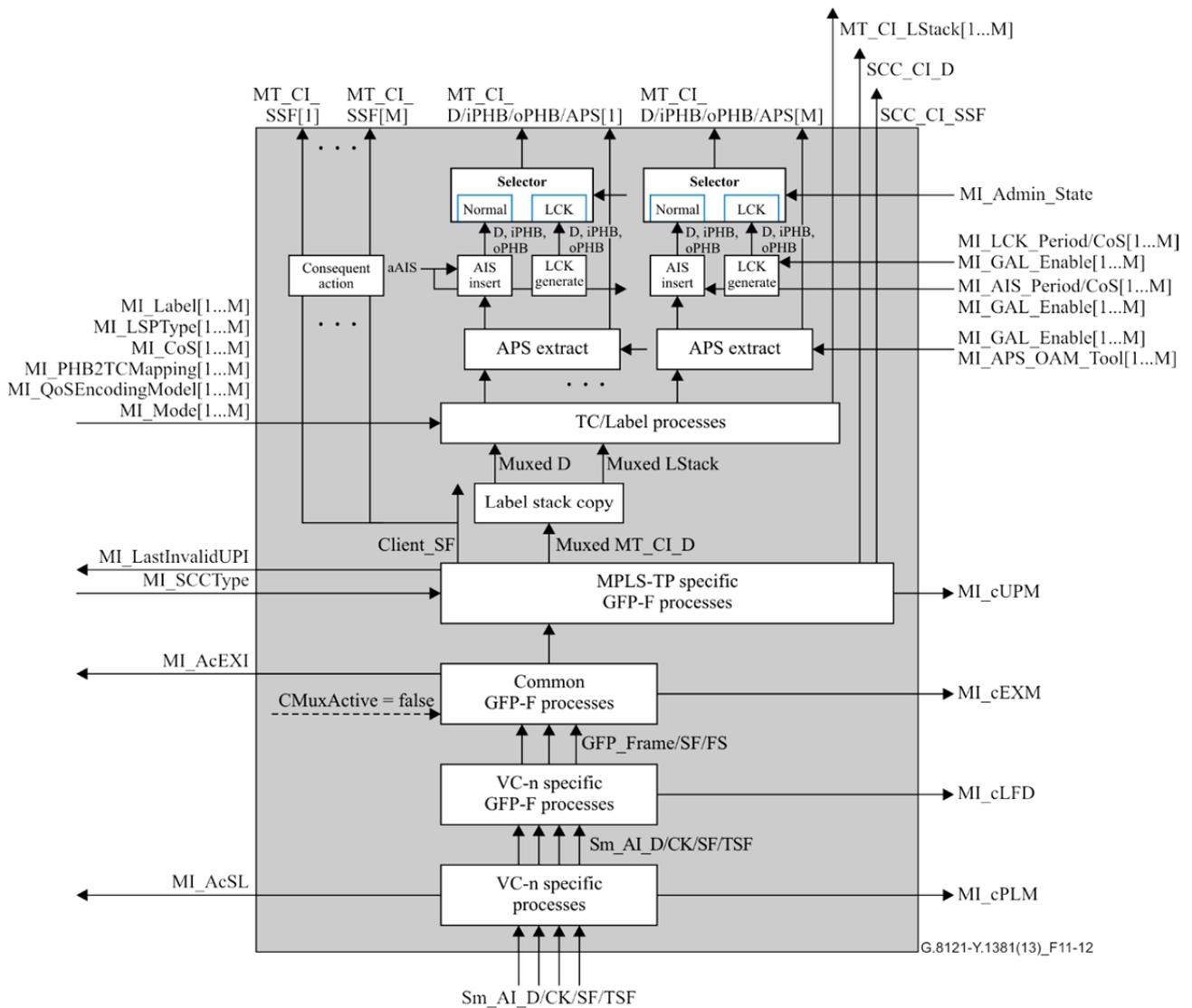
**Interfaces:**

**Table 11-6 – Sm/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>Sm_AP:</b>  Sm_AI_Data  Sm_AI_Clock  Sm_AI_FrameStart  Sm_AI_TSF</p> <p><b>Sm/MT_A_Sk_MP:</b>  Sm/MT_A_Sk_MI_SCCType  Sm/MT_A_Sk_MI_Label[1...M]  Sm/MT_A_Sk_MI_LSPTType[1...M]  Sm/MT_A_Sk_MI_CoS[1...M]  Sm/MT_A_Sk_MI_TC2PHBMapping[1...M]  Sm/MT_A_Sk_MI_QoSDecodingMode[1...M]  Sm/MT_A_Sk_MI_Mode[1...M]</p> <p>Sm/MT_A_Sk_MI_LCK_Period[1...M]  Sm/MT_A_Sk_MI_LCK_CoS[1...M]  Sm/MT_A_Sk_MI_Admin_State  Sm/MT_A_Sk_MI_AIS_Period[1...M]  Sm/MT_A_Sk_MI_AIS_CoS[1...M]  Sm/MT_A_Sk_MI_GAL_Enable[1...M]  Sm/MT_A_Sk_MI_LCK_OAM_Tool [1...M]  Sm/MT_A_Sk_MI_AIS_OAM_Tool[1...M]</p> <p>Sm/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p>	<p><b>Each MT_CP:</b>  MT_CI_Data  MT_CI_iPHB  MT_CI_oPHB  MT_CI_SSF  MI_CI_LStack</p> <p><b>SCC_CP:</b>  SCC_CI_Data  SCC_CI_SSF</p> <p><b>Sm/MT_A_Sk_MP:</b>  Sm/MT_A_Sk_MI_AcSL  Sm/MT_A_Sk_MI_AcEXI  Sm/MT_A_Sk_MI_LastValidUPI  Sm/MT_A_Sk_MI_cPLM  Sm/MT_A_Sk_MI_cLFD  Sm/MT_A_Sk_MI_cEXM  Sm/MT_A_Sk_MI_cUPM</p>

**Processes:**

A process diagram of this function is shown in Figure 11-12.



**Figure 11-12 – Sm/MT\_A\_Sk process diagram**

– *Selector generation process:*

See clause 8.6.1. The normal CI is blocked if Admin\_State = LOCKED.

– *AIS insert process:*

See clause 8.6.2. There is a single AIS insert process for each MT.

– *LCK generation process:*

See clause 8.6.3. There is a single LCK insert process for each MT.

– *TC/Label processes:*

See clause 8.2.2.

– *Label stack copy process:*

See clause 8.2.3.

– *MPLS-T- specific GFP-F sink process:*

See clause 8.4.2.

– *Common GFP sink process:*

See clause 8.5.3.2 of [ITU-T G.806]. GFP channel multiplexing is not supported (CMuxActive=false).

– *VC-m-specific GFP sink process:*

See clause 8.5.2.2 of [ITU-T G.806]. The GFP frames are de-mapped from the VC-m payload area according to clause 10.6 of [ITU-T G.707].

– *VC-m-specific sink process:*

**V5[5-7] and K4[1]:** The signal label is recovered from the extended signal label position as described in clause 8.2.3.2 of [ITU-T G.783] and clause 6.2.4.2 of [ITU-T G.806]. The signal label for "GFP mapping" in Table 9-13 of [ITU-T G.707] shall be expected. The accepted value of the signal label is also available at the Sm/MT\_A\_Sk\_MP.

**Defects:**

dPLM – See clause 6.2.4.2 of [ITU-T G.806].

dLFD – See clause 6.2.5.2 of [ITU-T G.806].

dUPM – See clause 8.4.2.

dEXM – See clause 6.2.4.4 of [ITU-T G.806].

**Consequent actions:** The function shall perform the following consequent actions:

aSSF ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

aAIS ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

**Defect correlations:** The function shall perform the following defect correlations to determine the most probable fault cause (see clause 6.4 of [ITU-T G.806]). This fault cause shall be reported to the EMF.

cPLM ← dPLM and (not AI\_TSF)

cLFD ← dLFD and (not dPLM) and (not AI\_TSF)

cEXM ← dEXM and (not dPLM) and (not dLFD) and (not AI\_TSF)

cUPM ← dUPM and (not dEXM) and (not dPLM) and (not dLFD) and (not AI\_TSF)

**Performance monitoring:** For further study.

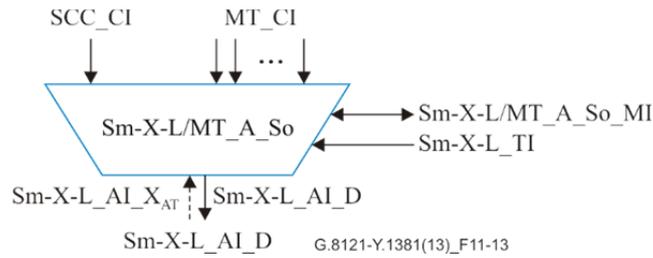
#### **11.1.4 LCAS-capable VC-m to MPLS-TP adaptation functions (Sm-X-L/MT\_A; m=11, 12)**

##### **11.1.4.1 LCAS-capable VC-m to MPLS-TP adaptation source function (Sm-X-L/MT\_A\_So)**

This function maps MT\_CI information onto an Sm-X-L\_AI signal (m=11, 12).

Data at the Sm-X-L\_AP is a VC-m-X (m = 11, 12), with a payload as described in [ITU-T G.707], but with indeterminate POH bytes: J2, V5[1-4], V5[8].

**Symbol:**



**Figure 11-13 – Sm-X-L/MT\_A\_So symbol**

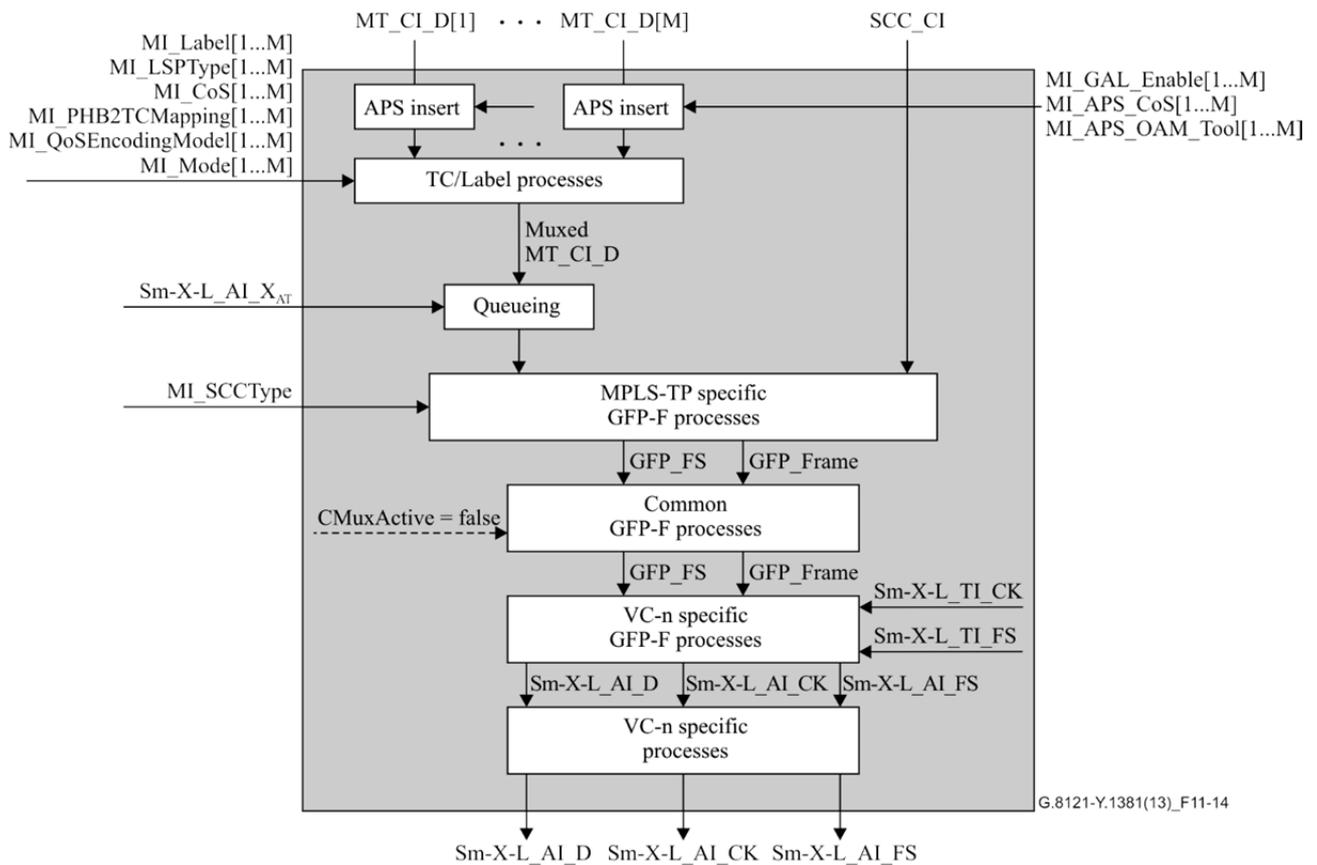
**Interfaces:**

**Table 11-7 – Sm-X-L/MT\_A\_So interfaces**

Inputs	Outputs
<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB  <b>SCC_CP:</b>            SCC_CI_Data  <b>Sm-X-L_AP:</b>            Sm-X-L_AI_XAT  <b>Sm-X-L_TP:</b>            Sm-X-L_TI_Clock            Sm-X-L_TI_FrameStart  <b>Sm-X-L/MT_A_So_MP:</b>            Sm-X-L/MT_A_So_MI_SCCType            Sm-X-L/MT_A_So_MI_Label[1...M]            Sm-X-L/MT_A_So_MI_LSPTType[1...M]            Sm-X-L/MT_A_So_MI_CoS[1...M]            Sm-X-L/MT_A_So_PHB2TCMapping[1...M]            Sm-X-L/MT_A_So_MI_QoSEncodingMode[1...M]            Sm-X-L/MT_A_So_MI_Mode[1...M]</p> <p>Sm-X-L/MT_A_So_MI_GAL_Enable[1...M]            Sm-X-L/MT_A_So_MI_APS_OAM_CoS[1...M]            Sm-X-L/MT_A_So_MI_APS_OAM_Tool[1...M]</p>	<p><b>Sm-X-L_AP:</b>            Sm-X-L_AI_Data            Sm-X-L_AI_Clock            Sm-X-L_AI_FrameStart</p>

**Processes:**

A process diagram of this function is shown in Figure 11-14.



**Figure 11-14 – Sm-X-L/MT\_A\_So process diagram**

The processes have the same definition as in clause 11.1.1.1.

**Defects:** None.

**Consequent actions:** None.

**Defect correlations:** None.

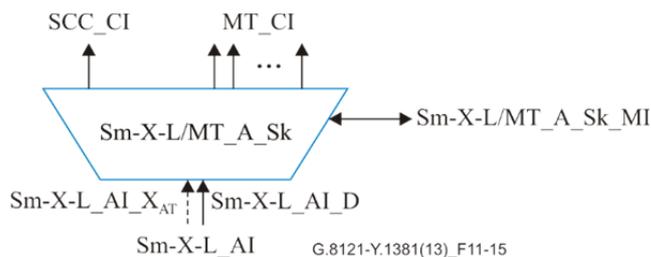
**Performance monitoring:** For further study.

#### 11.1.4.2 LCAS-capable VC-m to MPLS-TP adaptation sink function (Sm-X-L/MT\_A\_Sk)

This function extracts MT\_CI information from the Sm-X-L\_AI signal (m=11, 12), delivering MT\_CI.

Data at the Sm-X-L\_AP is a VC-m-Xv (m=11, 12) but with indeterminate POH bytes J2, V5[1-4], V5[8], as per [ITU-T G.707].

**Symbol:**



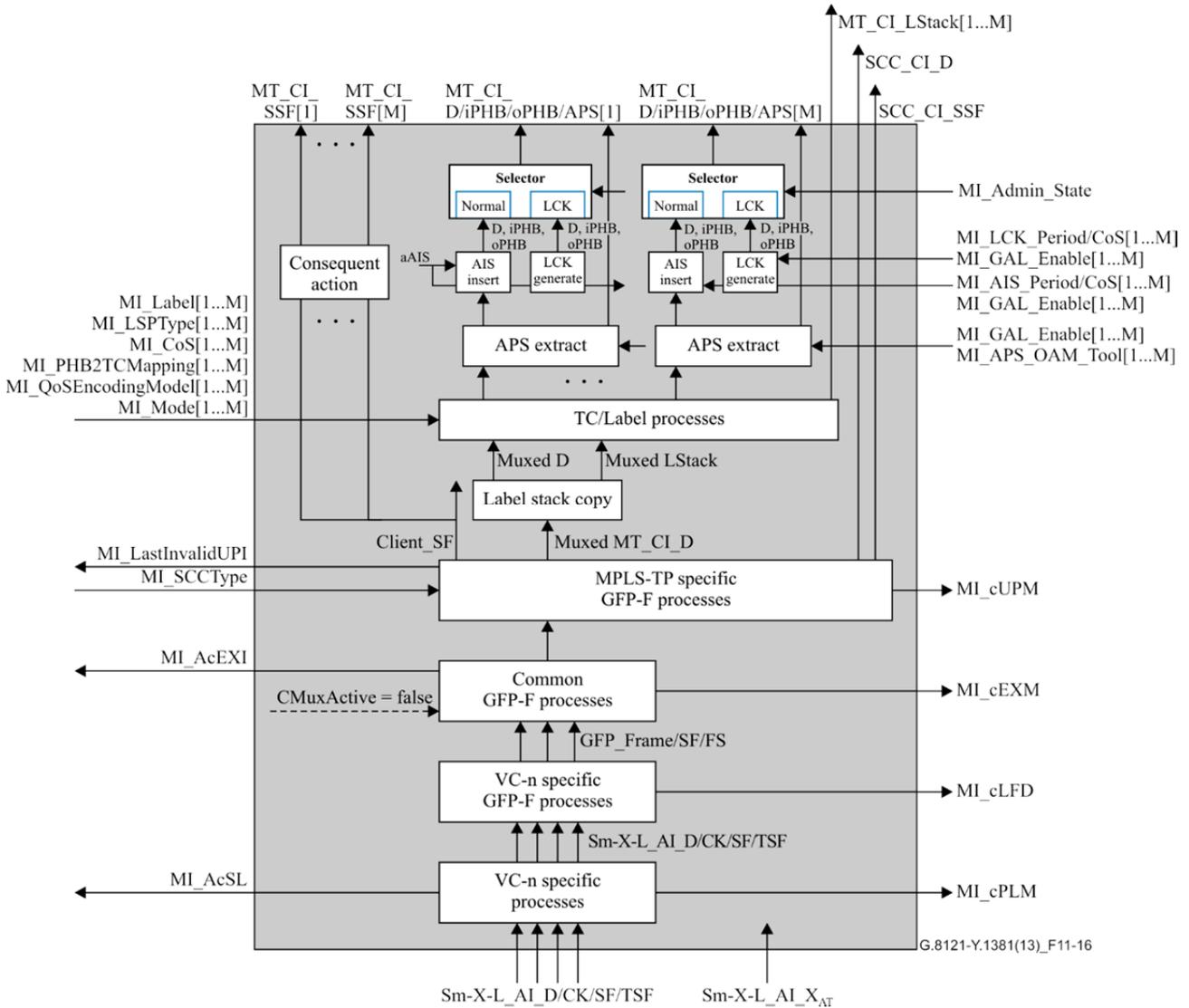
**Figure 11-15 – Sm-X-L/MT\_A\_Sk symbol**

**Interfaces:**

**Table 11-8 – Sm-X-L/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>Sm-X-L_AP:</b>  Sm-X-L_AI_Data  Sm-X-L_AI_Clock  Sm-X-L_AI_FrameStart  Sm-X-L_AI_TSF  Sm-X-L_AI_XAR</p> <p><b>Sm-X-L/MT_A_Sk_MP:</b>  Sm-X-L/MT_A_Sk_MI_SCCType  Sm-X-L/MT_A_Sk_MI_Label[1...M]  Sm-X-L/MT_A_Sk_MI_LSPTType[1...M]  Sm-X-L/MT_A_Sk_MI_CoS[1...M]  Sm-X-L/MT_A_Sk_MI_TC2PHBMapping[1...M]  Sm-X-L/MT_A_Sk_MI_QoSDecodingMode[1...M]  Sm-X-L/MT_A_Sk_MI_Mode[1...M]  Sm-X-L/MT_A_Sk_MI_LCK_Period[1...M]  Sm-X-L/MT_A_Sk_MI_LCK_CoS[1...M]  Sm-X-L/MT_A_Sk_MI_Admin_State  Sm-X-L/MT_A_Sk_MI_AIS_Period[1...M]  Sm-X-L/MT_A_Sk_MI_AIS_CoS[1...M]  Sm-X-L/MT_A_Sk_MI_GAL_Enable[1...M]  Sm-X-L/MT_A_Sk_MI_LCK_OAM_Tool [1...M]  Sm-X-L/MT_A_Sk_MI_AIS_OAM_Tool [1...M]  Sm-X-L/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p>	<p><b>Each MT_CP:</b>  MT_CI_Data  MT_CI_iPHB  MT_CI_oPHB  MT_CI_SSF  MI_CI_LStack</p> <p><b>SCC_CP:</b>  SCC_CI_Data  SCC_CI_SSF</p> <p><b>Sm-X-L/MT_A_Sk_MP:</b>  Sm-X-L/MT_A_Sk_MI_AcSL  Sm-X-L/MT_A_Sk_MI_AcEXI  Sm-X-L/MT_A_Sk_MI_LastValidUPI  Sm-X-L/MT_A_Sk_MI_cPLM  Sm-X-L/MT_A_Sk_MI_cLFD  Sm-X-L/MT_A_Sk_MI_cEXM  Sm-X-L/MT_A_Sk_MI_cUPM</p>

**Processes:**



**Figure 11-16 – Sm-X-L/MT\_A\_Sk process diagram**

See process diagram and process description in clause 11.1.1.2. The additional Sm-X-L\_AI\_X<sub>AR</sub> interface is not connected to any of the internal processes.

**Defects:**

- dPLM – See clause 6.2.4.2 of [ITU-T G.806].
- dLFD – See clause 6.2.5.2 of [ITU-T G.806].
- dUPM – See clause 8.4.2.
- dEXM – See clause 6.2.4.4 of [ITU-T G.806].

**Consequent actions:** The function shall perform the following consequent actions:

- aSSF ← AI\_TSF or dPLM or dLFD or dUPM or dEXM
- aAIS ← AI\_or dPLM or dLFD or dUPM or dEXM

**Defect correlations:** The function shall perform the following defect correlations to determine the most probable fault cause (see clause 6.4 of [ITU-T G.806]). This fault cause shall be reported to the EMF.

cPLM ← dPLM and (not AI\_TSF)

cLFD ← dLFD and (not dPLM) and (not AI\_TSF)

cEXM ← dEXM and (not dPLM) and (not dLFD) and (not AI\_TSF)

cUPM ← dUPM and (not dEXM) and (not dPLM) and (not dLFD) and (not AI\_TSF)

**Performance monitoring:** For further study.

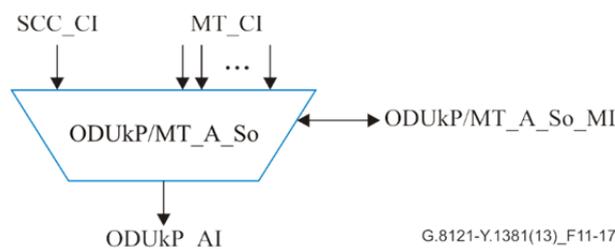
## 11.2 OTH to MPLS-TP adaptation function (O/MT\_A)

### 11.2.1 ODU<sub>k</sub> to MPLS-TP adaptation functions

#### 11.2.1.1 ODU<sub>k</sub> to MPLS-TP adaptation source function (ODU<sub>k</sub>P/MT\_A\_So)

The ODU<sub>k</sub>P/MT\_A\_So function creates the ODU<sub>k</sub> signal from a free running clock. It maps the MT\_CI information into the payload of the OPU<sub>k</sub>, adds the OPU<sub>k</sub> overhead (RES, PT) and default ODU<sub>k</sub> overhead.

**Symbol:**



**Figure 11-17 – ODU<sub>k</sub>P/MT\_A\_So symbol**

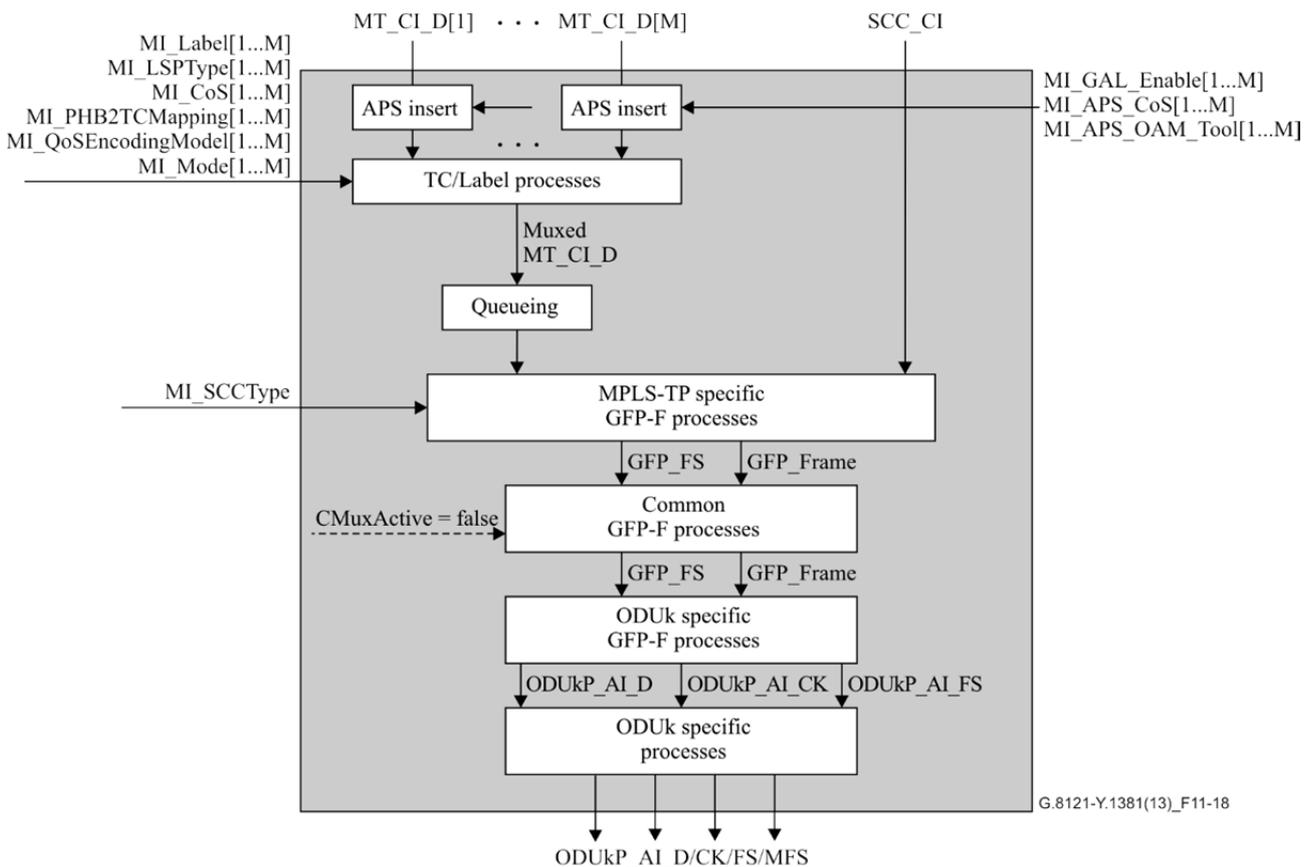
**Interfaces:**

**Table 11-9 – ODU<sub>k</sub>P/MT\_A\_So interfaces**

Inputs	Outputs
<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB</p> <p><b>SCC_CP:</b>            SCC_CI_Data</p> <p><b>ODU<sub>k</sub>P/MT_A_So_MP:</b>            ODU<sub>k</sub>P/MT_A_So_MI_Active            ODU<sub>k</sub>P/MT_A_So_MI_SCCType            ODU<sub>k</sub>P/MT_A_So_MI_Label[1...M]            ODU<sub>k</sub>P/MT_A_So_MI_LSPTType[1...M]            ODU<sub>k</sub>P/MT_A_So_MI_CoS[1...M]            ODU<sub>k</sub>P/MT_A_So_PHB2TCMapping[1...M]            ODU<sub>k</sub>P/MT_A_So_MI_QoSEncodingMode[1...M]            ODU<sub>k</sub>P/MT_A_So_MI_Mode[1...M]            ODU<sub>k</sub>P/MT_A_So_MI_GAL_Enable[1...M]            ODU<sub>k</sub>P/MT_A_So_MI_APS_OAM_CoS[1...M]            ODU<sub>k</sub>P/MT_A_So_MI_APS_OAM_Tool[1...M]</p>	<p><b>ODU<sub>k</sub>P_AP:</b>            ODU<sub>k</sub>P_AI_Data            ODU<sub>k</sub>P_AI_Clock            ODU<sub>k</sub>P_AI_FrameStart            ODU<sub>k</sub>P_AI_MultiFrameStart</p>

**Processes:**

A process diagram of this function is shown in Figure 11-18.



**Figure 11-18 – ODUkP/MT\_A\_So process diagram**

– *TC/Label processes:*

See clause 8.2.1.

– *Queueing process:*

See clause 8.3.

– *MPLS-TP-specific GFP-F source process:*

See clause 8.4.1.

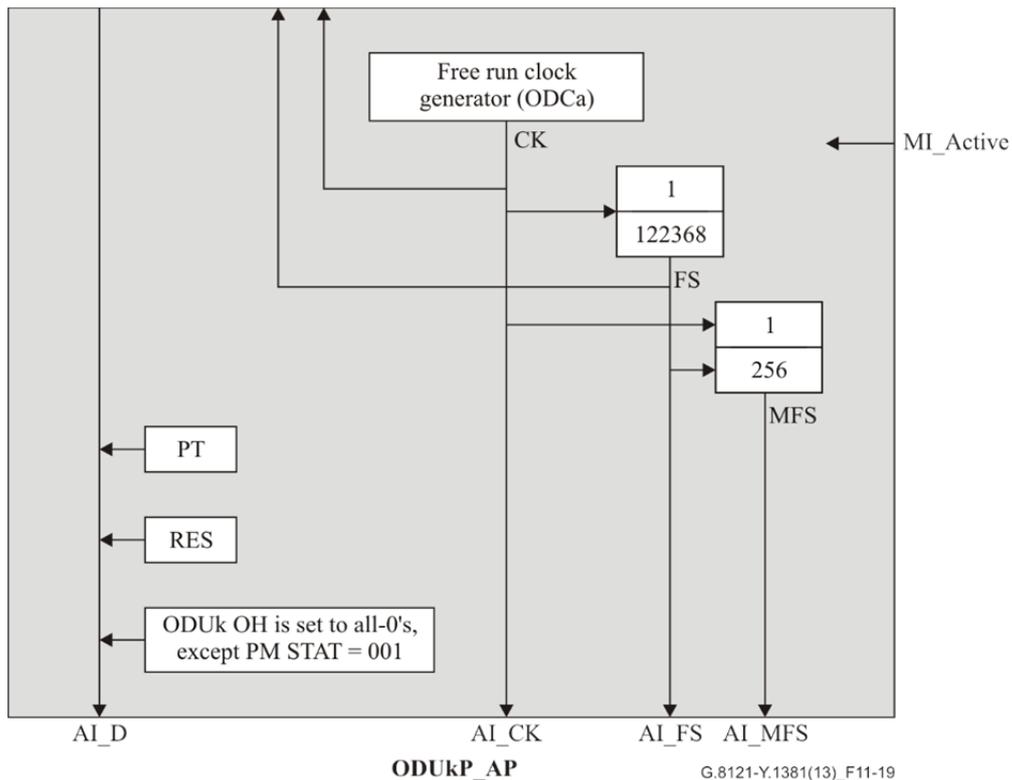
– *Common GFP source process:*

See clause 8.5.3.1 of [ITU-T G.806]. GFP channel multiplexing is not supported (CMuxActive=false).

– *ODUk-specific GFP source process:*

See clause 8.5.2.1 of [ITU-T G.806]. The GFP frames are mapped into the ODUk payload area according to clause 17.3 of [ITU-T G.709].

– ODUk-specific source process:



**Figure 11-19 – ODUkP-specific source processes**

**Clock and (multi)frame start signal generation:** The function shall generate a local ODUk clock (ODUKP\_AI\_CK) of " $239/(239 - k) * 4^{(k-1)} * 2\,488\,320\text{ kHz} \pm 20\text{ ppm}$ " from a free running oscillator. The jitter and wander requirements as defined in Annex A of [ITU-T G.8251] (ODCa clock) apply.

The function shall generate the (multi)frame start reference signals AI\_FS and AI\_MFS for the ODUk signal. The AI\_FS signal shall be active once per 122368 clock cycles. AI\_MFS shall be active once every 256 frames.

**PT:** The payload type information is derived directly from the adaptation function type. The value for "GFP mapping" shall be inserted into the PT byte position of the PSI overhead, as defined in clause 15.9.2.1.1 of [ITU-T G.709].

**RES:** The function shall insert all-0's into the RES bytes.

All other bits of the ODUk overhead should be sourced as "0"s, except the ODUk-PM STAT field which should be set to the value "normal path signal" (001).

**Defects:** None.

**Consequent actions:** None.

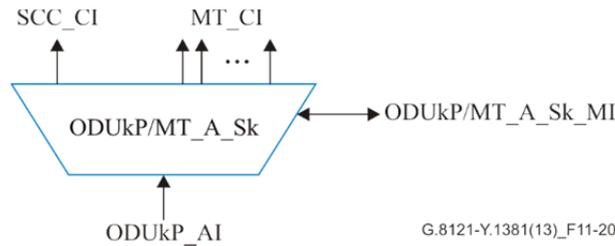
**Defect correlations:** None.

**Performance monitoring:** For further study.

#### 11.2.1.2 ODUk to MPLS-TP adaptation sink function (ODUKP/MT\_A\_Sk)

The ODUkP/MT\_A\_Sk extracts MT\_CI information from the ODUkP payload area. It extracts the OPUk overhead (PT and RES) and monitors the reception of the correct payload type.

**Symbol:**



**Figure 11-20 – ODUkP/MT\_A\_Sk symbol**

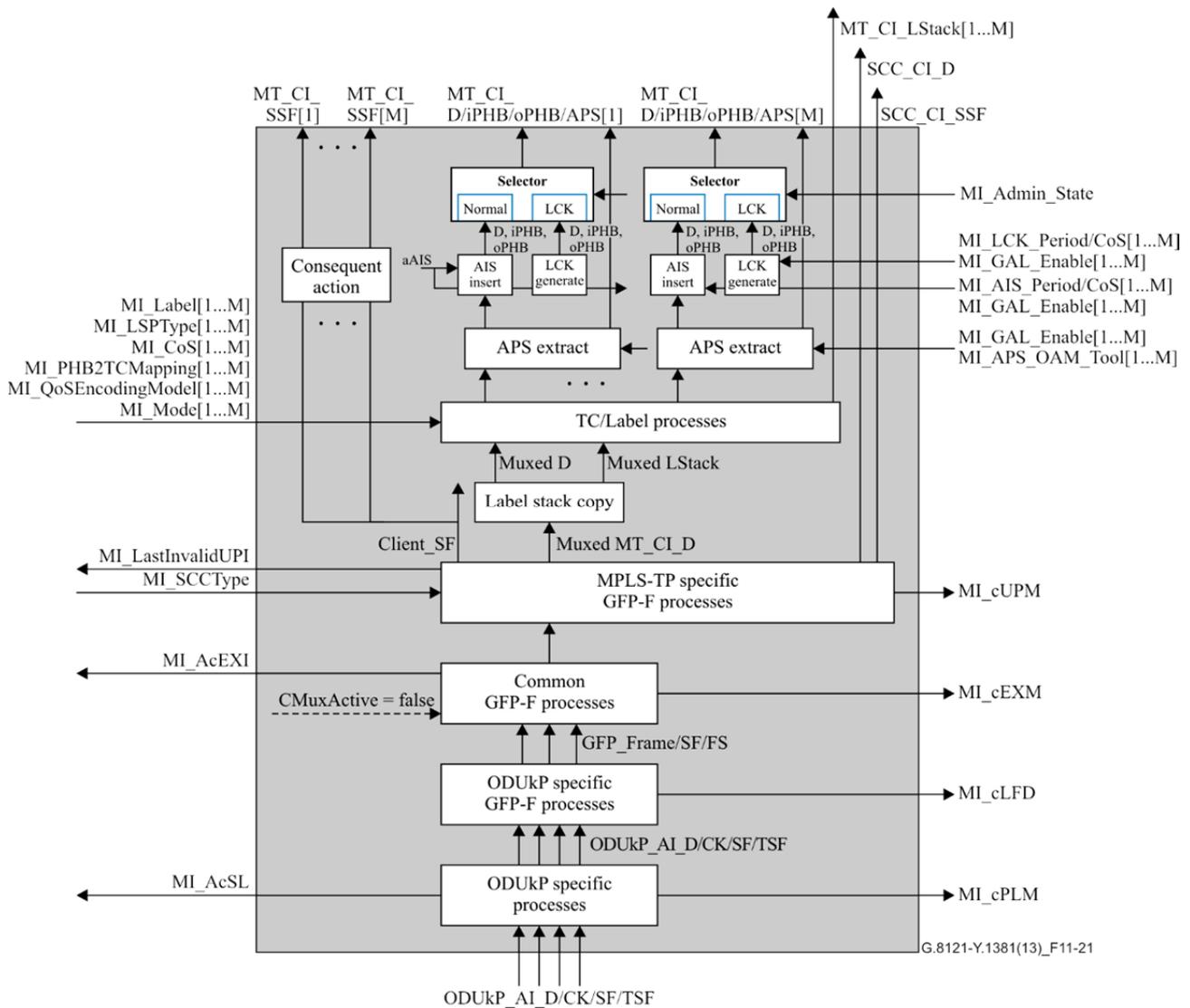
**Interfaces:**

**Table 11-10 – ODUkP/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>ODUkP_AP:</b>            ODUkP_AI_Data            ODUkP_AI_Clock            ODUkP_AI_FrameStart            ODUkP_AI_MultiFrameStart            ODUkP_AI_TSF</p> <p><b>ODUkP/MT_A_Sk_MP:</b>            ODUkP/MT_A_Sk_MI_Active            ODUkP/MT_A_Sk_MI_SCCType            ODUkP/MT_A_Sk_MI_Label[1...M]            ODUkP/MT_A_Sk_MI_LSPTType[1...M]            ODUkP/MT_A_Sk_MI_CoS[1...M]            ODUkP/MT_A_Sk_MI_TC2PHBMapping[1...M]            ODUkP/MT_A_Sk_MI_QoSDecodingMode[1...M]            ODUkP/MT_A_Sk_MI_Mode[1...M]</p> <p>ODUkP/MT_A_Sk_MI_LCK_Period[1...M]            ODUkP/MT_A_Sk_MI_LCK_CoS[1...M]            ODUkP/MT_A_Sk_MI_Admin_State            ODUkP/MT_A_Sk_MI_AIS_Period[1...M]            ODUkP/MT_A_Sk_MI_AIS_CoS[1...M]            ODUkP/MT_A_Sk_MI_GAL_Enable[1...M]            ODUkP/MT_A_Sk_MI_LCK_Tool[1...M]            ODUkP/MT_A_Sk_MI_AIS_Tool[1...M]</p> <p>ODUkP/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p>	<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB            MT_CI_SSF            MT_CI_LStack</p> <p><b>SCC_CP:</b>            SCC_CI_Data            SCC_CI_SSF</p> <p><b>ODUkP/MT_A_Sk_MP:</b>            ODUkP/MT_A_Sk_MI_AcPT            ODUkP/MT_A_Sk_MI_AcEXI            ODUkP/MT_A_Sk_MI_LastValidUPI            ODUkP/MT_A_Sk_MI_cPLM            ODUkP/MT_A_Sk_MI_cLFD            ODUkP/MT_A_Sk_MI_cEXM            ODUkP/MT_A_Sk_MI_cUPM</p>

**Processes:**

A process diagram of this function is shown in Figure 11-21.



**Figure 11-21 – ODUkP/MT\_A\_Sk process diagram**

– *Selector generation process:*

See clause 8.6.1. The normal CI is blocked if Admin\_State = LOCKED.

– *AIS insert process:*

See clause 8.6.2. There is a single AIS insert process for each MT.

– *LCK generation process:*

See clause 8.6.3. There is a single LCK insert process for each MT.

– *TC/Label processes:*

See clause 8.2.2.

– *Label stack copy process:*

See clause 8.2.3.

– *MPLS-TP-specific GFP-F sink process:*

See clause 8.4.2.

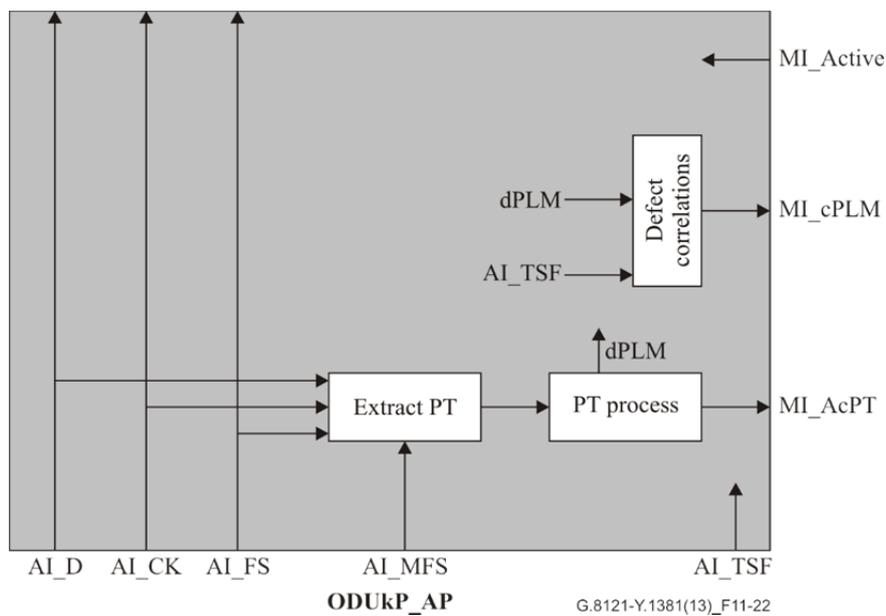
– *Common GFP sink process:*

See clause 8.5.3.2 of [ITU-T G.806]. GFP channel multiplexing is not supported (CMuxActive=false).

– *ODUk-specific GFP sink process:*

See clause 8.5.2.2 of [ITU-T G.806]. The GFP frames are de-mapped from the ODUk payload area according to clause 17.3 of [ITU-T G.709].

– *ODUk-specific sink process:*



**Figure 11-22 – ODUkP-specific sink processes**

**PT:** The function shall extract the PT byte from the PSI overhead as defined in clause 8.7.1 of [ITU-T G.798]. The payload type value for "GFP mapping" in clause 15.9.2.1.1 of [ITU-T G.709] shall be expected. The accepted PT value is available at the MP (MI\_AcPT) and is used for PLM defect detection.

**RES:** The value in the RES bytes shall be ignored.

**Defects:**

dPLM – See clause 6.2.4.1 of [ITU-T G.798].

dLFD – See clause 6.2.5.2 of [ITU-T G.806].

dEXM – See clause 6.2.4.4 of [ITU-T G.806].

dUPM – See clause 8.4.2.

**Consequent actions:** The function shall perform the following consequent actions:

aSSF ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

aAIS ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

**Defect correlations:** The function shall perform the following defect correlations to determine the most probable fault cause (see clause 6.4 of [ITU-T G.806]). This fault cause shall be reported to the EMF.

cPLM ← dPLM and (not AI\_TSF)

cLFD ← dLFD and (not dPLM) and (not AI\_TSF)

cEXM ← dEXM and (not dPLM) and (not dLFD) and (not AI\_TSF)

cUPM ← dUPM and (not dEXM) and (not dPLM) and (not dLFD) and (not AI\_TSF)

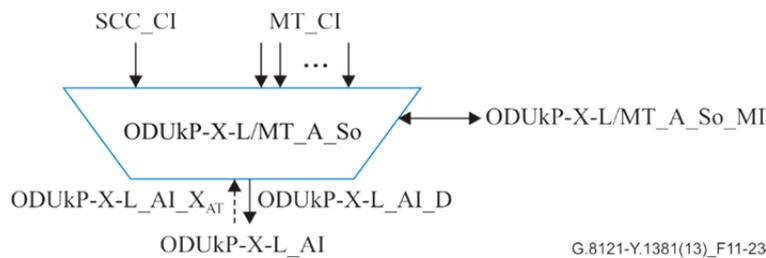
**Performance monitoring:** For further study.

### 11.2.2 LCAS-capable ODU<sub>k</sub> to MPLS-TP adaptation functions (ODU<sub>k</sub>P-X-L/MT\_A; k=1,2,3)

#### 11.2.2.1 LCAS-capable ODU<sub>k</sub> to MPLS-TP adaptation source function (ODU<sub>k</sub>P-X-L/MT\_A\_So)

The ODU<sub>k</sub>P-X-L/MT\_A\_So function creates the ODU<sub>k</sub>-X-L signal from a free running clock. It maps the MT\_CI information into the payload of the OPU<sub>k</sub>-X<sub>v</sub> (k = 1, 2, 3), and adds the OPU<sub>k</sub>-X<sub>v</sub> overhead (RES, vcPT).

**Symbol:**



**Figure 11-23 – ODU<sub>k</sub>P-X-L/MT\_A\_So symbol**

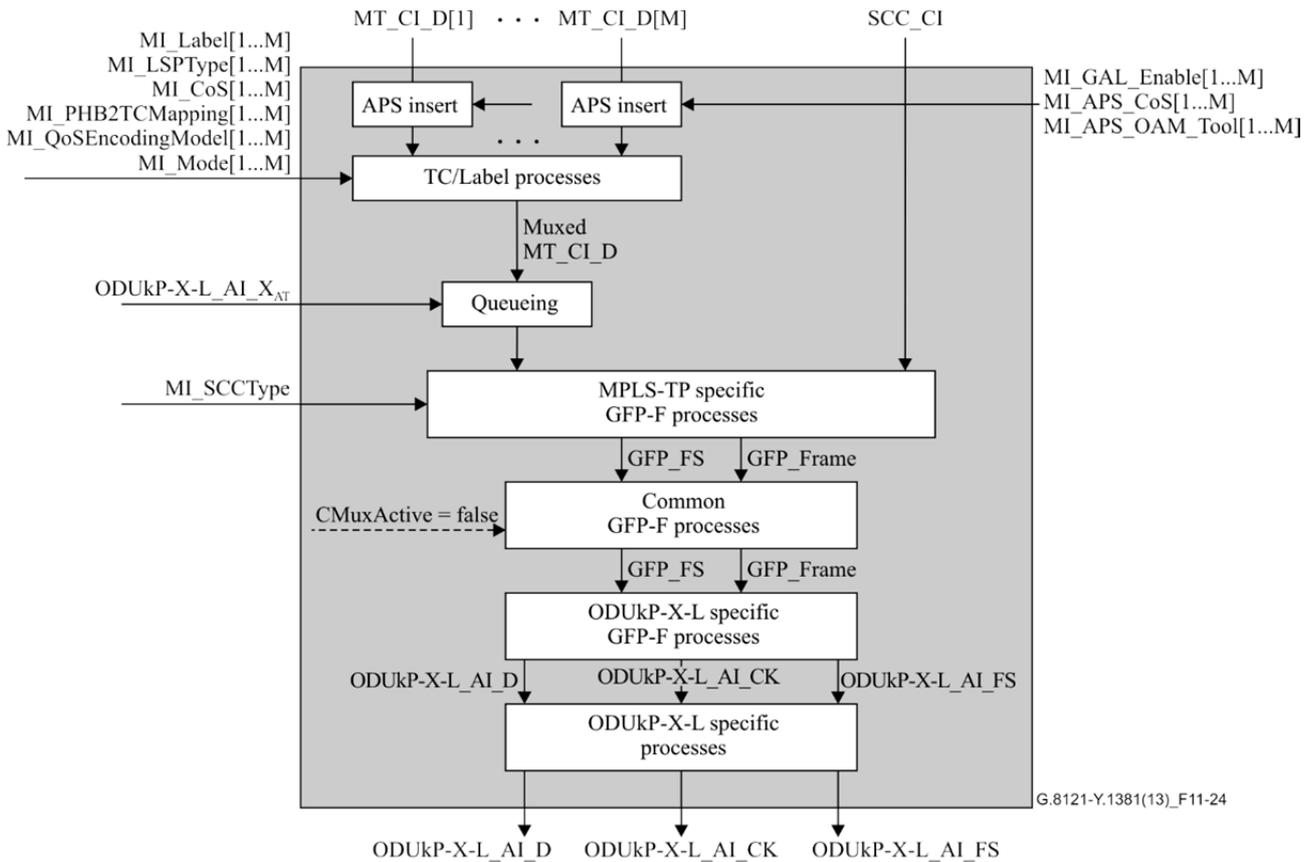
**Interfaces:**

**Table 11-11 – ODU<sub>k</sub>P-X-L/MT\_A\_So interfaces**

Inputs	Outputs
<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB</p> <p><b>SCC_CP:</b>            SCC_CI_Data</p> <p><b>ODU<sub>k</sub>P-X-L_AP:</b>            ODU<sub>k</sub>P-X-L_AI_X<sub>AT</sub></p> <p><b>ODU<sub>k</sub>P-X-L/MT_A_So_MP:</b>            ODU<sub>k</sub>P-X-L/MT_A_So_MI_Active            ODU<sub>k</sub>P-X-L/MT_A_So_MI_SCCType            ODU<sub>k</sub>P-X-L/MT_A_So_MI_Label[1...M]            ODU<sub>k</sub>P-X-L/MT_A_So_MI_LSPTType[1...M]            ODU<sub>k</sub>P-X-L/MT_A_So_MI_CoS[1...M]            ODU<sub>k</sub>P-X-L/MT_A_So_PHB2TCMapping[1...M]            ODU<sub>k</sub>P-X-L/MT_A_So_MI_QoSEncodingMode[1...M]            ODU<sub>k</sub>P-X-L/MT_A_So_MI_Mode[1...M]            ODU<sub>k</sub>P-X-L/MT_A_So_MI_GAL_Enable[1...M]            ODU<sub>k</sub>P-X-L/MT_A_So_MI_APS_OAM_CoS[1...M]            ODU<sub>k</sub>P-X-L/MT_A_So_MI_APS_OAM_Tool[1...M]</p>	<p><b>ODU<sub>k</sub>P-X-L_AP:</b>            ODU<sub>k</sub>P-X-L_AI_Data            ODU<sub>k</sub>P-X-L_AI_Clock            ODU<sub>k</sub>P-X-L_AI_FrameStart            ODU<sub>k</sub>P-X-L_AI_MultiFrameStart</p>

**Processes:**

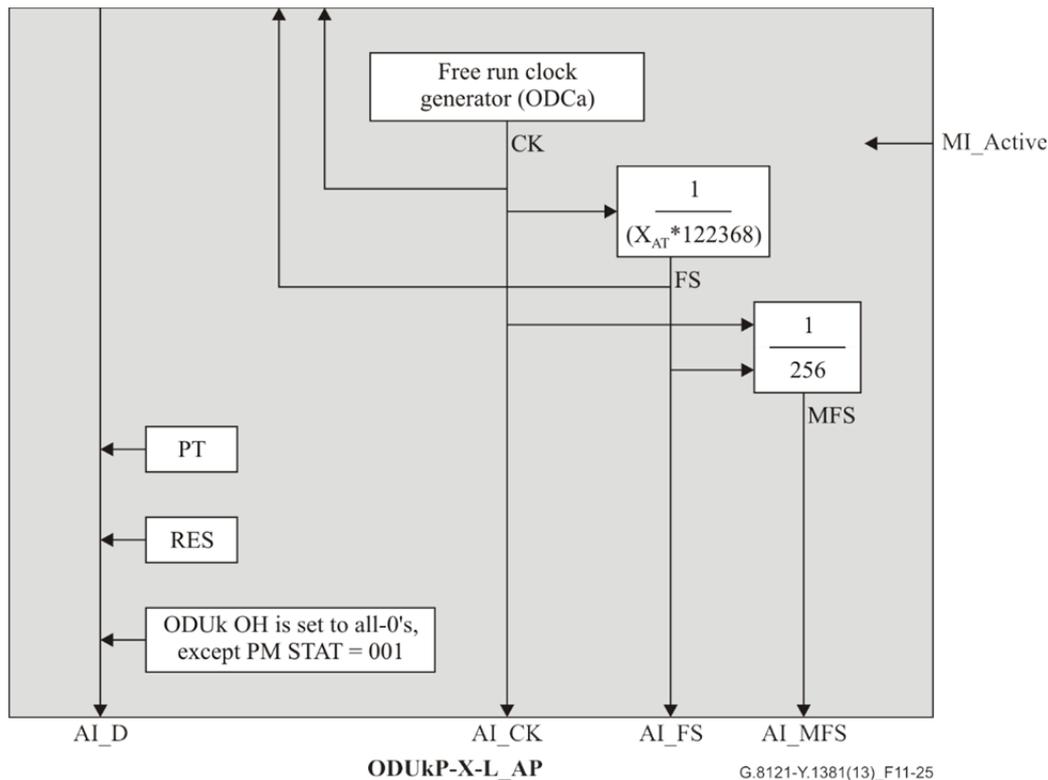
A process diagram of this function is shown in Figure 48.



**Figure 11-24 – ODUkP-X-L/MT\_A\_So process diagram**

The processes have the same definition as in clause 11.2.1.1.

## ODUkP-X-L specific source process:



**Figure 11-25 – ODUkP-X-L specific source processes**

**Clock and (Multi)frame start signal generation:** The function shall generate a local ODUk clock (ODUkP\_AI\_CK) of " $X_{AT} * 239 / (239 - k) * 4^{(k-1)} * 2\,488\,320$  kHz  $\pm$  20 ppm" from a free running oscillator. The jitter and wander requirements as defined in Annex A of [ITU-T G.8251] (ODCa clock) apply.

The function shall generate the (multi)frame start reference signals AI\_FS and AI\_MFS for the ODUk signal. The AI\_FS signal shall be active once per 122368 clock cycles. AI\_MFS shall be active once every 256 frames.

**vcPT:** The payload type information is derived directly from the adaptation function type. The value for "GFP mapping" shall be inserted into the vcPT byte position of the PSI overhead as defined in clause 18.1.2.2 of [ITU-T G.709].

**RES:** The function shall insert all-0's into the RES bytes.

**Defects:** None.

**Consequent actions:** None.

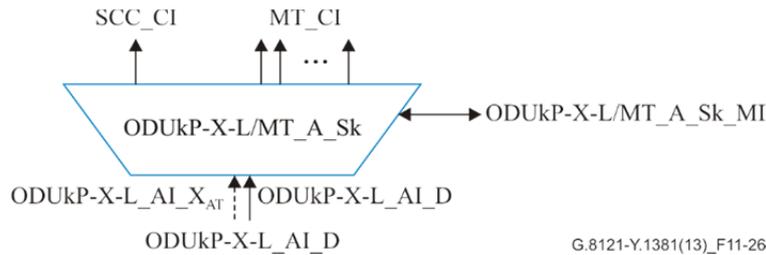
**Defect correlations:** None.

**Performance monitoring:** For further study.

### 11.2.2.2 LCAS-capable ODUk to MPLS-TP adaptation sink function (ODUkP-X-L/MT\_A\_Sk)

The ODUkP-X-L/MT\_A\_Sk extracts MT\_CI information from the ODUkP-Xv payload area. It extracts the OPUk-Xv overhead (vcPT and RES) and monitors the reception of the correct payload type.

**Symbol:**



G.8121-Y.1381(13)\_F11-26

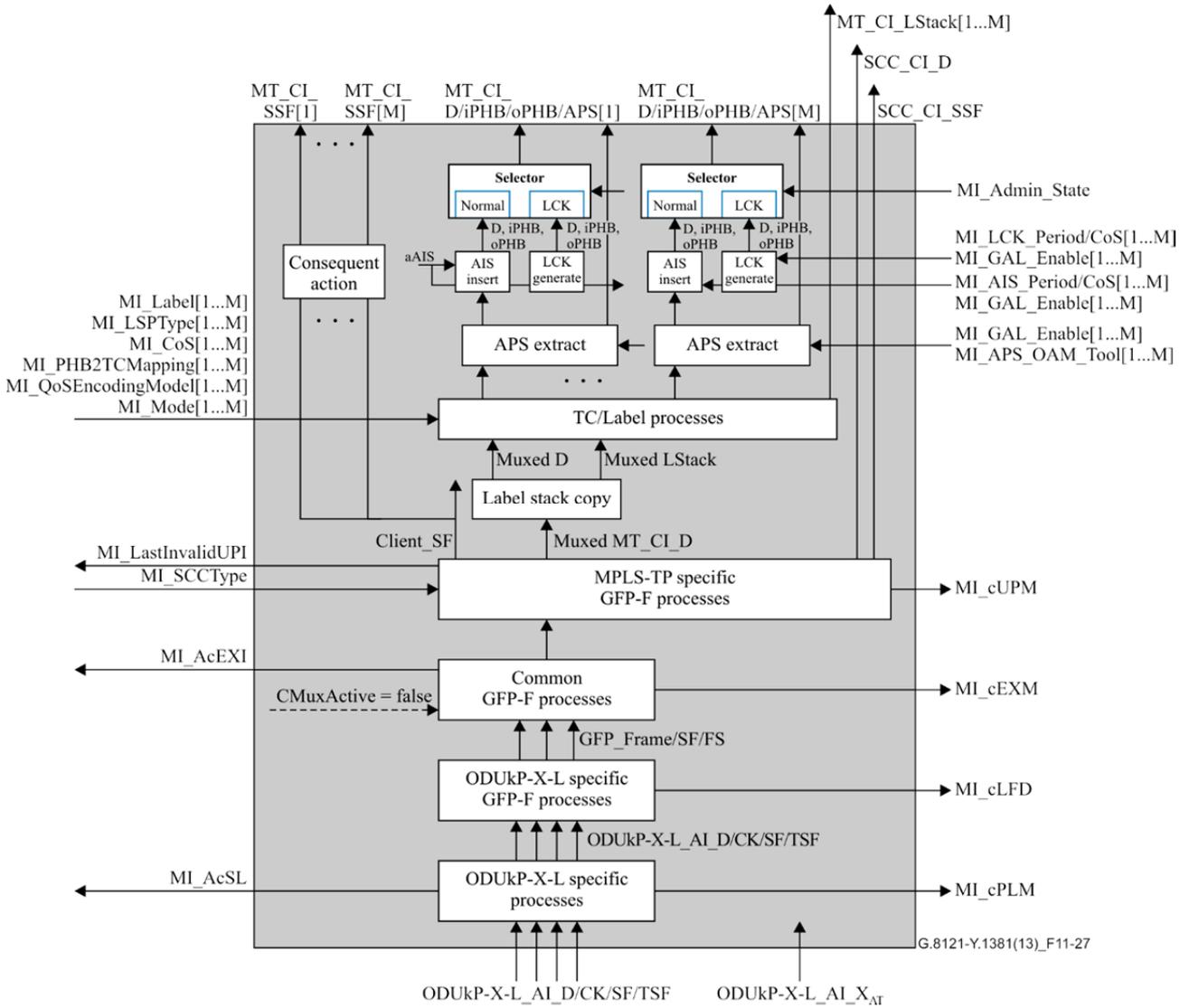
**Figure 11-26 – ODUkP-X-L/MT\_A\_Sk symbol**

**Interfaces:**

**Table 11-12 – ODUkP-X-L/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>ODUkP-X-L_AP:</b>            ODUkP-X-L_AI_Data            ODUkP-X-L_AI_Clock            ODUkP-X-L_AI_FrameStart            ODUkP-X-L_AI_MultiFrameStart            ODUkP-X-L_AI_TSF            ODUkP-X-L_AI_XAR</p> <p><b>ODUkP-X-L/MT_A_Sk_MP:</b>            ODUkP-X-L/MT_A_Sk_MI_Active            ODUkP-X-L/MT_A_Sk_MI_SCCType            ODUkP-X-L/MT_A_Sk_MI_Label[1...M]            ODUkP-X-L/MT_A_Sk_MI_LSPTType[1...M]            ODUkP-X-L/MT_A_Sk_MI_CoS[1...M]            ODUkP-X-L/MT_A_Sk_MI_TC2PHBMapping[1...M]            ODUkP-X-L/MT_A_Sk_MI_QoSDecodingMode[1...M]            ODUkP-X-L/MT_A_Sk_MI_Mode[1...M]</p> <p>ODUkP-X-L/MT_A_Sk_MI_LCK_Period[1...M]            ODUkP-X-L/MT_A_Sk_MI_LCK_CoS[1...M]            ODUkP-X-L/MT_A_Sk_MI_Admin_State            ODUkP-X-L/MT_A_Sk_MI_AIS_Period[1...M]            ODUkP-X-L/MT_A_Sk_MI_AIS_CoS[1...M]            ODUkP-X-L/MT_A_Sk_MI_GAL_Enable[1...M]            ODUkP-X-L/MT_A_Sk_MI_LCK_Tool[1...M]            ODUkP-X-L/MT_A_Sk_MI_AIS_Tool[1...M]</p> <p>ODUkP-X-L/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p>	<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB            MT_CI_SSF            MT_CI_LStack</p> <p><b>SCC_CP:</b>            SCC_CI_Data            SCC_CI_SSF</p> <p><b>ODUkP-X-L/MT_A_Sk_MP:</b>            ODUkP-X-L/MT_A_Sk_MI_AcVcPT            ODUkP-X-L/MT_A_Sk_MI_AcEXI            ODUkP-X-L/MT_A_Sk_MI_LastValidUPI            ODUkP-X-L/MT_A_Sk_MI_cVcPLM            ODUkP-X-L/MT_A_Sk_MI_cLFD            ODUkP-X-L/MT_A_Sk_MI_cEXM            ODUkP-X-L/MT_A_Sk_MI_cUPM</p>

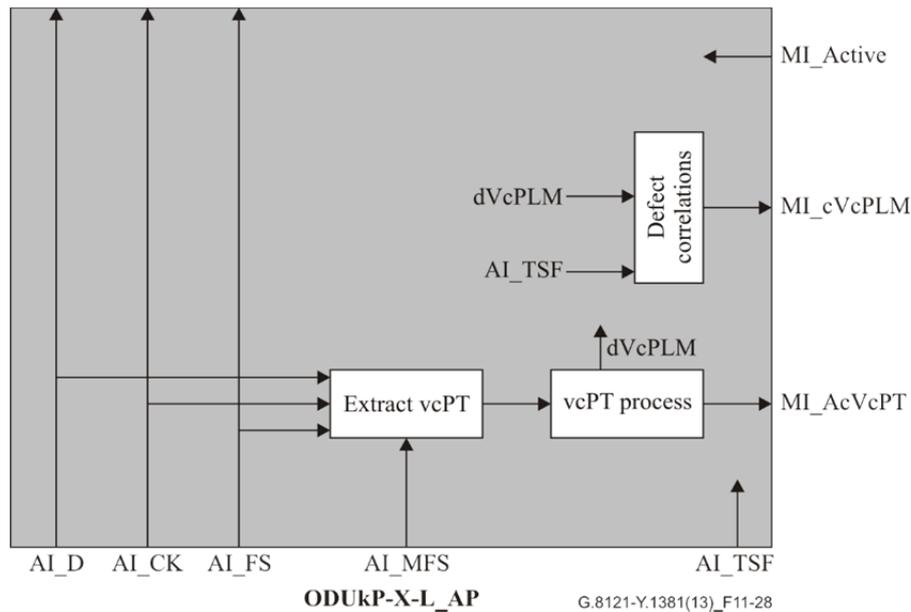
**Processes:**



**Figure 11-27 – ODUkP-X-L/MT\_A\_Sk process diagram**

See process diagram and process description in clause 11.2.1.2. The additional ODUkP-X-L\_AI\_X<sub>AR</sub> interface is not connected to any of the internal processes.

**ODUkP-X-L specific sink process:**



**Figure 11-28 – ODUkP-X-L specific sink processes**

**PT:** The function shall extract the vcPT byte from the PSI overhead as defined in clause 8.7.3 of [ITU-T G.798]. The payload type value for "GFP mapping" in clause 18.1.2.2 of [ITU-T G.709] shall be expected. The accepted PT value is available at the MP (MI\_AcPT) and is used for PLM defect detection.

**RES:** The value in the RES bytes shall be ignored.

**Defects:**

dVcPLM – See clause 6.2.4.2 of [ITU-T G.798].

dLFD – See clause 6.2.5.2 of [ITU-T G.806].

dUPM – See clause 8.4.2.

dEXM – See clause 6.2.4.4 of [ITU-T G.806].

**Consequent actions:** The function shall perform the following consequent actions:

aSSF ← AI\_TS or dVcPLM or dLFD or dUPM or dEXM

aAIS ← AI\_TS or dVcPLM or dLFD or dUPM or dEXM

**Defect correlations:** The function shall perform the following defect correlations to determine the most probable fault cause (see clause 6.4 of [ITU-T G.806]). This fault cause shall be reported to the EMF.

cVcPLM ← dVcPLM and (not AI\_TS)

cLFD ← dLFD and (not dVcPLM) and (not AI\_TS)

cEXM ← dEXM and (not dVcPLM) and (not dLFD) and (not AI\_TS)

cUPM ← dUPM and (not dEXM) and (not dVcPLM) and (not dLFD) and (not AI\_TS)

**Performance monitoring:** For further study.

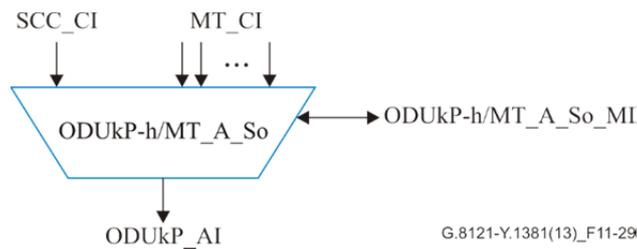
### 11.2.3 HAO-capable ODUk to MPLS-TP adaptation functions (ODUkP-h/MT\_A; k=ODUflex)

The Hitless Adjustment of ODUflex (HAO) capability is specified in [ITU-T G.7044].

#### 11.2.3.1 HAO-capable ODUk to MPLS-TP adaptation source function (ODUkP-h/MT\_A\_So)

The ODUkP-h/MT\_A\_So function creates the ODUk signal from a free running clock. It maps the MT\_CI information into the payload of the OPUk (k=flex) and it adds the OPUk overhead (RES, PT, RCOH) and default ODUk overhead.

**Symbol:**



**Figure 11-29 – ODUkP-h/MT\_A\_So symbol**

**Interfaces:**

**Table 11-13 – ODUkP-h/MT\_A\_So interfaces**

Inputs	Outputs
<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB</p> <p><b>ODUkP_RP:</b>            ODUkP_RI_RP            ODUkP_RI_TSCC            ODUkP_RI_NCS</p> <p><b>SCC_CP:</b>            SCC_CI_Data</p> <p><b>ODUkP-h/MT_A_So_MP:</b>            ODUkP-h/MT_A_So_MI_Active            ODUkP-h/MT_A_So_MI_SCCType            ODUkP-h/MT_A_So_MI_Label[1...M]            ODUkP-h/MT_A_So_MI_LSPTType[1...M]            ODUkP-h/MT_A_So_MI_CoS[1...M]            ODUkP-h/MT_A_So_PHB2TCMapping[1...M]            ODUkP-h/MT_A_So_MI_QoSEncodingMode[1...M]            ODUkP-h/MT_A_So_MI_Mode[1...M]</p> <p>ODUkP-h/MT_A_So_MI_GAL_Enable[1...M]            ODUkP-h/MT_A_So_MI_APS_OAM_CoS[1...M]            ODUkP-h/MT_A_So_MI_APS_OAM_Tool[1...M]</p> <p>ODUkP-h/MT_A_So_MI_INCREASE</p>	<p><b>ODUkP_AP:</b>            ODUkP_AI_Data            ODUkP_AI_Clock            ODUkP_AI_FrameStart            ODUkP_AI_MultiFrameStart            ODUkP_(A/M)I_RP            ODUkP_(A/M)I_TSCC</p> <p><b>ODUkP-h/MT_A_So_MP:</b>            ODUkP-h/MT_A_So_MI_ADJSTATE</p>

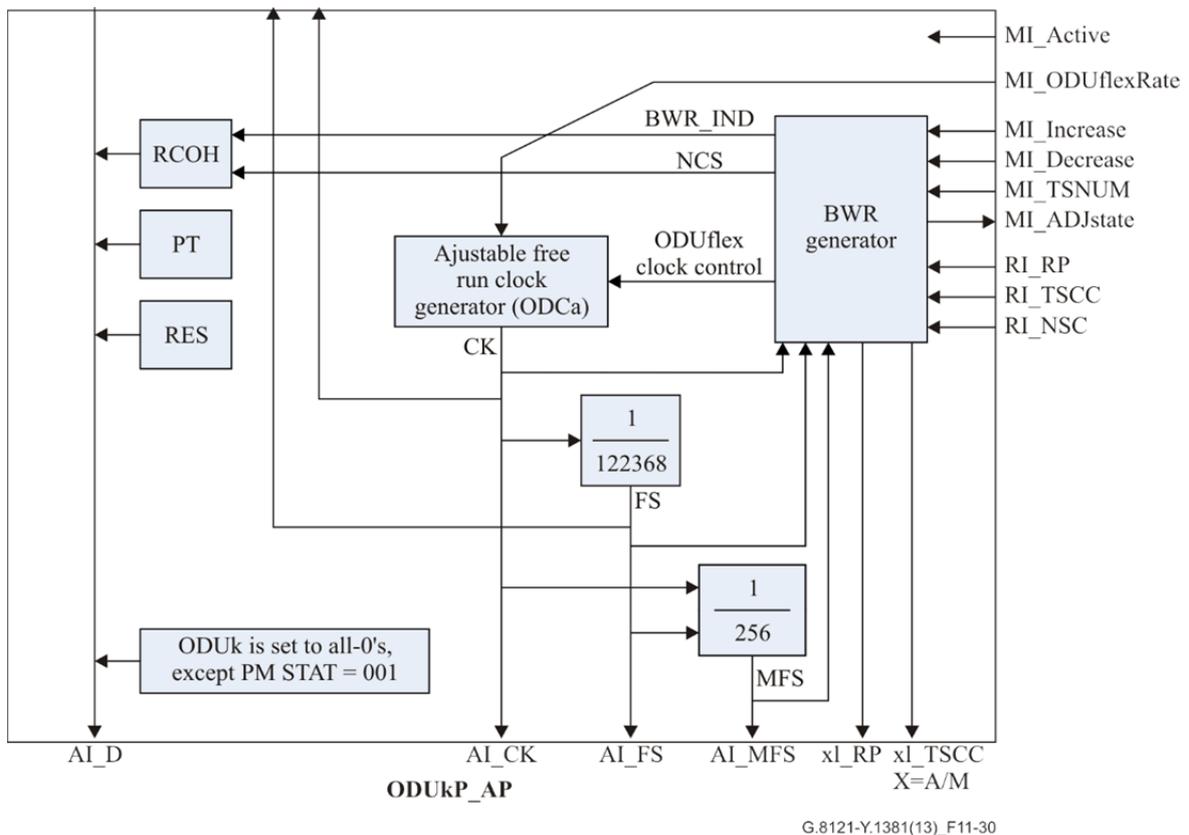
**Table 11-13 – ODUkP-h/MT\_A\_So interfaces**

Inputs	Outputs
ODUkP-h/MT_A_So_MI_DECREASE ODUkP-h/MT_A_So_MI_TSNUM ODUkP-h/MT_A_So_MI_ODUflexRate	
NOTE – (A/M)I_XXX indicates that the XXX signal may either be an AI_XXX or an MI_XXX signal.	

**Processes:**

A process diagram of this function is shown as Figure 11-18 in clause 11.2.1.1. An ODU-specific process for HAO-capable adaptation functions is described below. Other processes are described in clause 11.2.1.1.

– *ODUk-specific source process:*



**Figure 11-30 – ODUkP (k=flex) specific source processes**

**Clock and (multi)frame start signal generation:** The function shall generate a local ODUk clock with a clock rate within the minimum to maximum clock rate of the ODUflex signal as given in Table 14-2 of [ITU-T G.798]. The jitter and wander requirements defined in Annex A of [ITU-T G.8251] (ODCa clock) apply.

The function shall generate the (multi)frame start reference signals AI\_FS and AI\_MFS for the ODUk signal. The AI\_FS signal shall be active once per 122368 clock cycles. AI\_MFS shall be active once every 256 frames.

**PT:** The payload type information is derived directly from the adaptation function type. The value for "GFP mapping" shall be inserted into the PT byte position of the PSI overhead, as defined in

clause 15.9.2.1.1 of [ITU-T G.709]. The PT value of a HAO-capable adaptation function is the same as a non-HAO-capable one.

**RES:** The function shall insert all-0's into the RES bytes.

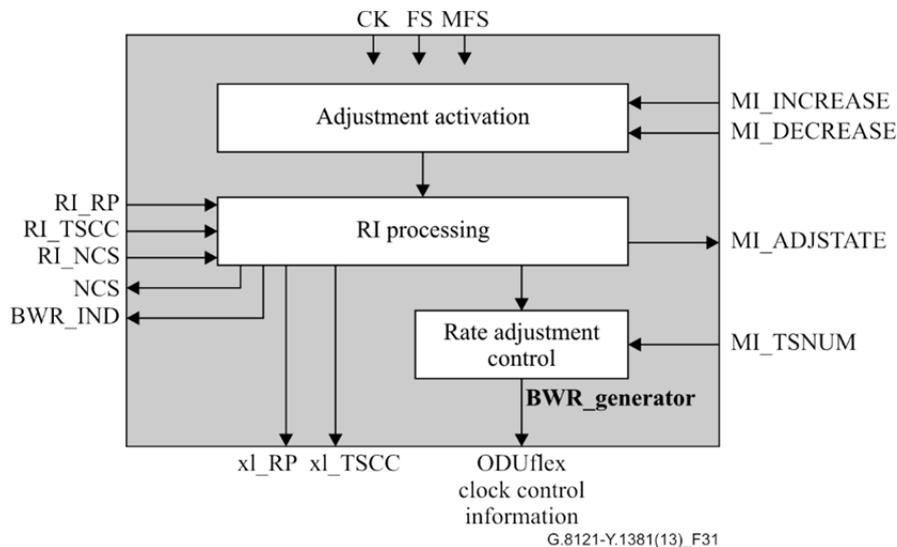
All other bits of the ODUk overhead should be sourced as "0"s, except the ODUk-PM STAT field which should be set to the value "normal path signal" (001).

**RCOH generator:** This process inserts the NCS generated by the HAO process into the NCS field of the RCOH in OPUflex.

**BWR\_Generator:** This process is used for BWR protocol adjustment processing and generation of the BWR protocol overhead. It contains the following processes as shown in Figure 11-31.

*Adjustment activation:* When MI\_INCREASE or MI\_DECREASE is true, the BWR protocol is activated and RI processing is started.

*Rate adjustment control:* Generates the ODUflex clock control signal. The original ODUflex clock rate will gradually change to a new ODUflex clock rate so that no GMP buffer overflow or underflow will occur in the ODUflex network connection. Refer to [ITU-T G.7044].



**Figure 11-31 – BWR\_Generator process**

*RI processing:* This process performs the BWR protocol according to RI\_RP, RI\_TSCC, RI\_NCS signals received from the BWR\_Receiver process.

- When RI processing is activated, xI\_RP and xI\_TSCC (x is A or M) signals are set to one (1).
- The value of the NCS signal is set to ACK(1) when receiving RI\_RP=1 and the value of RI\_TSCC is changed from 0 to 1.
- Rate adjustment control is activated when receiving RI\_RP=1 and RI\_TSCC=1 and RI\_NCS=ACK(1).
- BWR\_IND is set to "1" x μs before the ODUflex signal's bit rate adjustment starts, and it is set to "0" y μs before the ODUflex signal's bit rate adjustment completes. x is almost equal to y and shall be in the range of 125 to 250 μs.
- The value of xI\_TSCC signal is set to 0 when rate adjustment is completed.
- The value of NCS signal is set to NACK(0) when receiving RI\_RP=1 and the value of RI\_TSCC is changed from 1 to 0.

- The value of RP signal is set to 0 when receiving RI\_NCS=NACK(0) and sending NCS=NACK(0).
- The completion of the resize process is reported to the NMS when receiving RI\_RP=0.

**Defects:** None.

**Consequent actions:** None.

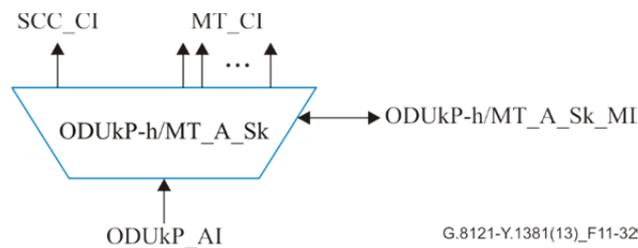
**Defect correlations:** None.

**Performance monitoring:** For further study.

### 11.2.3.2 HAO-capable ODUk to MPLS-TP adaptation sink function (ODUkP-h/MT\_A\_Sk)

The ODUkP-h/MT\_A\_Sk extracts MT\_CI information from the ODUkP payload area. It extracts the OPUk overhead (PT, RCOH and RES) and monitors the reception of the correct payload type.

**Symbol:**



**Figure 11-32 – ODUkP-h/MT\_A\_Sk symbol**

**Interfaces:**

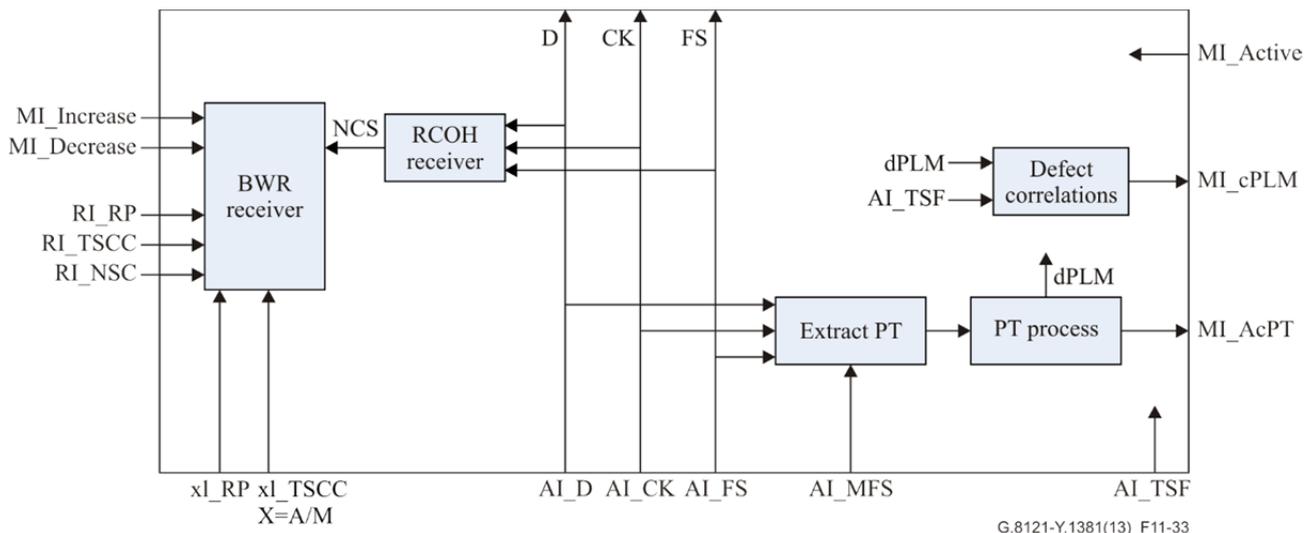
**Table 11-14 – ODUkP-h/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>ODUkP_AP:</b>            ODUkP_AI_Data            ODUkP_AI_Clock            ODUkP_AI_FrameStart            ODUkP_AI_MultiFrameStart            ODUkP_AI_TSF            ODUkP_(A/M)I_RP            ODUkP_(A/M)I_TSCC</p> <p><b>ODUkP-h/MT_A_Sk_MP:</b>            ODUkP-h/MT_A_Sk_MI_Active            ODUkP-h/MT_A_Sk_MI_SCCType            ODUkP-h/MT_A_Sk_MI_Label[1...M]            ODUkP-h/MT_A_Sk_MI_LSPTYPE[1...M]            ODUkP-h/MT_A_Sk_MI_CoS[1...M]            ODUkP-h/MT_A_Sk_MI_TC2PHBMapping[1...M]            ODUkP-h/MT_A_Sk_MI_QoSDecodingMode[1...M]            ODUkP/MT_A_Sk_MI_Mode[1...M]</p> <p>ODUkP-h/MT_A_Sk_MI_LCK_Period[1...M]            ODUkP-h/MT_A_Sk_MI_LCK_CoS[1...M]            ODUkP-h/MT_A_Sk_MI_LCK_Tool[1...M]            ODUkP-h/MT_A_Sk_MI_AIS_Tool[1...M]</p> <p>ODUkP-h/MT_A_Sk_MI_Admin_State            ODUkP-h/MT_A_Sk_MI_AIS_Period[1...M]            ODUkP-h/MT_A_Sk_MI_AIS_CoS[1...M]            ODUkP-h/MT_A_Sk_MI_LCK_Tool[1...M]            ODUkP-h/MT_A_Sk_MI_AIS_Tool[1...M]            ODUkP-h/MT_A_Sk_MI_GAL_Enable[1...M]</p> <p>ODUkP-h/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p> <p>ODUkP-h/MT_A_Sk_MI_INCREASE            ODUkP-h/MT_A_Sk_MI_DECREASE</p>	<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB            MT_CI_SSF            MT_CI_LStack</p> <p><b>SCC_CP:</b>            SCC_CI_Data            SCC_CI_SSF</p> <p><b>ODUkP_RP:</b>            ODUkP_RI_RP            ODUkP_RI_TSCC            ODUkP_RI_NCS</p> <p><b>ODUkP-h/MT_A_Sk_MP:</b>            ODUkP-h/MT_A_Sk_MI_AcPT            ODUkP-h/MT_A_Sk_MI_AcEXI            ODUkP-h/MT_A_Sk_MI_LastValidUPI            ODUkP-h/MT_A_Sk_MI_cPLM            ODUkP-h/MT_A_Sk_MI_cLFD            ODUkP-h/MT_A_Sk_MI_cEXM            ODUkP-h/MT_A_Sk_MI_cUPM</p>

**Processes:**

A process diagram of this function is shown as Figure 11-21 in clause 11.2.1.2. An ODU-specific process for HAO-capable adaptation functions is described below. Other processes are described in clause 11.2.1.2.

– ODUk-specific sink process:



**Figure 11-33 – ODUkP (k=flex) specific sink processes**

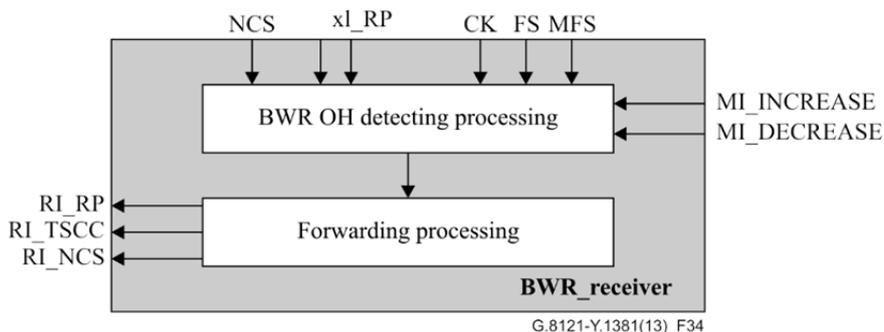
**PT:** The function shall extract the PT byte from the PSI overhead as defined in clause 8.7.1 of [ITU-T G.798]. The payload type value for "GFP mapping" in clause 15.9.2.1.1 of [ITU-T G.709] shall be expected. The accepted PT value is available at the MP (MI\_AcPT) and is used for PLM defect detection. The PT value of a HAO-capable adaptation function is the same as a non-HAO-capable one.

**RES:** The value in the RES bytes shall be ignored.

**RCOH receiver:** This process extracts the NCS from the RCOH overhead area, and then forwards it to BWR\_Receiver.

**BWR\_Receiver:** This process extracts and detects the BWR protocol overhead, with the exception of the BWR\_IND signal. It is shown in Figure 11-34.

When MI\_INCREASE or MI\_DECREASE is true, the BWR protocol is activated and starts to receive AI\_RP/MI\_RP, AI\_TSCC/MI\_TSCC from the BWR\_RELAY\_Receiver process and NCS from the extract NCS process. Then the detected value of RP, TSCC and NCS are sent to the BWR generator.



**Figure 11-34 – BWR\_Receiver process**

**Defects:**

dPLM – See clause 6.2.4.1 of [ITU-T G.798].

dLFD – See clause 6.2.5.2 of [ITU-T G.806].

dEXM – See clause 6.2.4.4 of [ITU-T G.806].

dUPM – See clause 8.4.2.

**Consequent actions:** The function shall perform the following consequent actions:

aSSF ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

aAIS ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

**Defect correlations:** The function shall perform the following defect correlations to determine the most probable fault cause (see clause 6.4 of [ITU-T G.806]). This fault cause shall be reported to the EMF.

cPLM ← dPLM and (not AI\_TSF)

cLFD ← dLFD and (not dPLM) and (not AI\_TSF)

cEXM ← dEXM and (not dPLM) and (not dLFD) and (not AI\_TSF)

cUPM ← dUPM and (not dEXM) and (not dPLM) and (not dLFD) and (not AI\_TSF)

**Performance monitoring:** For further study.

### 11.3 PDH to MPLS-TP adaptation function (P/MT\_A)

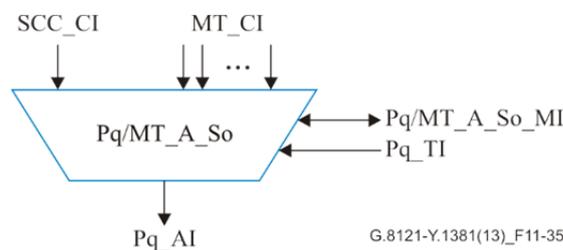
#### 11.3.1 P<sub>q</sub> to MPLS-TP adaptation functions (P<sub>q</sub>/MT\_A; q = 11s, 12s, 31s, 32e)

##### 11.3.1.1 P<sub>q</sub> to MPLS-TP adaptation source function (P<sub>q</sub>/MT\_A\_So)

This function maps MT\_CI information onto a P<sub>q</sub>\_AI signal (q = 11s, 12s, 31s, 32e).

Data at the P<sub>q</sub>\_AP is a P<sub>q</sub> (q = 11s, 12s, 31s, 32e), with a payload as described in [ITU-T G.7043] with a value of N=1. The VLI byte is reserved and not used for payload data.

#### Symbol



**Figure 11-35 – P<sub>q</sub>/MT\_A\_So symbol**

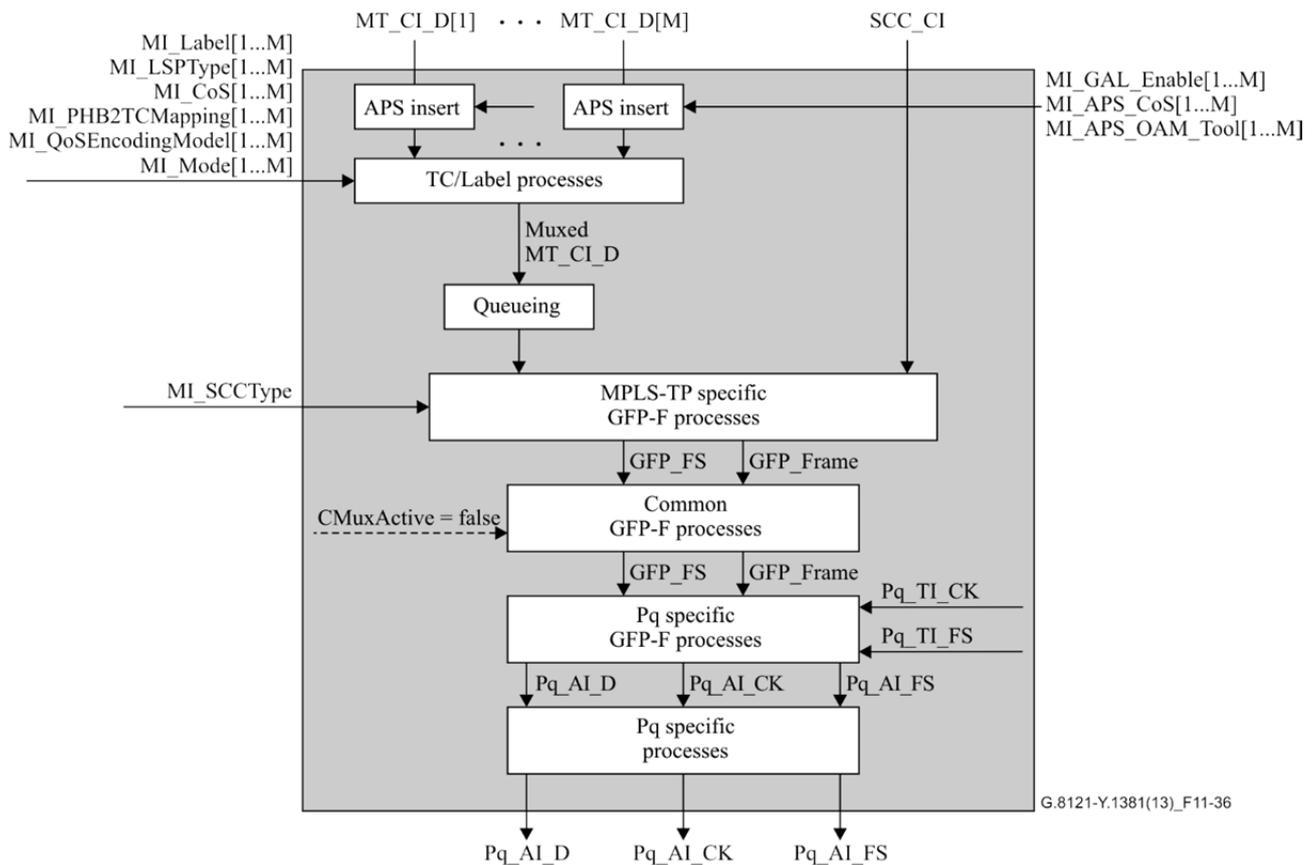
## Interfaces

**Table 11-15 – Pq/MT\_A\_So interfaces**

Inputs	Outputs
<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB</p> <p><b>SCC_CP:</b>            SCC_CI_Data</p> <p><b>Pq_TP:</b>            Pq_TI_Clock            Pq_TI_FrameStart</p> <p><b>Pq/MT_A_So_MP:</b>            Pq/MT_A_So_MI_SCCType            Pq/MT_A_So_MI_Label[1...M]            Pq/MT_A_So_MI_LSPTType[1...M]            Pq/MT_A_So_MI_CoS[1...M]            Pq/MT_A_So_PHB2TCMapping[1...M]            Pq/MT_A_So_MI_QoSEncodingMode[1...M]            Pq/MT_A_So_MI_Mode[1...M]</p> <p>Pq/MT_A_So_MI_GAL_Enable[1...M]            Pq/MT_A_So_MI_APS_OAM_CoS[1...M]            Pq/MT_A_So_MI_APS_OAM_Tool[1...M]</p>	<p><b>Pq_AP:</b>            Pq_AI_Data            Pq_AI_Clock            Pq_AI_FrameStart</p>

## Processes

A process diagram of this function is shown in Figure 11-36.



**Figure 11-36 – Pq/MT\_A\_So process diagram**

*TC/Label processes:*

See clause 8.2.1.

*Queueing process:*

See clause 8.3.

*MPLS-TP-specific GFP-F source process:*

See clause 8.4.1.

*Common GFP source process:*

See clause 8.5.3.1 of [ITU-T G.806]. GFP channel multiplexing is not supported (CMuxActive=false).

*P-specific GFP source process:*

See clause 8.5.2.1 of [ITU-T G.806]. The GFP frames are mapped into the Pq payload area according to [ITU-T G.8040].

*Pq-specific source process:*

NOTE – The VLI byte is fixed stuff equal to 0x00 at the Pq\_AP output of this function.

**P3Is specific:**

**MA:** Signal label information is derived directly from the adaptation function type. The value for "GFP mapping" in clause 2.1 of [ITU-T G.832] is placed in the Payload Type field of the MA byte.

**Defects:** None.

**Consequent actions:** None.

**Defect correlations:** None.

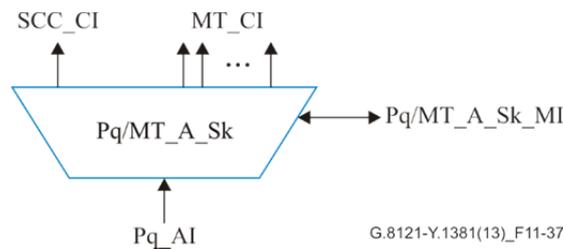
**Performance monitoring:** For further study.

### 11.3.1.2 Pq to MPLS-TP adaptation sink function (Pq/MT\_A\_Sk)

This function extracts MT\_CI information from the Pq\_AI signal (q = 11s, 12s, 31s, 32e), delivering MT\_CI.

Data at the Pq\_AP is a Pq (q = 11s, 12s, 31s, 32e), with a payload as described in [ITU-T G.7043] with a value of N=1. The VLI byte is reserved and not used for payload data.

#### Symbol



**Figure 11-37 – Pq/MT\_A\_Sk symbol**

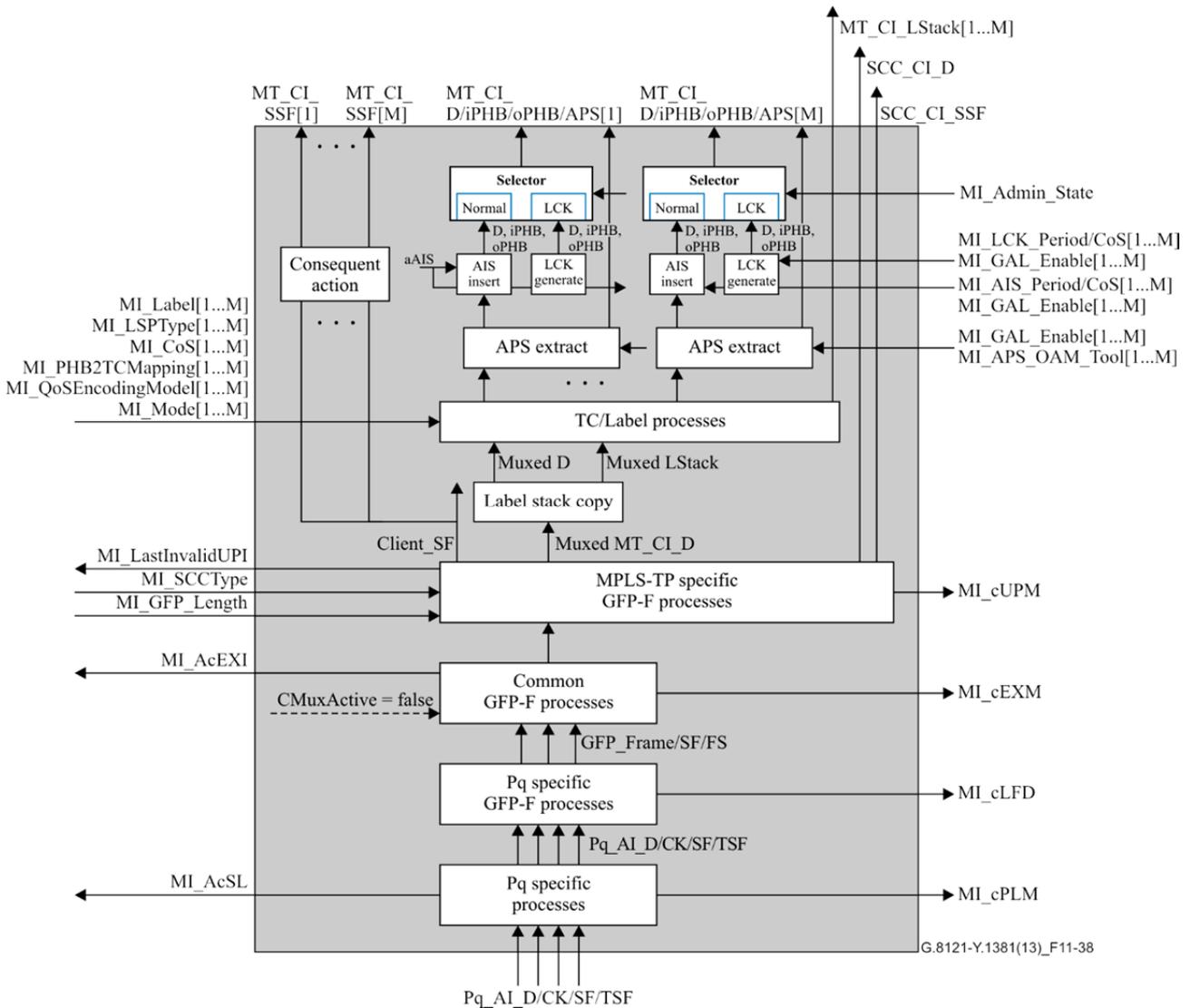
#### Interfaces

**Table 11-16 – Pq/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>Pq_AP:</b>  Pq_AI_Data  Pq_AI_Clock  Pq_AI_FrameStart  Pq_AI_TSF</p> <p><b>Pq/MT_A_Sk_MP:</b>  Pq/MT_A_Sk_MI_SCCType  Pq/MT_A_Sk_MI_Label[1...M]  Pq/MT_A_Sk_MI_LSPTType[1...M]  Pq/MT_A_Sk_MI_CoS[1...M]  Pq/MT_A_Sk_MI_TC2PHBMapping[1...M]  Pq/MT_A_Sk_MI_QoSDecodingMode[1...M]  Pq/MT_A_Sk_MI_Mode[1...M]</p> <p>Pq/MT_A_Sk_MI_LCK_Period[1...M]  Pq/MT_A_Sk_MI_LCK_CoS[1...M]  Pq/MT_A_Sk_MI_Admin_State  Pq/MT_A_Sk_MI_AIS_Period[1...M]  Pq/MT_A_Sk_MI_AIS_CoS[1...M]  Pq/MT_A_Sk_MI_GAL_Enable [1...M]  Pq/MT_A_Sk_MI_LCK_Tool[1...M]  Pq/MT_A_Sk_MI_AIS_Tool[1...M]  Pq/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p>	<p><b>Each MT_CP:</b>  MT_CI_Data  MT_CI_iPHB  MT_CI_oPHB  MT_CI_SSF  MT_CI_LStack</p> <p><b>SCC_CP:</b>  SCC_CI_Data  SCC_CI_SSF</p> <p><b>Pq/MT_A_Sk_MP:</b>  Pq/MT_A_Sk_MI_AcSL  Pq/MT_A_Sk_MI_AcEXI  Pq/MT_A_Sk_MI_LastValidUPI  Pq/MT_A_Sk_MI_cPLM  Pq/MT_A_Sk_MI_cLFD  Pq/MT_A_Sk_MI_cEXM  Pq/MT_A_Sk_MI_cUPM</p>

## Processes

A process diagram of this function is shown in Figure 11-38.



**Figure 11-38 – Pq/MT\_A\_Sk process diagram**

– *Selector generation process:*

See clause 8.6.1. The normal CI is blocked if Admin\_State = LOCKED.

– *AIS insert process:*

See clause 8.6.2. There is a single AIS insert process for each MT.

– *LCK generation process:*

See clause 8.6.3. There is a single LCK insert process for each MT.

*TC/Label processes:*

See clause 8.2.2.

– *Label stack copy process:*

See clause 8.2.3.

*MPLS-TP-specific GFP-F sink process:*

See clause 8.4.2.

*Common GFP sink process:*

See clause 8.5.3.1 of [ITU-T G.806]. GFP channel multiplexing is not supported (CMuxActive=false).

*Pq-specific GFP sink process:*

See clause 8.5.2.1 of [ITU-T G.806]. The GFP frames are de-mapped from the Pq payload area according to [ITU-T G.8040].

*Pq-specific sink process:*

NOTE – The VLI byte at the Pq\_AP input of this function is ignored.

**P31s specific:**

**MA:** The signal label is recovered from the Payload Type field in the MA byte as per clause 6.2.4.2 of [ITU-T G.806]. The signal label for "GFP mapping" in clause 2.1 of [ITU-T G.832] shall be expected. The accepted value of the signal label is also available at the P31s/ETH\_A\_Sk\_MP.

**Defects:**

dPLM – See clause 6.2.4.2 of [ITU-T G.806].

dLFD – See clause 6.2.5.2 of [ITU-T G.806].

dUPM – See clause 8.4.2.

dEXM – See clause 6.2.4.4 of [ITU-T G.806].

NOTE – dPLM is only defined for q = 31s. dPLM is assumed to be false for q = 11s, 12s, 32e.

**Consequent actions:** The function shall perform the following consequent actions:

aSSF ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

aAIS ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

**Defect correlations:** The function shall perform the following defect correlations to determine the most probable fault cause (see clause 6.4 of [ITU-T G.806]). This fault cause shall be reported to the EMF.

cPLM ← dPLM and (not AI\_TSF)

cLFD ← dLFD and (not dPLM) and (not AI\_TSF)

cEXM ← dEXM and (not dPLM) and (not dLFD) and (not AI\_TSF)

cUPM ← dUPM and (not dEXM) and (not dPLM) and (not dLFD) and (not AI\_TSF)

**Performance monitoring:** For further study.

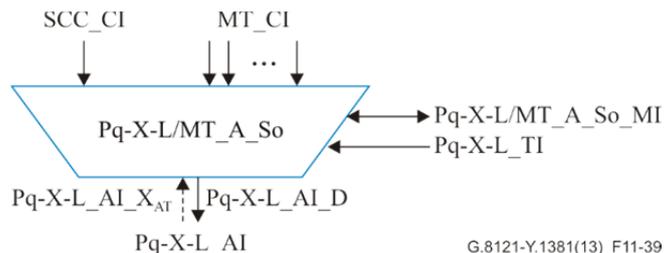
### **11.3.2 LCAS-capable Pq to MPLS-TP adaptation functions (Pq-X-L/MT\_A; q=11s, 12s, 31s, 32e)**

#### **11.3.2.1 LCAS-capable Pq to MPLS-TP adaptation source function (Pq-X-L/MT\_A\_So)**

This function maps MT\_CI information onto a Pq-X-L\_AI signal (q=11s, 12s, 31s, 32e).

Data at the Pq-X-L\_AP is a Pq-X (q = 11s, 12s, 31s, 32e), with a payload as described in [ITU-T G.7043].

## Symbol



**Figure 11-39 – Pq-X-L/MT\_A\_So symbol**

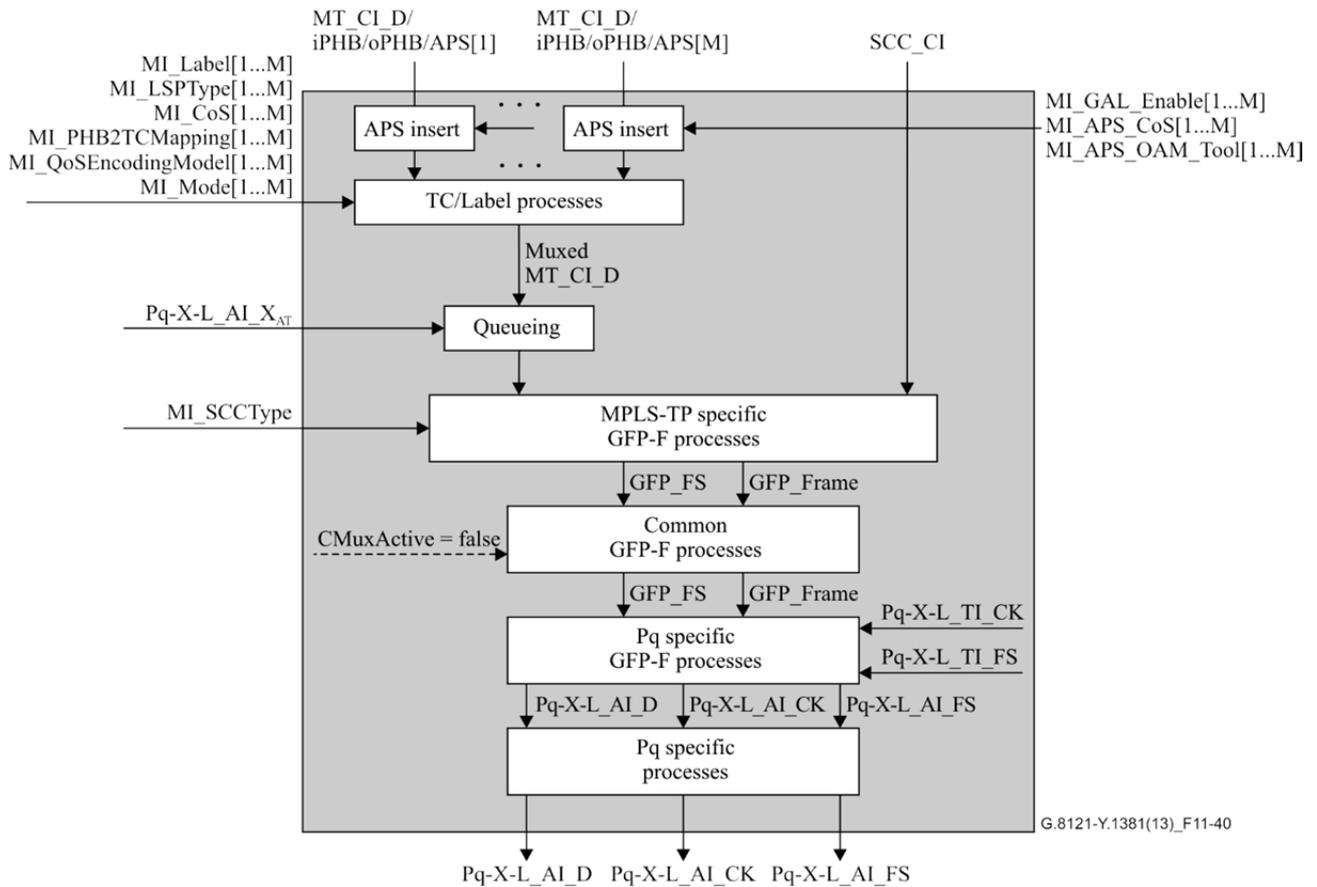
## Interfaces

**Table 11-17 – Pq-X-L/MT\_A\_So interfaces**

Inputs	Outputs
<p><b>Each MT_CP:</b>            MT_CI_Data            MT_CI_iPHB            MT_CI_oPHB</p> <p><b>SCC_CP:</b>            SCC_CI_Data</p> <p><b>Pq-X-L_AP:</b>            Pq-X-L_AI_XAT</p> <p><b>Pq-X-L_TP:</b>            Pq-X-L_TI_Clock            Pq-X-L_TI_FrameStart</p> <p><b>Pq-X-L/MT_A_So_MP:</b>            Pq-X-L/MT_A_So_MI_SCCType            Pq-X-L/MT_A_So_MI_Label[1...M]            Pq-X-L/MT_A_So_MI_LSPTType[1...M]            Pq-X-L/MT_A_So_MI_CoS[1...M]            Pq-X-L/MT_A_So_PHB2TCMapping[1...M]            Pq-X-L/MT_A_So_MI_QoSEncodingMode[1...M]            Pq-X-L/MT_A_So_MI_Mode[1...M]</p> <p>Pq-X-L/MT_A_So_MI_GAL_Enable[1...M]            Pq-X-L/MT_A_So_MI_APS_OAM_CoS[1...M]            Pq-X-L/MT_A_So_MI_APS_OAM_Tool[1...M]</p>	<p><b>Pq-X-L_AP:</b>            Pq-X-L_AI_Data            Pq-X-L_AI_Clock            Pq-X-L_AI_FrameStart</p>

## Processes

A process diagram of this function is shown in Figure 11-40.



**Figure 11-40 – Pq-X-L/MT\_A\_So process diagram**

The processes have the same definition as in clause 11.1.1.1.

**Defects: None.**

**Consequent actions: None.**

**Defect correlations: None.**

**Performance monitoring**

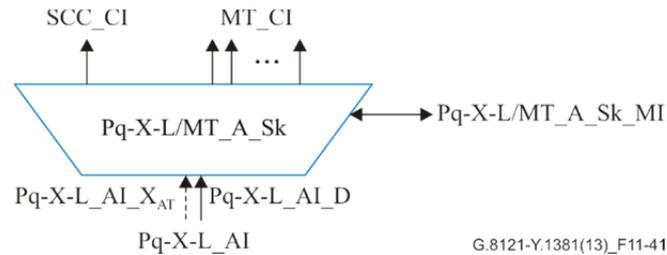
For further study.

### 11.3.2.2 LCAS-capable Pq to MPLS-TP adaptation sink function (Pq-X-L/MT\_A\_Sk)

This function extracts MT\_CI information from the Pq-X-L\_AI signal (q = 11s, 12s, 31s, 32e), delivering MT\_CI.

Data at the Pq-X-L\_AP is a Pq-Xv (q = 11s, 12s, 31s, 32e), with a payload as described in [ITU-T G.7043].

## Symbol



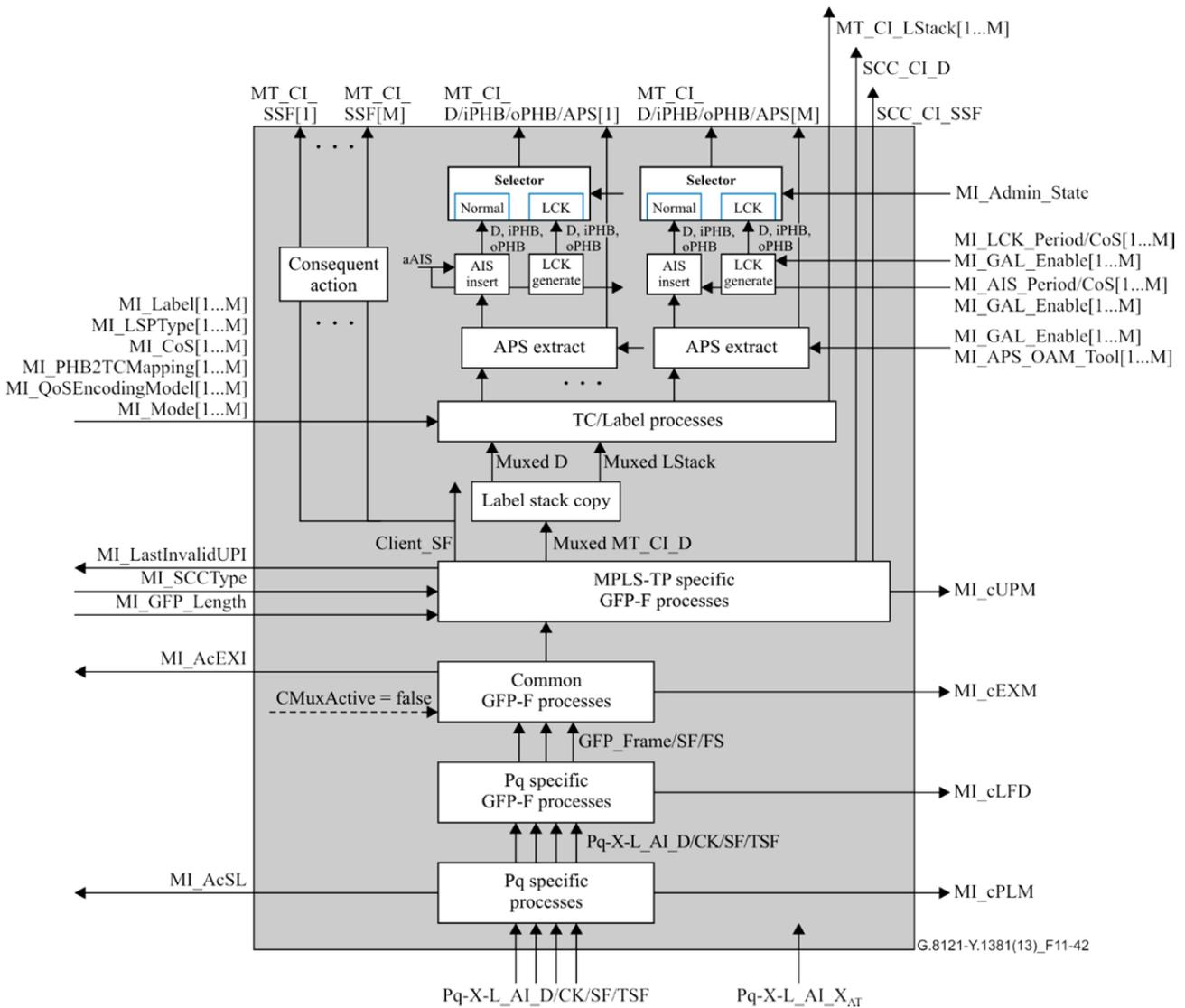
**Figure 11-41 – Pq-X-L/MT\_A\_Sk symbol**

## Interfaces

**Table 11-18 – Pq-X-L/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>Pq-X-L_AP:</b>  Pq-X-L_AI_Data  Pq-X-L_AI_ClocK  Pq-X-L_AI_FrameStart  Pq-X-L_AI_TSF  Pq-X-L_AI_XAR</p> <p><b>Pq-X-L/MT_A_Sk_MP:</b>  Pq-X-L/MT_A_Sk_MI_SCCType  Pq-X-L/MT_A_Sk_MI_Label[1...M]  Pq-X-L/MT_A_Sk_MI_LSPTType[1...M]  Pq-X-L/MT_A_Sk_MI_CoS[1...M]  Pq-X-L/MT_A_Sk_MI_TC2PHBMapping[1...M]  Pq-X-L/MT_A_Sk_MI_QoSDecodingMode[1...M]  Pq-X-L/MT_A_Sk_MI_Mode[1...M]</p> <p>Pq-X-L//MT_A_Sk_MI_LCK_Period[1...M]  Pq-X-L//MT_A_Sk_MI_LCK_P[1...M]  Pq-X-L//MT_A_Sk_MI_Admin_State  Pq-X-L//MT_A_Sk_MI_AIS_Period[1...M]  Pq-X-L//MT_A_Sk_MI_AIS_P[1...M]</p> <p>Pq-X-L//MT_A_Sk_MI_GAL_Enable[1...M]  Pq-X-L/MT_A_Sk_MI_LCK_Tool[1...M]  Pq-X-L/MT_A_Sk_MI_AIS_Tool[1...M]</p> <p>Pq-X-L/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p>	<p><b>Each MT_CP:</b>  MT_CI_Data  MT_CI_iPHB  MT_CI_oPHB  MT_CI_SSF  MT_CI_LStack</p> <p><b>SCC_CP:</b>  SCC_CI_Data  SCC_CI_SSF</p> <p><b>Pq-X-L/MT_A_Sk_MP:</b>  Pq-X-L/MT_A_Sk_MI_AcSL  Pq-X-L/MT_A_Sk_MI_AcEXI  Pq-X-L/MT_A_Sk_MI_LastValidUPI  Pq-X-L/MT_A_Sk_MI_cPLM  Pq-X-L/MT_A_Sk_MI_cLFD  Pq-X-L/MT_A_Sk_MI_cEXM  Pq-X-L/MT_A_Sk_MI_cUPM</p>

## Processes



**Figure 11-42 – Pq-X-L/MT\_A\_Sk process diagram**

See process diagram and process description in clause 11.1.1.2. The additional Pq-X-L\_AI\_X<sub>AR</sub> interface is not connected to any of the internal processes.

### Defects:

dPLM – See clause 6.2.4.2 of [ITU-T G.806].

dLFD – See clause 6.2.5.2 of [ITU-T G.806].

dUPM – See clause 8.4.2.

dEXM – See clause 6.2.4.4 of [ITU-T G.806].

NOTE – dPLM is only defined for q = 31s. dPLM is assumed to be false for q = 11s, 12s, 32e.

**Consequent actions:** The function shall perform the following consequent actions:

aSSF ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

aAIS ← AI\_TSF or dPLM or dLFD or dUPM or dEXM

**Defect correlations:** The function shall perform the following defect correlations to determine the most probable fault cause (see clause 6.4 of [ITU-T G.806]). This fault cause shall be reported to the EMF.

cPLM ← dPLM and (not AI\_TSF)

cLFD ← dLFD and (not dPLM) and (not AI\_TSF)

cEXM ← dEXM and (not dPLM) and (not dLFD) and (not AI\_TSF)

cUPM ← dUPM and (not dEXM) and (not dPLM) and (not dLFD) and (not AI\_TSF)

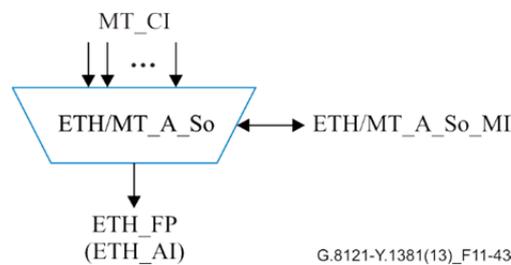
**Performance monitoring:** For further study.

## 11.4 Ethernet to MPLS-TP adaptation function

### 11.4.1 ETH to MPLS-TP adaptation function (ETH/MT\_A)

#### 11.4.1.1 ETH to MPLS-TP adaptation function (ETH/MT\_A)

##### Symbol



**Figure 11-43 – ETH/MT\_A\_So symbol**

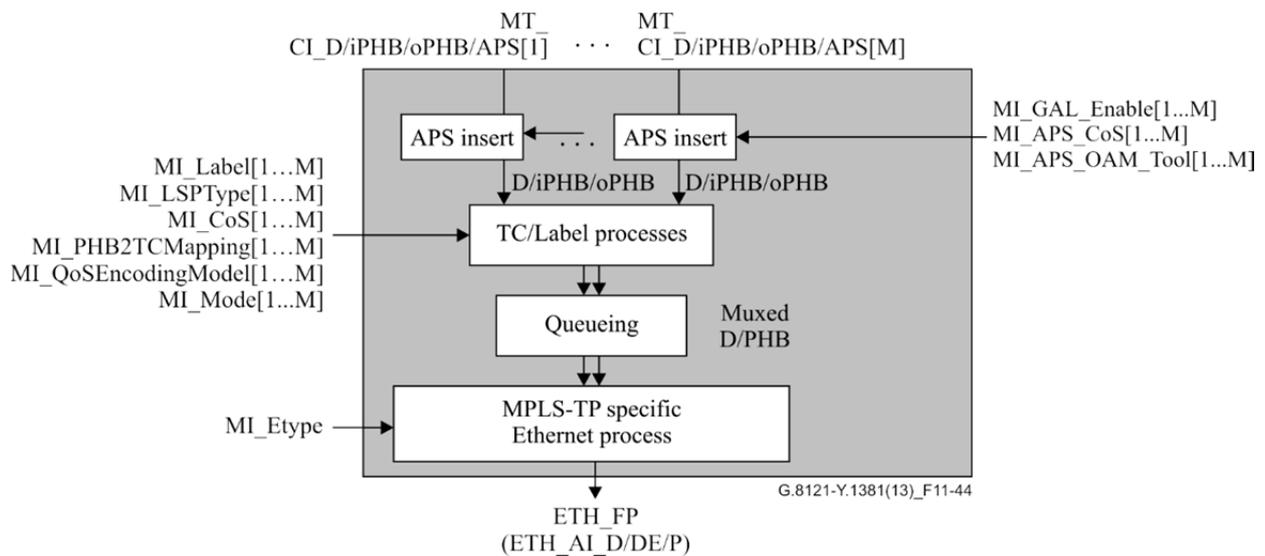
##### Interfaces

**Table 11-19 – ETH/MT\_A\_So interfaces**

Inputs	Outputs
<p><b>Each MT_CP:</b>            MT_CI_Data[1...M]            MT_CI_iPHB[1...M]            MT_CI_oPHB[1...M]</p> <p><b>ETH/MT_A_So_MP:</b>            ETH/MT_A_So_MI_Label[1...M]            ETH/MT_A_So_MI_LSPTYPE[1...M]            ETH/MT_A_So_MI_CoS[1...M]            ETH/MT_A_So_PHB2TCMapping[1...M]            ETH/MT_A_So_MI_QoSEncodingMode[1...M]            ETH/MT_A_So_MI_Mode[1...M]            ETH/MT_A_So_MI_Etype</p> <p>ETH/MT_A_So_MI_GAL_Enable[1...M]            ETH/MT_A_So_MI_APS_OAM_CoS[1...M]            ETH/MT_A_So_MI_APS_OAM_Tool[1...M]</p>	<p><b>ETYn_AP:</b>            ETH_AI_Data            ETH_AI_P            ETH_AI_DE</p>

##### Processes

A process diagram of this function is shown in Figure 11-44.



**Figure 11-44 – ETH/MT\_A\_So process**

– *TC/Label processing*

See clause 8.2.1.

– *Queueing process:*

See clause 8.3.

*MPLS-TP-specific Ethernet process:*

This process inserts the Ethertype for MPLS-TP packets according to [IETF RFC 5332].

**Defects:** None.

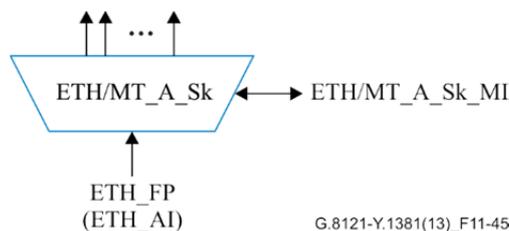
**Consequent actions:** None.

**Defect correlations:** None.

**Performance monitoring:** For further study.

**11.4.1.2 ETH to MPLS-TP adaptation function (ETY/MT\_A)**

**Symbol**



**Figure 11-45 – ETH/ETH\_A\_Sk symbol**

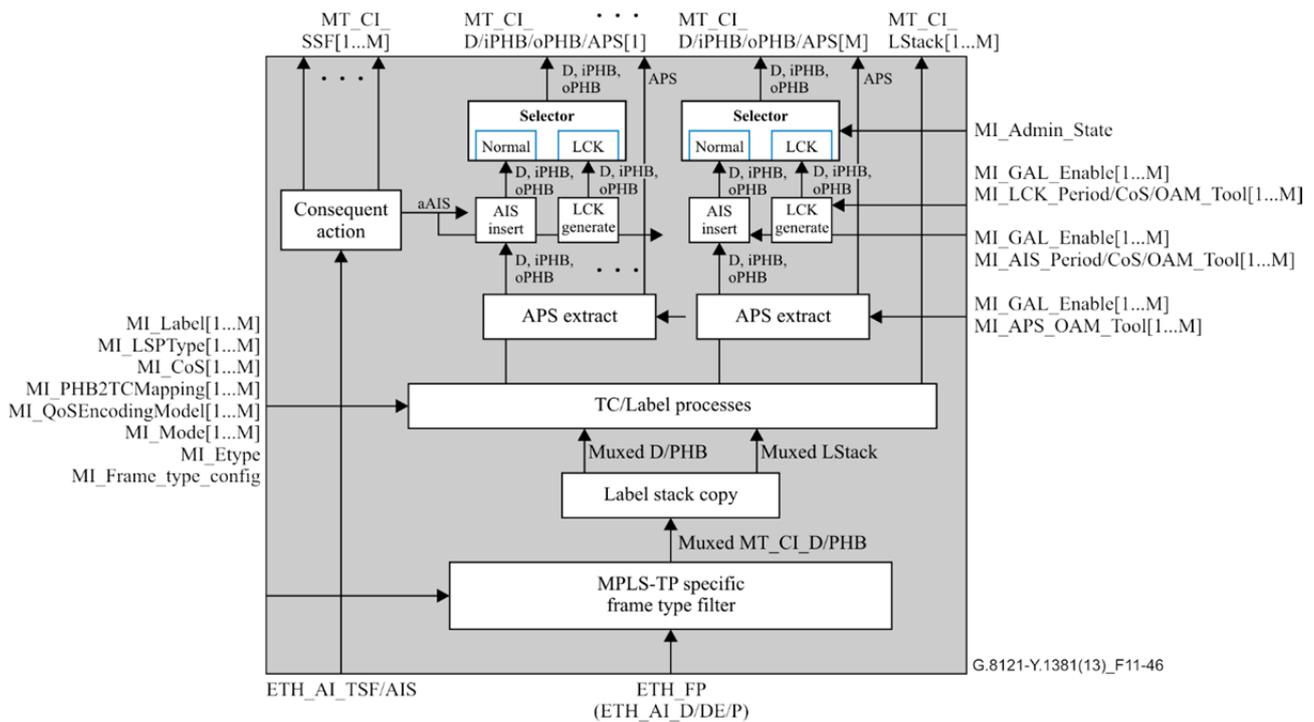
## Interfaces

**Table 11-20 – ETH/MT\_A\_Sk interfaces**

Inputs	Outputs
<p><b>ETH_AP:</b>            ETH_AI_Data            ETH_AI_P            ETH_AI_DE            ETH_AI_TSF            ETH_AI_AIS</p> <p><b>ETH/MT_A_Sk_MP:</b>            ETH/MT_A_Sk_MI_Etype            ETH/MT_A_Sk_MI_Frame_Type_Config</p> <p>ETH/MT_A_Sk_MI_Label[1...M]            ETH/MT_A_Sk_MI_LSPTType[1...M]            ETH/MT_A_Sk_MI_CoS[1...M]            ETH/MT_A_Sk_MI_TC2PHBMapping[1...M]            ETH/MT_A_Sk_MI_QoSDecodingMode[1...M]            ETH/MT_A_Sk_MI_Mode[1...M]</p> <p>ETH/MT_A_Sk_MI_GAL_Enable[1...M]            ETH/MT_A_Sk_MI_Admin_State</p> <p>ETH/MT_A_Sk_MI_LCK_Period[1...M]            ETH/MT_A_Sk_MI_LCK_CoS[1...M]</p> <p>ETH/MT_A_Sk_MI_AIS_Period[1...M]            ETH/MT_A_Sk_MI_AIS_CoS[1...M]            ETH/MT_A_Sk_MI_LCK_OAM_Tool[1...M]            ETH/MT_A_Sk_MI_AIS_OAM_Tool[1...M]            ETH/MT_A_Sk_MI_APS_OAM_Tool[1...M]</p>	<p><b>Each MT_CP:</b>            MT_CI_Data[1...M]            MT_CI_iPHB[1...M]            MT_CI_oPHB[1...M]            MI_CI_Lstack[1...M]</p>

## Processes

A process diagram of this function is shown in Figure 11-46.



**Figure 11-46 – ETYn/ETH\_A\_Sk process**

– *Selector generation process:*

See clause 8.6.1. The normal CI is blocked if Admin\_State = LOCKED.

– *AIS insert process:*

See clause 8.6.2. There is a single AIS insert process for each MT.

– *LCK generation process:*

See clause 8.6.3. There is a single LCK insert process for each MT.

– *TC/Label processes:*

See clause 8.2.2.

– *Label stack copy process:*

See clause 8.2.3.

– *MPLS-TP-specific filter process:*

This process is for the reception process of the Ethertype for MPLS-TP packets according to [IETF RFC 5332].

**Defects:** None.

**Consequent actions:** For further study.

**Defect correlations:** None.

**Performance monitoring:** For further study.

## Appendix I

### Examples of processing packets with expired TTL

(This appendix does not form an integral part of this Recommendation.)

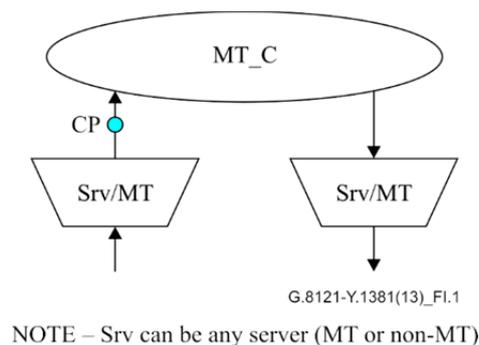
MPLS-TP packets received with an expired TTL shall not be forwarded. However, MPLS-TP OAM packets received with an expired TTL can be processed and their processing can occur at different locations (i.e., from different atomic functions) within MPLS-TP equipment.

The proper behaviour depends on the MPLS-TP connection configuration within the node. The following examples are considered and described:

- Intermediate node with no MIPs
- Intermediate node – interface MIPs
- Intermediate node – node MIP
- Terminating node – down MEP or node MEP
- Terminating node – up MEP (with interface MIP).

NOTE – As indicated in clause 9.4.2.2.2, the MI\_DS\_MP\_Type parameter should be properly configured by the EMF and not exposed to the operator as a configuration parameter of the NE management. The examples described in this appendix provide guidelines on how the EMF can properly configure the MI\_DS\_MP\_Type.

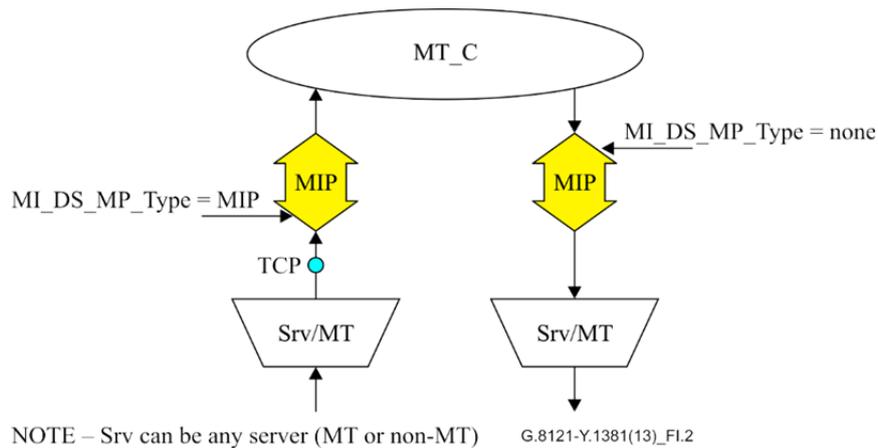
Figure I.1 describes the behaviour of an intermediate node with no MIPs using the atomic functions defined in this Recommendation:



**Figure I.1 – Intermediate node with no MIPs**

The Server/MT\_A\_Sk is connected to the MT\_C via an MT\_CP. Therefore the TTL decrement process, as defined in clause 8.2.2, will discard all the MPLS-TP packets (user data or OAM) that are received with an expired TTL.

Figure I.2 describes the behaviour of an intermediate node supporting per-interface MIPs using the atomic functions defined in this Recommendation:



**Figure I.2 – Intermediate node with per-interface MIPs**

The Server/MT\_A\_Sk is connected to the ingress MIP via an MT\_TCP. Therefore the TTL decrement process, as defined in clause 8.2.2, will forward all the MPLS-TP packets (user data or OAM) that are received with an expired TTL to the ingress MIP.

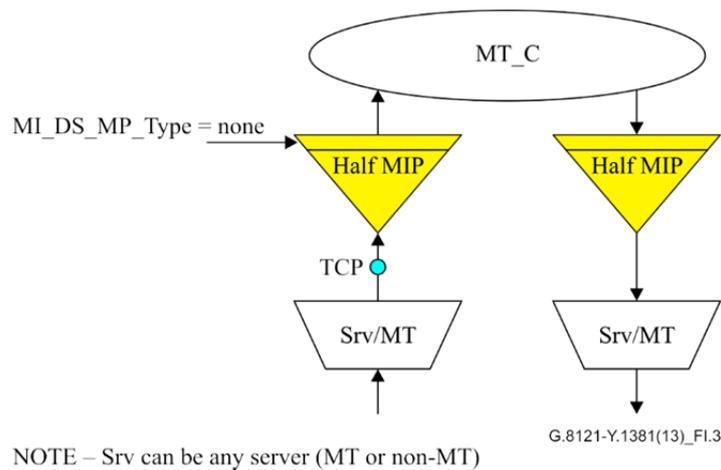
The MTDi\_TT\_Sk atomic function within the ingress MIP will process all the MPLS-TP OAM packets received with an expired TTL and which are targeted to the ingress MIP.

The TTL check process in the MTDi/MT\_A\_Sk within the ingress MIP, as defined in clause 9.4.2.2.2, is properly configured by the EMF (MI\_DS\_MP\_Type=MIP) to drop all the MPLS-TP user data packets received with an expired TTL and to forward all the MPLS-TP OAM packets received with an expired TTL together (i.e., with fate share) with all the MPLS-TP packets received with a non-expired TTL.

These packets are forwarded up to the egress MIP where the MTDi\_TT\_Sk atomic function will process all the MPLS-TP OAM packets received with an expired TTL and which are targeted to the egress MIP.

The TTL check process in the MTDi/MT\_A\_Sk within the egress MIP, as defined in clause 9.4.2.2.2, is properly configured by the EMF (MI\_DS\_MP\_Type=none) to drop all the MPLS-TP packets received with an expired TTL. Although MPLS-TP user data packets with an expired TTL will never arrive at this point, this check will ensure also that any MPLS-TP OAM packet with an expired TTL is not forwarded.

Figure I.3 describes the behaviour of an intermediate node with a per-node MIP using the atomic functions defined in this Recommendation. The per-node MIP is modelled as being composed by two half-MIPs on each side of the MT\_C:



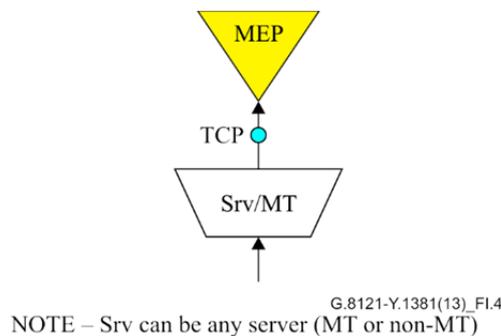
**Figure I.3 – Intermediate node with a per-node MIP**

The Server/MT\_A\_Sk is connected to the ingress MIP via an MT\_TCP. Therefore the TTL decrement process, as defined in clause 8.2.2, will forward all the MPLS-TP packets (user data or OAM) that are received with an expired TTL to the ingress half-MIP.

The MTDi\_TT\_Sk atomic function within the ingress half-MIP will process all the MPLS-TP OAM packets received with an expired TTL and which are targeted to the node MIP.

The TTL check process in the MTDi/MT\_A\_Sk, as defined in clause 9.4.2.2.2, is properly configured by the EMF (MI\_DS\_MP\_Type=none) to drop all the MPLS-TP packets (user data or OAM) that are received with an expired TTL.

Figure I.4 describes the behaviour of a terminating node with a down MEP or a per-node MEP using the atomic functions defined in this Recommendation. These two cases are modelled in the same way:

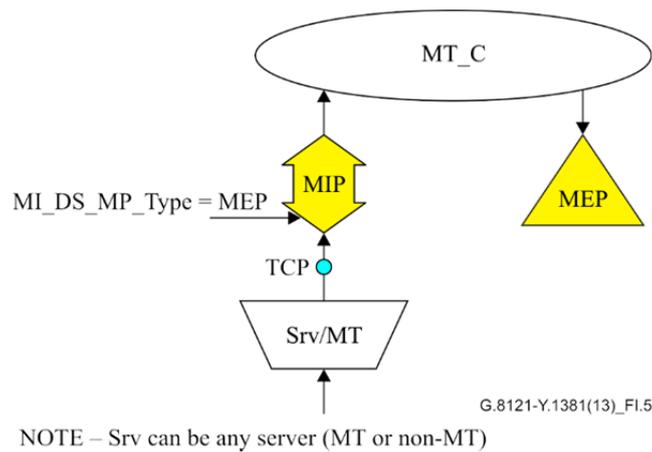


**Figure I.4 – Terminating node with a down MEP or node MEP**

The Server/MT\_A\_Sk is connected to an MEP via an MT\_TCP. Therefore the TTL decrement process, as defined in clause 8.2.2, will forward all the MPLS-TP packets (user data or OAM) that are received with an expired TTL to the MEP.

The MEP terminates the MPLS-TP trail and processes all the MPLS-TP packets it receives regardless of whether the TTL has expired or not.

Figure I.5 describes the behaviour of a terminating node with an up MEP, and therefore a per-interface ingress MIP, using the atomic functions defined in this Recommendation:



**Figure I.5 – Terminating node with an up MEP (and a per-interface MIP)**

The Server/MT\_A\_Sk is connected to the ingress MIP via an MT\_TCP. Therefore the TTL decrement process, as defined in clause 8.2.2, will forward all the MPLS-TP packets (user data or OAM) that are received with an expired TTL to the ingress MIP.

The MTDi\_TT\_Sk atomic function within the ingress MIP will process all the MPLS-TP OAM packets received with an expired TTL and which are targeted to the ingress MIP.

The TTL check process in the MTDi/MT\_A\_Sk within the ingress MIP, as defined in clause 9.4.2.2.2, is properly configured by the EMF (MI\_DS\_MP\_Type=MEP) to forward all the MPLS-TP packets (user data or OAM) that are received with an expired TTL together (i.e., with fate share) with all the MPLS-TP packets received with a non-expired TTL.

These packets are forwarded up to the up MEP that terminates the MPLS-TP trail and processes all the MPLS-TP packets it receives regardless of whether the TTL has expired or not.

## Appendix II

### Flow of PHB information through MEP and MIP

(This appendix does not form an integral part of this Recommendation.)

This Recommendation describes the various atomic functions that comprise MIPs and MEPs:

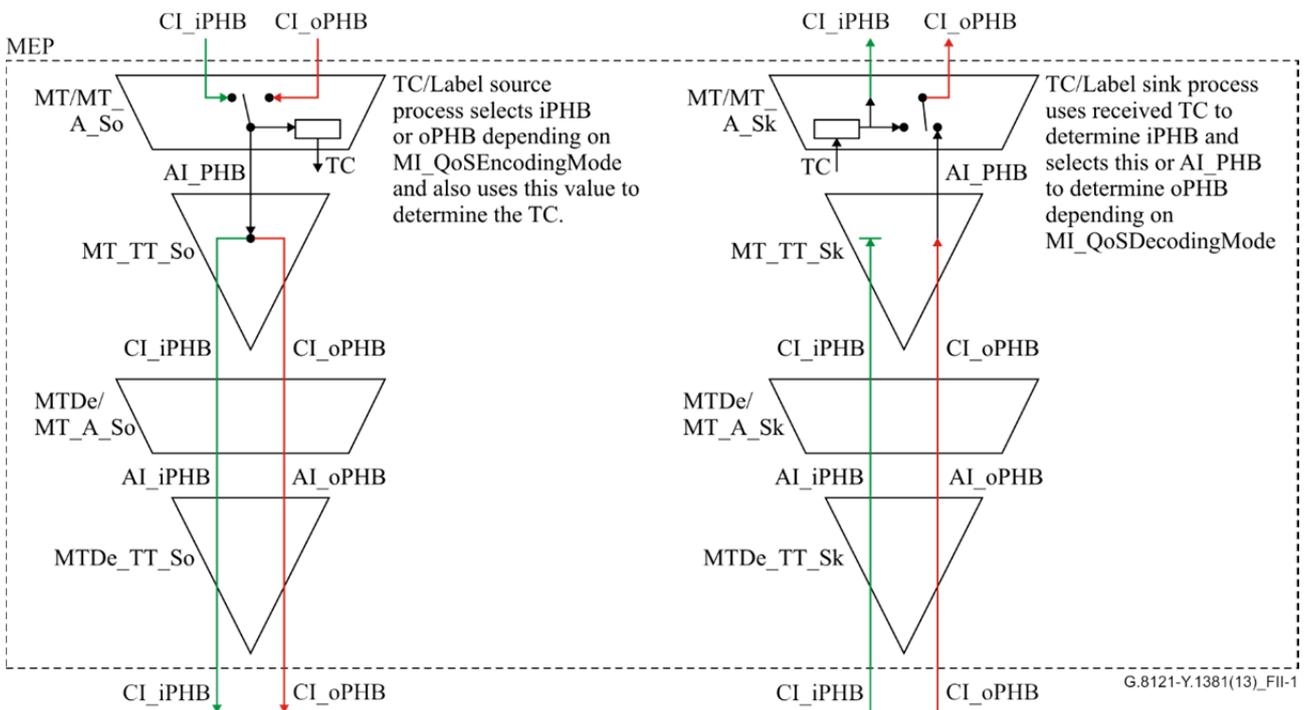
- MEP: MT/MT\_A, MT\_TT, MTDe/MT\_A, MTDe\_TT
- MIP: Two MHFs each comprising MTDi\_TT, MTDi/MT\_A

The handling of PHB values is described in clauses 8.2, 9.2, 9.3 and 9.4. The PHB information is passed between the atomic functions in characteristic information (CI) and adapted information (AI).

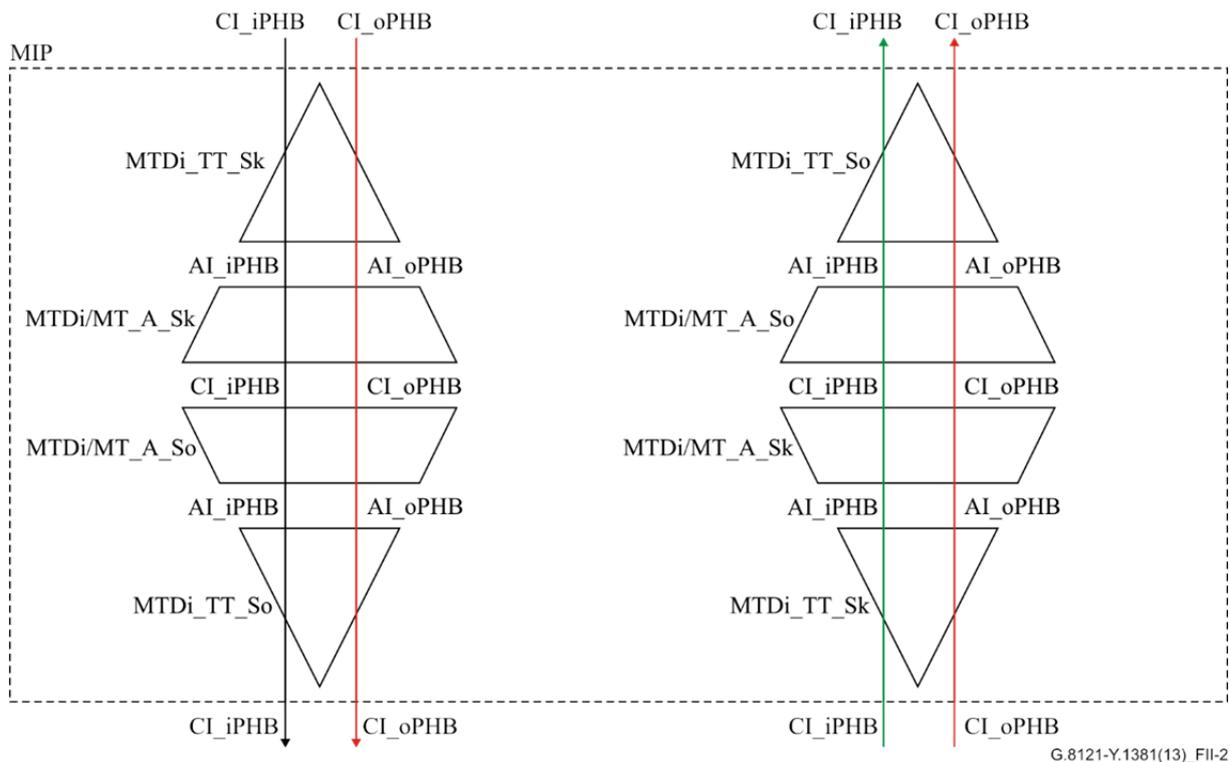
As described in clause 10 of [ITU-T G.8110.1], the MPLS-TP Diffserv architecture supports two models: the "Short Pipe" model, and the "Uniform" model. To support this, it is necessary in certain cases to pass two PHB values in the CI and AI, referred to as the incoming PHB (iPHB) and outgoing PHB (oPHB). In other cases, only a single PHB value needs to be passed between the atomic functions.

Figures 10-1 and 10-2 of [ITU-T G.8110.1] are the reference diagrams showing how PHB values are used in the two models. However, these do not show all of the atomic functions defined in this Recommendation that comprise MEPs and MIPs. In particular, it is unclear at first glance why in some cases the AI carries a single PHB value, while in other cases it carries separate iPHB and oPHB values.

The figures below show an MEP and an MIP and illustrate the flow of PHB information through them. The iPHB values are shown in green, oPHB values in red, and where only a single PHB value is used, this is shown in black.



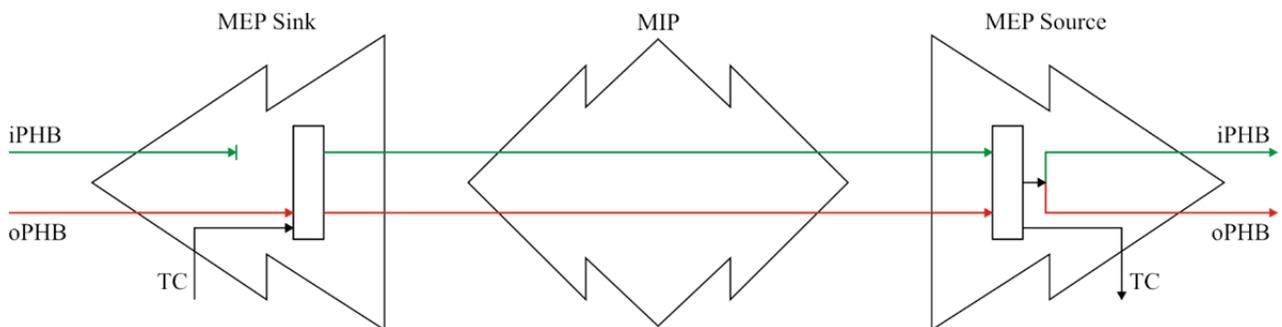
**Figure II.1 – Flow of PHB information through an MEP**



G.8121-Y.1381(13)\_FII-2

**Figure II.2 – Flow of PHB information through an MIP**

By considering a case where the sink side of one MEP is connected to the source side of another MEP, via an MIP, it becomes clear why both the iPHB and oPHB values must be passed through the MIP without modification. This is illustrated in the following figure. The same logic would apply if the MTDe\_TT and MTDe/MT\_A atomic functions were used without their associated MT\_TT and MT/MT\_A functions.



G.8121-Y.1381(13)\_FII-3

**Figure II.3 – Example showing two MEPs and an MIP**

## Appendix III

### Examples of MPLS-TP linear protection

(This appendix does not form an integral part of this Recommendation.)

The MPLS-TP linear protection switching architecture can be SNC/I and SNC/S protection, as defined in [ITU-T G.808.1]. The other architectures are for further study.

MPLS-TP subnetwork connection protection is used to protect a section of a connection (e.g., that section where two separate routes are available) within an operator's network or multiple operators' networks. Two independent subnetwork connections exist, which act as working and protection transport entities for the (protected) normal traffic signal.

Figure III.1 shows an example of the MPLS-TP SNC/I protection switching models. Protection switching is triggered by defects detected at the MPLS-TP link connection (e.g., server layer trail and server/MT adaptation function). The server layer may be MPLS-TP layer managed by another operator.

MPLS-TP SNC may be indirectly monitored by using the inherently available data from the MPLS-TP server layers. The trail termination sink of the server layer provides the TSF based SF protection switching criteria via Server/MT\_A or MT/MT\_A. No defect detection is performed on the protected MPLS-TP layers itself.

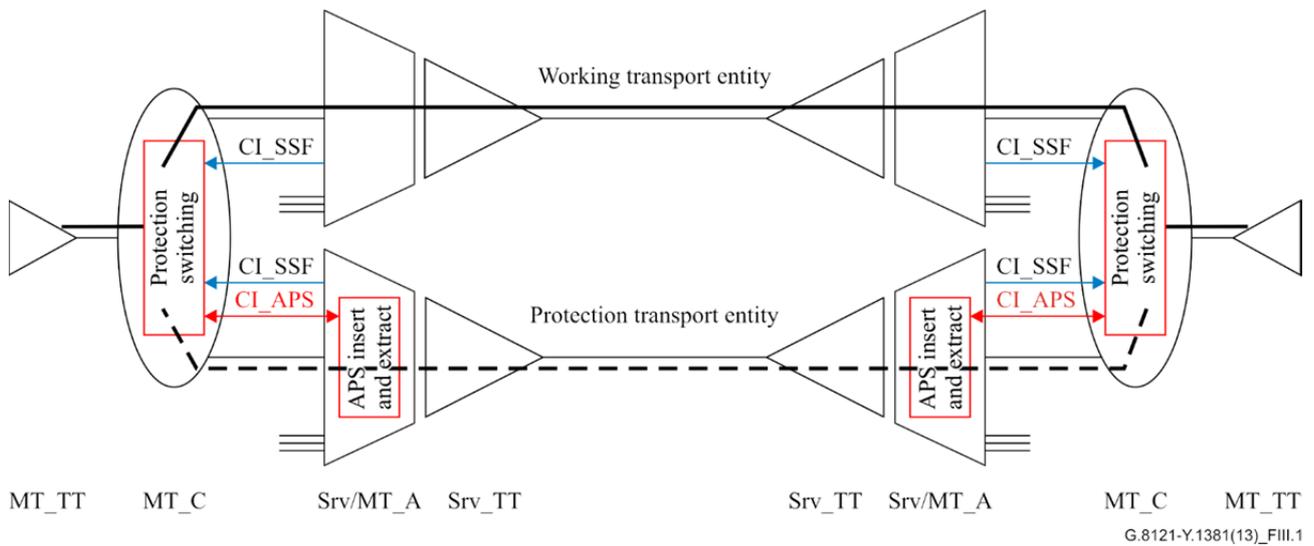
NOTE – Protection switching triggered by signal degrade is for further study.

Srv/MT\_A\_So in the protection transport entity includes the APS insert process and Srv/MT\_A\_Sk in the protection transport entity includes the APS extract process. These processes generate or process the OAM signal to indicate the CI\_APS information. The specific CI\_APS information is outside the scope of this Recommendation.

The protection switching controller process is provided in the MT connection function (MT\_C).

Within MT\_C, an MPLS-TP SNC protection switching process is instantiated to protect the MPLS-TP connection. When protection switching is configured for an MPLS-TP connection, i.e., the protected MPLS SNC, it is defined between two MT connection points (MT\_CPs). Each instantiated SNC protection switching process determines the specific output MT\_CP over which the protected MT\_CI is transferred.

As described in clause 9.1 of this Recommendation, the detail of the protection switching process in MT\_C is for further study.

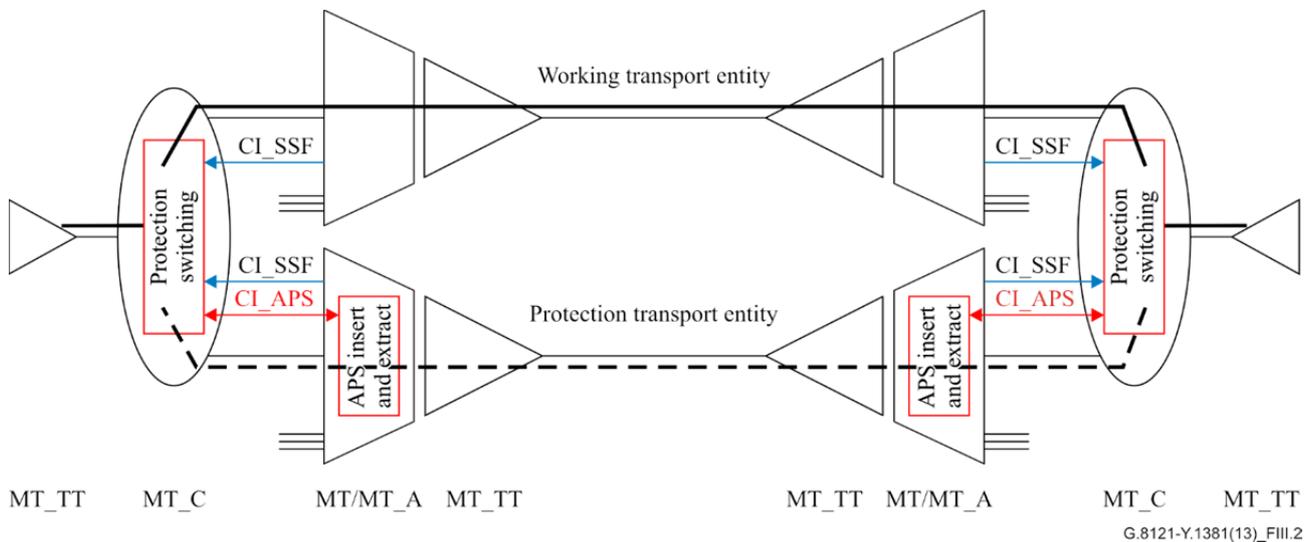


**Figure III.1 – MPLS-TP SNC/I protection switching architecture**

Figure III.2 shows an example of the MPLS-TP SNC/S protection switching models. These subnetwork connections provide sublayer monitoring for a segment of a given LSP, as described in clause 8.2.5 of [ITU-T G.8110.1].

MT/MT\_A\_So in the protection transport entity includes the APS insert process and MT/MT\_A\_Sk in the protection transport entity includes the APS extract process. These processes generate or process the OAM signal to indicate the CI\_APS information. The specific CI\_APS information is outside the scope of this Recommendation.

The protection switching controller process is provided in the MT connection function (MT\_C). The functionality in MT\_C provides the same as described for SNC/I above.



**Figure III.2 – MPLS-TP SNC/S protection switching architecture**

## **Bibliography**

- [b-ITU-T G.8151] Recommendation ITU-T G.8151/Y.1374 (2012), *Management aspects of the MPLS-TP network element*.
- [b-IETF RFC 6378] IETF RFC 6378, *MPLS Transport Profile (MPLS-TP) Linear Protection*.



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