

International Telecommunication Union

ITU-T

TELECOMMUNICATION
STANDARDIZATION SECTOR
OF ITU

G.709/Y.1331

Amendment 2

(11/2007)

SERIES G: TRANSMISSION SYSTEMS AND MEDIA,
DIGITAL SYSTEMS AND NETWORKS

Digital terminal equipments – General

SERIES Y: GLOBAL INFORMATION
INFRASTRUCTURE, INTERNET PROTOCOL ASPECTS
AND NEXT-GENERATION NETWORKS

Internet protocol aspects – Transport

Interfaces for the Optical Transport Network (OTN)
Amendment 2

ITU-T Recommendation G.709/Y.1331 (2003) –
Amendment 2



ITU-T G-SERIES RECOMMENDATIONS
TRANSMISSION SYSTEMS AND MEDIA, DIGITAL SYSTEMS AND NETWORKS

INTERNATIONAL TELEPHONE CONNECTIONS AND CIRCUITS	G.100–G.199
GENERAL CHARACTERISTICS COMMON TO ALL ANALOGUE CARRIER-TRANSMISSION SYSTEMS	G.200–G.299
INDIVIDUAL CHARACTERISTICS OF INTERNATIONAL CARRIER TELEPHONE SYSTEMS ON METALLIC LINES	G.300–G.399
GENERAL CHARACTERISTICS OF INTERNATIONAL CARRIER TELEPHONE SYSTEMS ON RADIO-RELAY OR SATELLITE LINKS AND INTERCONNECTION WITH METALLIC LINES	G.400–G.449
COORDINATION OF RADIOTELEPHONY AND LINE TELEPHONY	G.450–G.499
TRANSMISSION MEDIA AND OPTICAL SYSTEMS CHARACTERISTICS	G.600–G.699
DIGITAL TERMINAL EQUIPMENTS	G.700–G.799
General	G.700–G.709
Coding of analogue signals by pulse code modulation	G.710–G.719
Coding of analogue signals by methods other than PCM	G.720–G.729
Principal characteristics of primary multiplex equipment	G.730–G.739
Principal characteristics of second order multiplex equipment	G.740–G.749
Principal characteristics of higher order multiplex equipment	G.750–G.759
Principal characteristics of transcoder and digital multiplication equipment	G.760–G.769
Operations, administration and maintenance features of transmission equipment	G.770–G.779
Principal characteristics of multiplexing equipment for the synchronous digital hierarchy	G.780–G.789
Other terminal equipment	G.790–G.799
DIGITAL NETWORKS	G.800–G.899
DIGITAL SECTIONS AND DIGITAL LINE SYSTEM	G.900–G.999
QUALITY OF SERVICE AND PERFORMANCE – GENERIC AND USER-RELATED ASPECTS	G.1000–G.1999
TRANSMISSION MEDIA CHARACTERISTICS	G.6000–G.6999
DATA OVER TRANSPORT – GENERIC ASPECTS	G.7000–G.7999
PACKET OVER TRANSPORT ASPECTS	G.8000–G.8999
ACCESS NETWORKS	G.9000–G.9999

For further details, please refer to the list of ITU-T Recommendations.

ITU-T Recommendation G.709/Y.1331

Interfaces for the Optical Transport Network (OTN)

Amendment 2

Summary

Amendment 2 to ITU-T Recommendation G.709/Y.1331 (03/2003) as modified per Corrigendum 1 (12/2006):

- clarifies the coding of the MSI fields in the OPU3 for the case of ODU1 and/or ODU2 mapping into tributary slots of the OPU3;
- extends the listed set of client signals of the ODUk with the T-MPLS client;
- corrects a number of editorial mistakes in clauses 19.2.x and Appendix I.

Source

Amendment 2 to ITU-T Recommendation G.709/Y.1331 (2003) was approved on 22 November 2007 by ITU-T Study Group 15 (2005-2008) under the ITU-T Recommendation A.8 procedure.

FOREWORD

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The World Telecommunication Standardization Assembly (WTSA), which meets every four years, establishes the topics for study by the ITU-T study groups which, in turn, produce Recommendations on these topics.

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As of the date of approval of this Recommendation, ITU had received notice of intellectual property, protected by patents, which may be required to implement this Recommendation. However, implementers are cautioned that this may not represent the latest information and are therefore strongly urged to consult the TSB patent database at <http://www.itu.int/ITU-T/ipr/>.

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CONTENTS

	Page
1) Clause 6.1, Basic signal structure	1
2) Clause 19.2.1, ODTU12	1
3) Clause 19.2.2, ODTU13	1
4) Clause 19.2.3, ODTU23	2
5) Clause 19.4, OPUk Multiplex Overhead	2
6) Clause 19.4.1, OPUk Multiplex Structure Identifier (MSI)	4
7) Clause 19.4.1.1, OPU2 Multiplex Structure Identifier (MSI)	4
8) Clause 19.4.1.2, OPU3 Multiplex Structure Identifier (MSI)	4
9) Clause 19.4.2, OPUk Payload Structure Identifier Reserved overhead (RES)	5
10) Appendix I	5
11) Appendix I, ODU1 into ODU2 multiplexing	5
12) Appendix I, ODU2 into ODU3 multiplexing	5
13) Appendix I, ODU1 into ODU3 multiplexing	5
14) Appendix VI	5

Interfaces for the Optical Transport Network (OTN)

Amendment 2

1) Clause 6.1, Basic signal structure

Replace Figure 6-1 with the following figure in which T-MPLS is included as client:

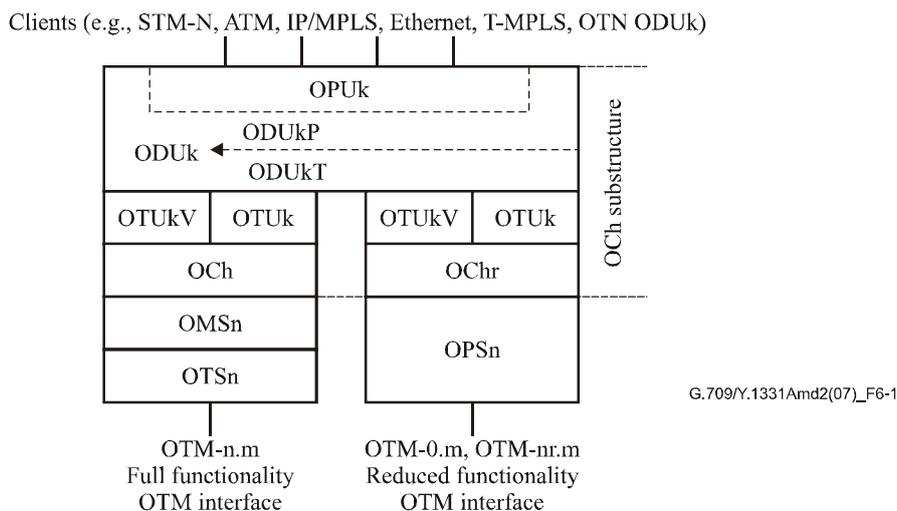


Figure 6-1 – Structure of the OTN interfaces

2) Clause 19.2.1, ODTU12

Modify the following paragraph as follows:

The Optical channel Data Tributary Unit 12 (ODTU12) is a structure with 952 columns by 16 (4 × 4) rows plus 1 column of Justification Overhead (JOH). It carries a justified ODU1 signal. The ODTU12 structure is illustrated in Figure 19-11. The location of the JOH column depends on the OPU2 tributary slot used when multiplexing the ODTU12 in the OPU2 (see 19.1.1).

3) Clause 19.2.2, ODTU13

Modify the following paragraph as follows:

The Optical channel Data Tributary Unit 13 (ODTU13) is a structure with 238 columns by 64 (16 × 4) rows plus 1 column of Justification Overhead (JOH). It carries a justified ODU1 signal. The ODTU13 structure is illustrated in Figure 19-12. The location of the JOH column depends on the OPU3 tributary slot used when multiplexing the ODTU13 in the OPU3 (see 19.1.2).

4) Clause 19.2.3, ODTU23

Modify the following paragraph as follows:

The Optical channel Data Tributary Unit 23 (ODTU23) is a structure with 952 columns by 64 (16×4) rows plus 4 times 1 column of Justification Overhead (JOH). It carries a justified ODU2 signal. The ODTU23 structure is illustrated in Figure 19-13. The location of the JOH column depends on the OPU3 tributary slot used when multiplexing the ODTU23 in the OPU3 (see 19.1.2). They might not be equally distributed.

5) Clause 19.4, OPUk Multiplex Overhead

Replace Figure 19-6 with the following figure in which the MSI field is defined with an OPUk specific length:

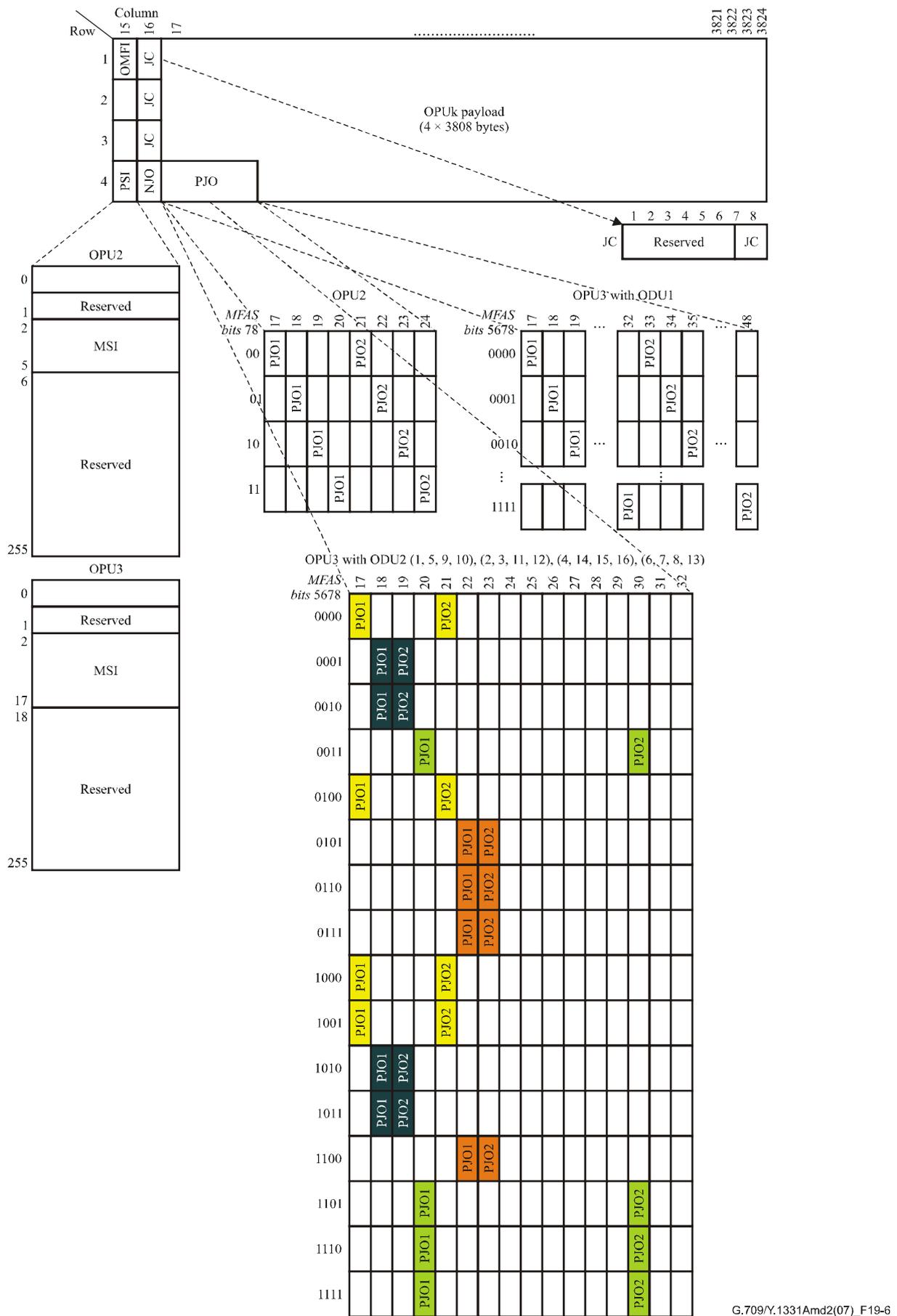


Figure 19-6 – OPUk multiplex overhead

6) Clause 19.4.1, OPUk Multiplex Structure Identifier (MSI)

Modify the following paragraphs as follows:

The multiplex structure identifier (MSI) overhead, which encodes the ODU multiplex structure in the OPU, is located in the mapping specific area of the PSI signal (OPU2: PSI[2] .. PSI[5], OPU3: PSI[2] .. PSI[17]). The MSI has an OPU specific length (OPU2: 4 bytes, OPU3: 16 bytes) and indicates the content of each tributary slot (TS) of an OPU. The generic coding for each TS is shown in Figure 19-7. One byte is used for each TS.

- Bits 1 and 2 indicate the ODU type transported in the TS.
- Bits 3 to 8 indicate the tributary port of the ODU transported. This is of interest in case of flexible assignment of ODUs to tributary slots (e.g., ODU2 into OPU3). In case of fixed assignment the tributary port number corresponds to the tributary slot number.

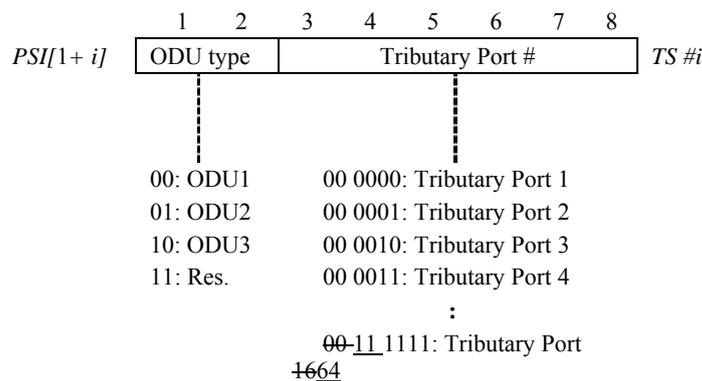


Figure 19-7 – Generic MSI coding

7) Clause 19.4.1.1, OPU2 Multiplex Structure Identifier (MSI)

Modify the following paragraphs as follows:

For the 4 OPU2 tributary slots 4 bytes of the PSI are used (PSI[2] .. PSI[5]) as shown in Figures 19-6 and 19-8.

- The ODU type is fixed ODU1.
- The tributary port # indicates the port number of the ODU1 that is being transported in this TS; the assignment of ports to tributary slots is fixed, the port number equals the tributary slot number.

~~The remaining 12 bytes of the MSI field (PSI[6] to PSI[17]) are unused. They are set to 0 and ignored by the receiver.~~

8) Clause 19.4.1.2, OPU3 Multiplex Structure Identifier (MSI)

Modify the following paragraphs as follows:

For the 16 OPU3 tributary slots 16 bytes of the PSI are used (PSI[2] .. PSI[17]) as shown in Figures 19-6 and 19-9.

- The ODU type indicates if the OPU3 TS is carrying ODU1 or ODU2. The default ODU type is ODU1; it is present when either a tributary slot carries an ODU1, or is not allocated to carry an ODU. Refer to Appendix VI for some examples.

9) Clause 19.4.2, OPUk Payload Structure Identifier Reserved overhead (RES)

Modify the following paragraph as follows:

251 (OPU2) and 239 (OPU3) bytes are reserved in the OPUk PSI for future international standardization. These bytes are located in PSI[1] and PSI[6] (OPU2), PSI[18] (OPU3) to {PSI[255]} of the OPUk overhead. These bytes are set to all ZEROs.

10) Appendix I

Modify the 4th paragraph as follows:

Let α represent justification ratio ($-1 \leq \alpha \leq 1$ for CBR client into ODUk mapping; $-\underline{1} \leq \alpha \leq \underline{2-1}$ for ODUj into ODUk mapping ($k > j$)), and use the further convention that positive α will correspond to negative justification and negative α to positive justification (the reason for this convention is explained below).

11) Appendix I, ODU1 into ODU2 multiplexing

Modify the last paragraph as follows:

In addition, stuff ratios of $-\underline{1-2}$ and $+\underline{12}$ are obtained for frequency offsets of $-\underline{47.998-113.65}$ ppm and $\underline{148.96-83.30}$ ppm, respectively. The range of frequency offset that can be accommodated is approximately 197 ppm. This is 50% larger than the range that can be accommodated by a +1/0/-1 justification scheme (see above), and is due to the additional positive stuff byte.

12) Appendix I, ODU2 into ODU3 multiplexing

Modify the last paragraph as follows:

In addition, stuff ratios of $-\underline{1-2}$ and $+\underline{2-1}$ are obtained for frequency offsets of $-\underline{30.195-95.85}$ ppm and $\underline{166.77-101.11}$ ppm, respectively. As above, the range of frequency offset that can be accommodated is approximately 197 ppm, which is 50% larger than the range that can be accommodated by a +1/0/-1 justification scheme (see above) due to the additional positive stuff byte.

13) Appendix I, ODU1 into ODU3 multiplexing

Modify the last paragraph as follows:

In addition, stuff ratios of $-\underline{1-2}$ and $+\underline{2-1}$ are obtained for frequency offsets of $-\underline{30.472-96.40}$ ppm and $\underline{167.32-101.39}$ ppm, respectively. As above, the range of frequency offset that can be accommodated is approximately 197 ppm, which is 50% larger than the range that can be accommodated by a +1/0/-1 justification scheme (see above) due to the additional positive stuff byte.

14) Appendix VI

Add a new Appendix VI with the following text:

Appendix VI

ODUk Multiplex Structure Identifier (MSI) examples

(This appendix does not form an integral part of this Recommendation)

The following figures present four examples of ODU1 and ODU2 carriage within an OPU3 and the associated MSI encoding.

	<u>1</u>	<u>2</u>	<u>3</u>	<u>4</u>	<u>5</u>	<u>6</u>	<u>7</u>	<u>8</u>	
<i>PSI/21</i>	00				000000				<i>TS1</i>
<i>PSI/31</i>	00				000001				<i>TS2</i>
<i>PSI/41</i>	00				000010				<i>TS3</i>
<i>PSI/51</i>	00				000011				<i>TS4</i>
<i>PSI/61</i>	00				000100				<i>TS5</i>
<i>PSI/71</i>	00				000101				<i>TS6</i>
<i>PSI/81</i>	00				000110				<i>TS7</i>
<i>PSI/91</i>	00				000111				<i>TS8</i>
<i>PSI/101</i>	00				001000				<i>TS9</i>
<i>PSI/111</i>	00				001001				<i>TS10</i>
<i>PSI/121</i>	00				001010				<i>TS11</i>
<i>PSI/131</i>	00				001011				<i>TS12</i>
<i>PSI/141</i>	00				001100				<i>TS13</i>
<i>PSI/151</i>	00				001101				<i>TS14</i>
<i>PSI/161</i>	00				001110				<i>TS15</i>
<i>PSI/171</i>	00				001111				<i>TS16</i>

Figure VI.1 – OPU3-MSI coding for case of 16-tuple ODU1 into OPU3

	<u>1</u>	<u>2</u>	<u>3</u>	<u>4</u>	<u>5</u>	<u>6</u>	<u>7</u>	<u>8</u>	
<i>PSI/21</i>	01				000000				<i>TS1</i>
<i>PSI/31</i>	01				000001				<i>TS2</i>
<i>PSI/41</i>	01				000010				<i>TS3</i>
<i>PSI/51</i>	01				000011				<i>TS4</i>
<i>PSI/61</i>	01				000000				<i>TS5</i>
<i>PSI/71</i>	01				000001				<i>TS6</i>
<i>PSI/81</i>	01				000010				<i>TS7</i>
<i>PSI/91</i>	01				000011				<i>TS8</i>
<i>PSI/101</i>	01				000000				<i>TS9</i>
<i>PSI/111</i>	01				000001				<i>TS10</i>
<i>PSI/121</i>	01				000010				<i>TS11</i>
<i>PSI/131</i>	01				000011				<i>TS12</i>
<i>PSI/141</i>	01				000000				<i>TS13</i>
<i>PSI/151</i>	01				000001				<i>TS14</i>
<i>PSI/161</i>	01				000010				<i>TS15</i>
<i>PSI/171</i>	01				000011				<i>TS16</i>

Figure VI.2 – OPU3-MSI coding for case of 4-tuple ODU2 into OPU3 TS# (1, 5, 9, 13), (2, 6, 10, 14), (3, 7, 11, 15) and (4, 8, 12, 16)

	<u>1</u>	<u>2</u>	<u>3</u>	<u>4</u>	<u>5</u>	<u>6</u>	<u>7</u>	<u>8</u>	
<i>PSI</i> [2]	01				000000				TS1
<i>PSI</i> [3]	01				000001				TS2
<i>PSI</i> [4]	01				000001				TS3
<i>PSI</i> [5]	01				000010				TS4
<i>PSI</i> [6]	01				000000				TS5
<i>PSI</i> [7]	01				000011				TS6
<i>PSI</i> [8]	01				000011				TS7
<i>PSI</i> [9]	01				000011				TS8
<i>PSI</i> [10]	01				000000				TS9
<i>PSI</i> [11]	01				000000				TS10
<i>PSI</i> [12]	01				000001				TS11
<i>PSI</i> [13]	01				000001				TS12
<i>PSI</i> [14]	01				000011				TS13
<i>PSI</i> [15]	01				000010				TS14
<i>PSI</i> [16]	01				000010				TS15
<i>PSI</i> [17]	01				000010				TS16

Figure VI.3 – OPU3-MSI coding for case of 4-tuple ODU2 into OPU3 TS# (1, 5, 9, 10), (2, 3, 11, 12), (4, 14, 15, 16) and (6, 7, 8, 13)

	<u>1</u>	<u>2</u>	<u>3</u>	<u>4</u>	<u>5</u>	<u>6</u>	<u>7</u>	<u>8</u>	
<i>PSI</i> [2]	01				000000				TS1
<i>PSI</i> [3]	00				000001				TS2
<i>PSI</i> [4]	00				000010				TS3
<i>PSI</i> [5]	00				000001				TS4
<i>PSI</i> [6]	01				000000				TS5
<i>PSI</i> [7]	00				000101				TS6
<i>PSI</i> [8]	00				000110				TS7
<i>PSI</i> [9]	00				000001				TS8
<i>PSI</i> [10]	01				000000				TS9
<i>PSI</i> [11]	01				000001				TS10
<i>PSI</i> [12]	00				001010				TS11
<i>PSI</i> [13]	00				001011				TS12
<i>PSI</i> [14]	01				000000				TS13
<i>PSI</i> [15]	00				001101				TS14
<i>PSI</i> [16]	00				001110				TS15
<i>PSI</i> [17]	01				000001				TS16

Figure VI.4 – OPU3-MSI coding for case of 5-tuple ODU1 and 2-tuple ODU2 into OPU3 TS# (2), (6), (11), (12), (14), (1, 5, 9, 13) and (4, 8, 10, 16) and OPU3 TS# 3, 7, 15 unallocated (default to ODU1)

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**GLOBAL INFORMATION INFRASTRUCTURE, INTERNET PROTOCOL ASPECTS AND
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General	Y.100–Y.199
Services, applications and middleware	Y.200–Y.299
Network aspects	Y.300–Y.399
Interfaces and protocols	Y.400–Y.499
Numbering, addressing and naming	Y.500–Y.599
Operation, administration and maintenance	Y.600–Y.699
Security	Y.700–Y.799
Performances	Y.800–Y.899
INTERNET PROTOCOL ASPECTS	
General	Y.1000–Y.1099
Services and applications	Y.1100–Y.1199
Architecture, access, network capabilities and resource management	Y.1200–Y.1299
Transport	Y.1300–Y.1399
Interworking	Y.1400–Y.1499
Quality of service and network performance	Y.1500–Y.1599
Signalling	Y.1600–Y.1699
Operation, administration and maintenance	Y.1700–Y.1799
Charging	Y.1800–Y.1899
NEXT GENERATION NETWORKS	
Frameworks and functional architecture models	Y.2000–Y.2099
Quality of Service and performance	Y.2100–Y.2199
Service aspects: Service capabilities and service architecture	Y.2200–Y.2249
Service aspects: Interoperability of services and networks in NGN	Y.2250–Y.2299
Numbering, naming and addressing	Y.2300–Y.2399
Network management	Y.2400–Y.2499
Network control architectures and protocols	Y.2500–Y.2599
Security	Y.2700–Y.2799
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