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ITU-T

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OF ITU

G.992.3

Corrigendum 1
(12/2003)

SERIES G: TRANSMISSION SYSTEMS AND MEDIA,
DIGITAL SYSTEMS AND NETWORKS

Digital sections and digital line system – Access networks

Asymmetric digital subscriber line transceivers 2
(ADSL2)

Corrigendum 1

ITU-T Recommendation G.992.3 (2002) – Corrigendum 1

ITU-T G-SERIES RECOMMENDATIONS
TRANSMISSION SYSTEMS AND MEDIA, DIGITAL SYSTEMS AND NETWORKS

INTERNATIONAL TELEPHONE CONNECTIONS AND CIRCUITS	G.100–G.199
GENERAL CHARACTERISTICS COMMON TO ALL ANALOGUE CARRIER-TRANSMISSION SYSTEMS	G.200–G.299
INDIVIDUAL CHARACTERISTICS OF INTERNATIONAL CARRIER TELEPHONE SYSTEMS ON METALLIC LINES	G.300–G.399
GENERAL CHARACTERISTICS OF INTERNATIONAL CARRIER TELEPHONE SYSTEMS ON RADIO-RELAY OR SATELLITE LINKS AND INTERCONNECTION WITH METALLIC LINES	G.400–G.449
COORDINATION OF RADIOTELEPHONY AND LINE TELEPHONY TESTING EQUIPMENTS	G.450–G.499
TRANSMISSION MEDIA CHARACTERISTICS	G.500–G.599
DIGITAL TERMINAL EQUIPMENTS	G.600–G.699
DIGITAL NETWORKS	G.700–G.799
DIGITAL SECTIONS AND DIGITAL LINE SYSTEM	G.800–G.899
General	G.900–G.909
Parameters for optical fibre cable systems	G.910–G.919
Digital sections at hierarchical bit rates based on a bit rate of 2048 kbit/s	G.920–G.929
Digital line transmission systems on cable at non-hierarchical bit rates	G.930–G.939
Digital line systems provided by FDM transmission bearers	G.940–G.949
Digital line systems	G.950–G.959
Digital section and digital transmission systems for customer access to ISDN	G.960–G.969
Optical fibre submarine cable systems	G.970–G.979
Optical line systems for local and access networks	G.980–G.989
Access networks	G.990–G.999
QUALITY OF SERVICE AND PERFORMANCE – GENERIC AND USER-RELATED ASPECTS	G.1000–G.1999
TRANSMISSION MEDIA CHARACTERISTICS	G.6000–G.6999
DIGITAL TERMINAL EQUIPMENTS	G.7000–G.7999
DIGITAL NETWORKS	G.8000–G.8999

For further details, please refer to the list of ITU-T Recommendations.

ITU-T Recommendation G.992.3

Asymmetric digital subscriber line transceivers 2 (ADSL2)

Corrigendum 1

Source

Corrigendum 1 to ITU-T Recommendation G.992.3 (2002) was approved on 14 December 2003 by ITU-T Study Group 15 (2001-2004) under the ITU-T Recommendation A.8 procedure.

FOREWORD

The International Telecommunication Union (ITU) is the United Nations specialized agency in the field of telecommunications. The ITU Telecommunication Standardization Sector (ITU-T) is a permanent organ of ITU. ITU-T is responsible for studying technical, operating and tariff questions and issuing Recommendations on them with a view to standardizing telecommunications on a worldwide basis.

The World Telecommunication Standardization Assembly (WTSA), which meets every four years, establishes the topics for study by the ITU-T study groups which, in turn, produce Recommendations on these topics.

The approval of ITU-T Recommendations is covered by the procedure laid down in WTSA Resolution 1.

In some areas of information technology which fall within ITU-T's purview, the necessary standards are prepared on a collaborative basis with ISO and IEC.

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In this Recommendation, the expression "Administration" is used for conciseness to indicate both a telecommunication administration and a recognized operating agency.

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CONTENTS

	Page
1) CL and CLR describe enabled capabilities.....	1
2) HDLC message segmentation.....	1
3) Clause 8.5.1: Definition of control parameters.....	1
4) Clause 9.5.2: Stationarity control mechanism.....	2
5) Loop attenuation.....	3
6) Dynamic behaviour.....	3

ITU-T Recommendation G.992.3

Asymmetric digital subscriber line transceivers 2 (ADSL2)

Corrigendum 1

1) CL and CLR describe enabled capabilities

Clauses 6.6.1, 7.10.1, 8.13.2 and K.x.10

Add the following paragraph to these clauses (just before first subclause header):

The CL and CLR messages shall describe capabilities of the ATU-C and ATU-R respectively and can be constrained by application requirements, service requirements, implementation choices, etc. Therefore, the capabilities indicated in the CL and CLR message are the enabled capabilities, which may be equal to or a subset of the set of capabilities supported by the ATU-C and ATU-R respectively. In any case, the MS message (and all subsequent initialization messages) shall account for all capability restrictions indicated in the CL and CLR messages.

2) HDLC message segmentation

Transmitter protocol (7.8.2.4.1)

Change 5th and 6th paragraphs as follows:

When sending a new command message, the LSB of the control field shall be inverted from the previous command message, irrespective of the priority class. The transmitter shall send the command message one time and await a response message. No more than one command message of each priority value shall be awaiting response message at any time. Upon receipt of a response message, a new command message may be sent. If a response message is not received, a time-out occurs and the command message is repeated without inverting the LSB of the control field. Alternately, the ATU may abandon the command message after an implementation specific number of retransmissions. There are different time-out durations for the different priority messages and are displayed in Table 7-17. Timeouts are based starting from the last octet of a request message sent to ~~last~~the first octet of a response message received.

When sending a new response message, the LSB of the control field shall be inverted from the previous response message, irrespective of the priority class.

3) Clause 8.5.1: Definition of control parameters

Change Table 8-4 as follows:

Table 8-4/G.992.3 – The transmit PMD function control parameters

<i>L0-TIME</i>	<p>These configuration parameters are related to the L2 low power state and exist only for the ATU-C. They are configured through the CO-MIB.</p> <p>The <i>L0-TIME</i> represents the minimum time (in seconds) between Exit from L2 low power state and the next Entry into the L2 low power state (see 9.5.2).</p> <p>The <i>L2-TIME</i> represents the minimum time (in seconds) between Entry into L2 low power state and the first L2 low power trim request and between two consecutive L2 power trim requests (see 9.5.2).</p> <p>The <i>L2-ATPR</i> value represents the maximum aggregate transmit power reduction that is allowed in an <u>L2 Request or an L2 low power trim request</u> (see 9.5.2).</p> <p><u>The <i>L2-ATPRT</i> value represents the total maximum aggregate transmit power reduction that is allowed in the L2 state; the total reduction is the sum of all reductions of L2 Request and L2 power trims (see 9.5.2).</u></p>
<i>L2-TIME</i>	
<i>L2-ATPR</i>	
<i>L2-ATPRT</i>	

4) **Clause 9.5.2: Stationarity control mechanism**

Change the clause as follows:

ATU-C PMD control parameters provide means to configure the minimum duration within link state L0 (before transition to a different link state) and the minimum duration within link state L2 before using the power trim procedure. This L2 minimum does not restrict the use of the fast exit power procedures. The minimum link state durations may depend on the amount of power cutback to be applied.

ATU-C PMD control parameters also provide means to configure the maximum aggregate transmit power reduction that is allowed in an L2 request and in any single L2 low power trim request, by means of the L2-ATPR control parameter.

The maximum PCBds in a L2 request command shall be limited by following constraint:

$$\frac{\text{maximum_PCBds} - \text{PCBds}(L0)}{\text{L2_ATPR}} \leq 1$$

where maximum_PCBds is the maximum PCBds value in the L2 request;

where PCBds(L0) is the PCBds value of the L0 state.

The proposed value of PCBds (in dB) in any L2 trim command shall be limited by following constraint:

$$\frac{\text{PCBds}(\text{proposed}) - \text{PCBds}(\text{current})}{\text{L2_ATPR}} \leq 1$$

where PCBds(proposed) is PCBds value proposed in the L2 trim command;

where PCBds(current) is the PCBds value currently used in the L2 state.

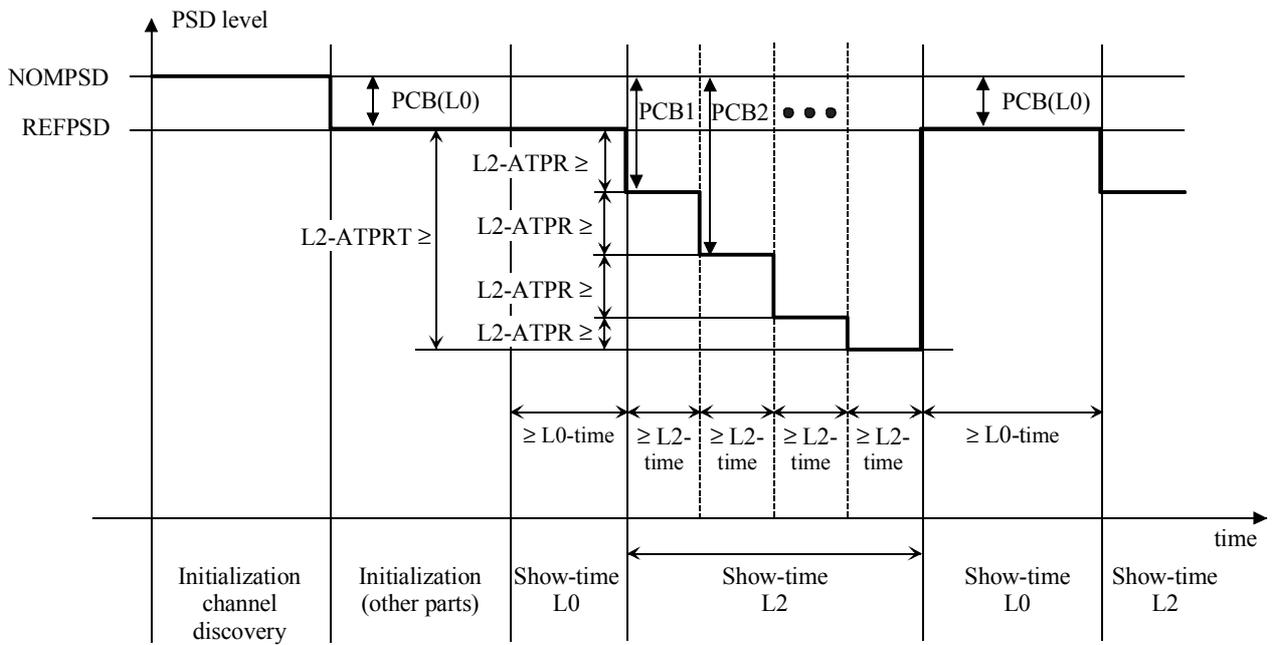
ATU-C PMD control parameters also provide means to configure the total maximum aggregate transmit power reduction that is allowed in the L2 state, by means of the L2-ATPRT control parameter. All PCBds values in the L2 state (i.e., the maximum PCBds in a L2 request command, and the proposed value of PCBds (in dB) in any L2 trim command) shall be limited by following constraint:

$$\frac{\text{PCBds} - \text{PCBds}(L0)}{\text{L2_ATPRT}} \leq 1$$

where PCBds is any PCBds value in the L2 state;

where PCBds(L0) is the PCBds value of the L0 state.

The L2 power state control parameters L0-TIME, L2-TIME, L2-ATPR and L2-ATPRT are illustrated in Figure 9-4a.



G.992.3COR.1_F9-4a

Figure 9-4a/G.992.3 – Illustration of the L2 power state control parameters

5) Loop attenuation

Loop Attenuation (LATN) (8.12.3.4)

In the equation, the LATN results in negative attenuation number. Needs to be inverted to positive number to map into unsigned integer.

In the equation, change " $10 \cdot \log(\dots)$ " to " $-10 \cdot \log(\dots)$ "

6) Dynamic behaviour

On-line reconfiguration procedures – Receiver initiated procedure (10.2.2.1)

Power management procedures – Receiver initiated procedures (10.3.2.1)

Change bullet 4 as follows in both clauses:

- 4) The receiving ATU's control function sends a PMD.Control.confirm primitive to the receive PMD function which then waits until the respective priority timeout (see 7.8.2.4.1) for a PMD.SynchFlag to be received from the transmit PMD function.

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Series A	Organization of the work of ITU-T
Series B	Means of expression: definitions, symbols, classification
Series C	General telecommunication statistics
Series D	General tariff principles
Series E	Overall network operation, telephone service, service operation and human factors
Series F	Non-telephone telecommunication services
Series G	Transmission systems and media, digital systems and networks
Series H	Audiovisual and multimedia systems
Series I	Integrated services digital network
Series J	Cable networks and transmission of television, sound programme and other multimedia signals
Series K	Protection against interference
Series L	Construction, installation and protection of cables and other elements of outside plant
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Series N	Maintenance: international sound programme and television transmission circuits
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Series P	Telephone transmission quality, telephone installations, local line networks
Series Q	Switching and signalling
Series R	Telegraph transmission
Series S	Telegraph services terminal equipment
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Series U	Telegraph switching
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