



INTERNATIONAL TELECOMMUNICATION UNION

ITU-T

TELECOMMUNICATION
STANDARDIZATION SECTOR
OF ITU

G.806

Corrigendum 1
(08/2004)

SERIES G: TRANSMISSION SYSTEMS AND MEDIA,
DIGITAL SYSTEMS AND NETWORKS

Digital networks – General aspects

Characteristics of transport equipment – Description
methodology and generic functionality

Corrigendum 1

ITU-T Recommendation G.806 (2004) – Corrigendum 1

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ITU-T Recommendation G.806

Characteristics of transport equipment – Description methodology and generic functionality

Corrigendum 1

Source

Corrigendum 1 to ITU-T Recommendation G.806 (2004) was approved on 22 August 2004 by ITU-T Study Group 15 (2001-2004) under the ITU-T Recommendation A.8 procedure.

FOREWORD

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ITU-T Recommendation G.806

Characteristics of transport equipment – Description methodology and generic functionality

Corrigendum 1

1 Introduction

This corrigendum contains editorial and technical corrections, to the 02/2004 revision of ITU-T Rec. G.806.

2 Corrections

2.1 Clause 6.2.6.4

Replace the text:

GFP Client Signal Fail (dCSF) is raised when a GFP frame with correct tHEC, with a PTI = "100" and a UPI value of either "0000 0001" or "0000 0010" is received. dUPM is cleared when no such GFP client management frame is received in $N \times 1000$ ms or a valid GFP client data frame is received. A value of 3 is suggested for N.

With:

GFP Client Signal Fail (dCSF) is raised when a GFP frame with correct tHEC, with a PTI = "100" and a UPI value of either "0000 0001" or "0000 0010" is received. dCSF is cleared when no such GFP client management frame is received in $N \times 1000$ ms or a valid GFP client data frame is received. A value of 3 is suggested for N.

2.2 Clause 8

In the text and figures of the clause and all its subclauses, remove all the occurrences of "MI_" in text strings (e.g., "MI_CMuxConfig" → "CMuxConfig").

2.3 Clause 8.5.2.2

Replace the first paragraph of the frame delineation process:

Frame delineation: GFP frame delineation is performed as defined in 6.3.1/G.7041/Y.1303. Frame delineation is assumed to be achieved (IF) when the process is in the "SYNC" state. Frame delineation is assumed to be lost (OOF) when the process is not in the "SYNC" state. Idle GFP frames participate in the delineation process and are then discarded.

With:

Frame delineation: GFP frame delineation is performed as defined in 6.3.1/G.7041/Y.1303. Frame delineation is assumed to be achieved when the process is in the "SYNC" state. Frame delineation is assumed to be lost when the process is not in the "SYNC" state. Idle GFP frames participate in the delineation process and are then discarded.

2.4 Clause 8.5.3.2

Replace Figure 8-14:

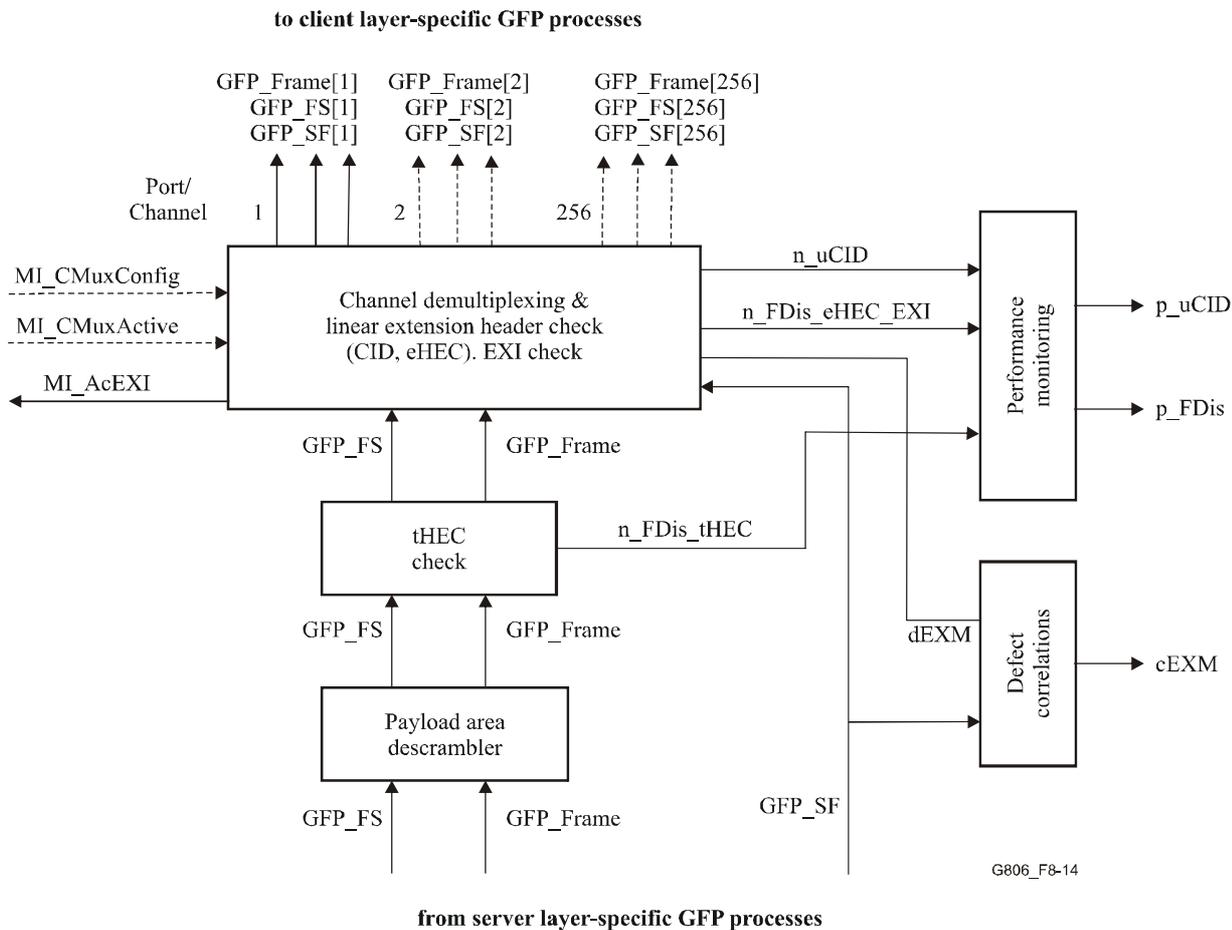


Figure 8-14/G.806 – Common GFP sink processes

With:

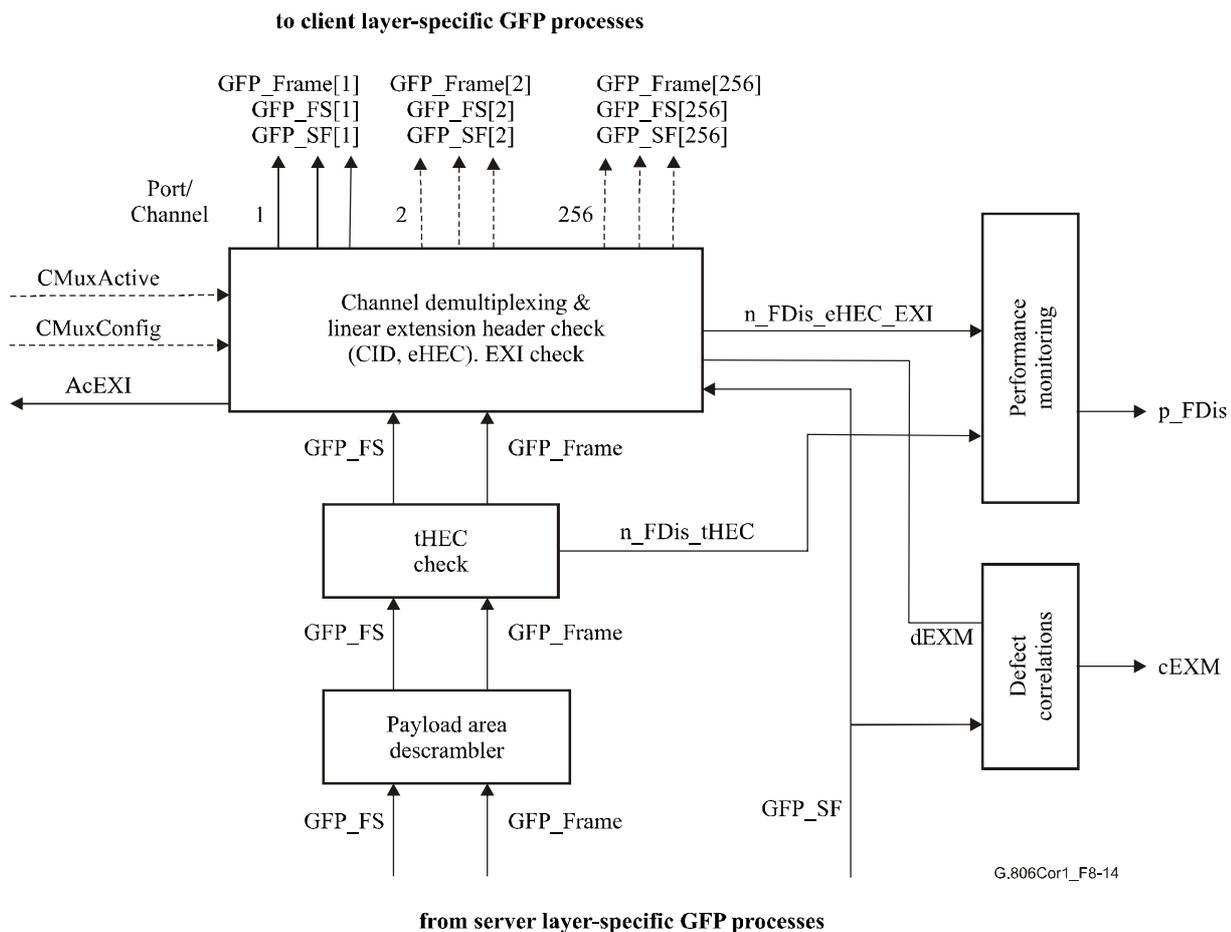


Figure 8-14/G.806 – Common GFP sink processes

Replace the fourth paragraph of the Channel demultiplexing, linear extension header check, EXI check process:

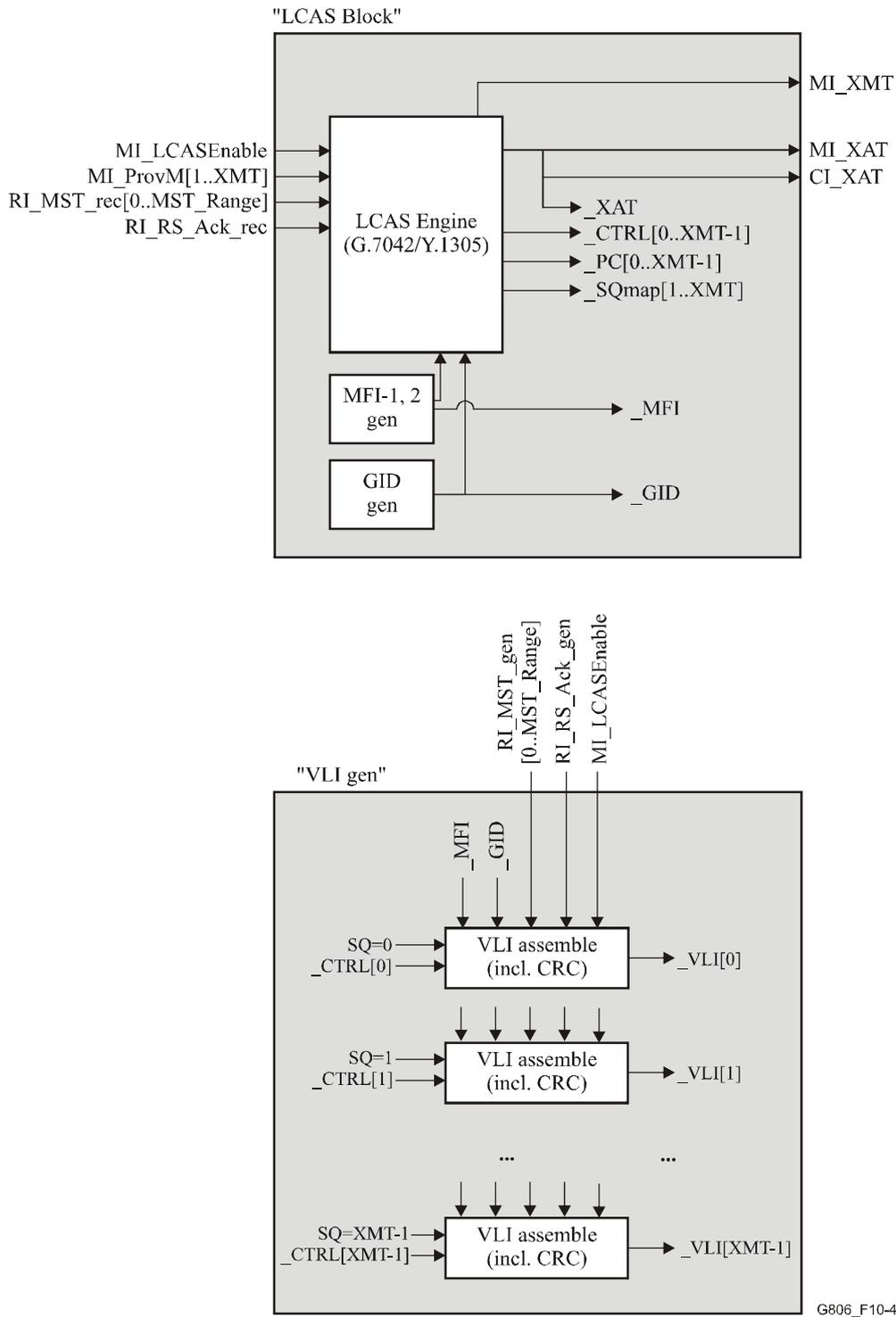
In case GFP channel multiplexing is not supported or not active (MI_CMuxActive=true) the accepted EXI (AcEXI) is compared with the value "0000". If it has a different value, the frame is discarded. All discarded frames are indicated by n_FDis_eHEC_EXI.

With:

In case GFP channel multiplexing is not supported or not active (CMuxActive=false), the accepted EXI (AcEXI) is compared with the value "0000". If it has a different value, the frame is discarded. All discarded frames are indicated by n_FDis_eHEC_EXI.

2.5 Clause 10.1.1.1

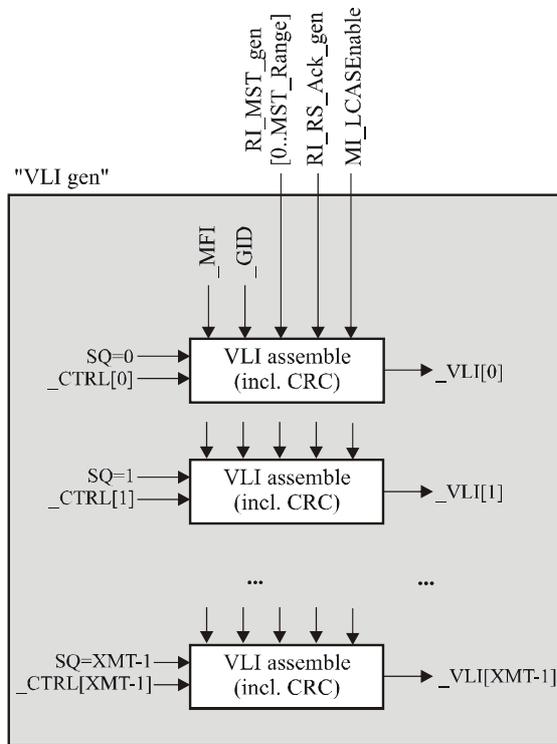
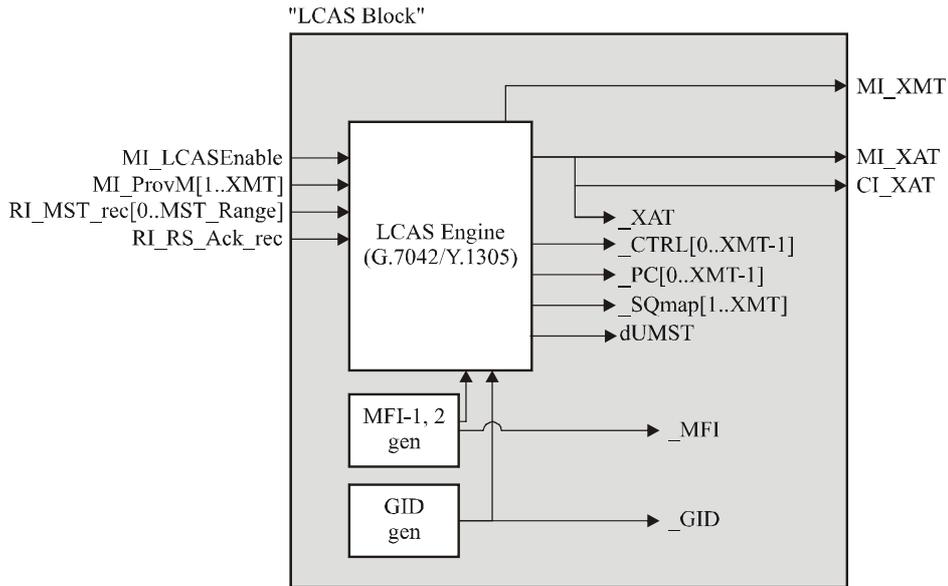
Replace Figure 10-4:



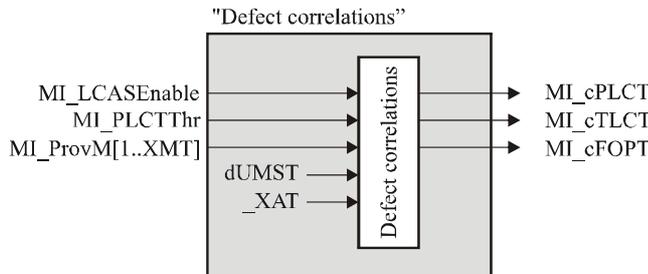
NOTE – The internal inputs _GID, _MFI, _CTRL[k] are sourced at the "LCAS Block".

Figure 10-4/G.806 – P-Xv/P-X-L_A_So processes (subdiagrams)

With:



NOTE – The internal inputs _GID, _MFI, _CTRL[k] are sourced at the "LCAS Block".



NOTE – The internal inputs dUMST and _XAT are sourced at the "LCAS Block".

G.806Cor1_F10-4

Figure 10-4/G.806 – P-Xv/P-X-L_A_So processes (subdiagrams)

Replace the dUMST defect definition:

Persistent unexpected MST (dUMST): A persistent detection (longer than a time t_{detect}) of RI_MST_rec[i]=0 (OK), while no RS-ACK is pending, for a member that carries the "IDLE" control word shall give rise to a dUMST defect. The defect shall be cleared as soon as RI_MST_rec[i]=1 (FAIL) is detected persistently (longer than t_{clear}) for all members with those control words. The value of the t_{detect} , t_{clear} parameters is ffs.

With:

Persistent unexpected MST (dUMST): A persistent detection (longer than a time t_{detect}) of RI_MST_rec[i]=0 (OK), while no RS-ACK is pending, for a member that does not carry the "ADD", "NORM", "EOS" or "DNU" control words shall give rise to a dUMST defect. The defect shall be cleared as soon as RI_MST_rec[i]=1 (FAIL) is detected persistently (longer than t_{clear}) for all members not carrying those control words. The value of the t_{detect} , t_{clear} parameters is ffs.

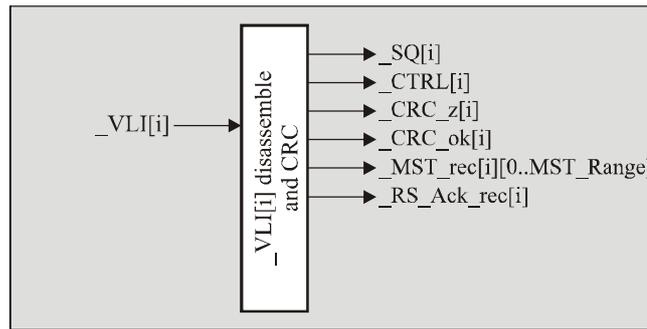
2.6 Clause 10.1.1.2

Add to Table 10-2 in the Output column under "P-Xv/P-X-L_A_Sk_MP":

P-Xv/P-X-L_A_Sk_MI_cLOA

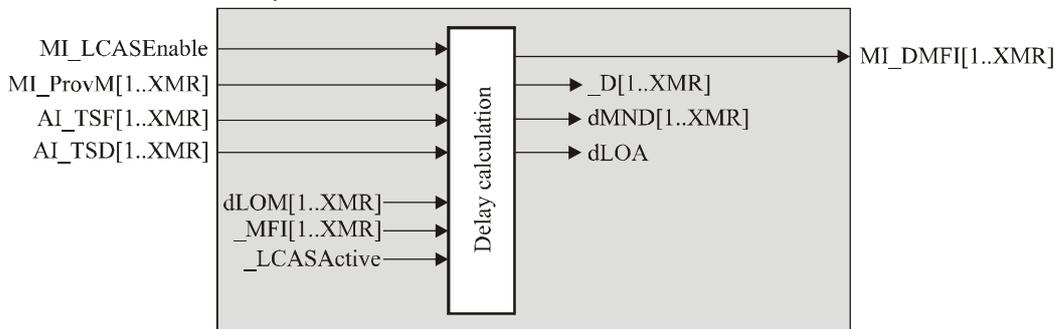
Replace Figure 10-7:

"VLI[i] Block" (repeat for k=1..XMR)



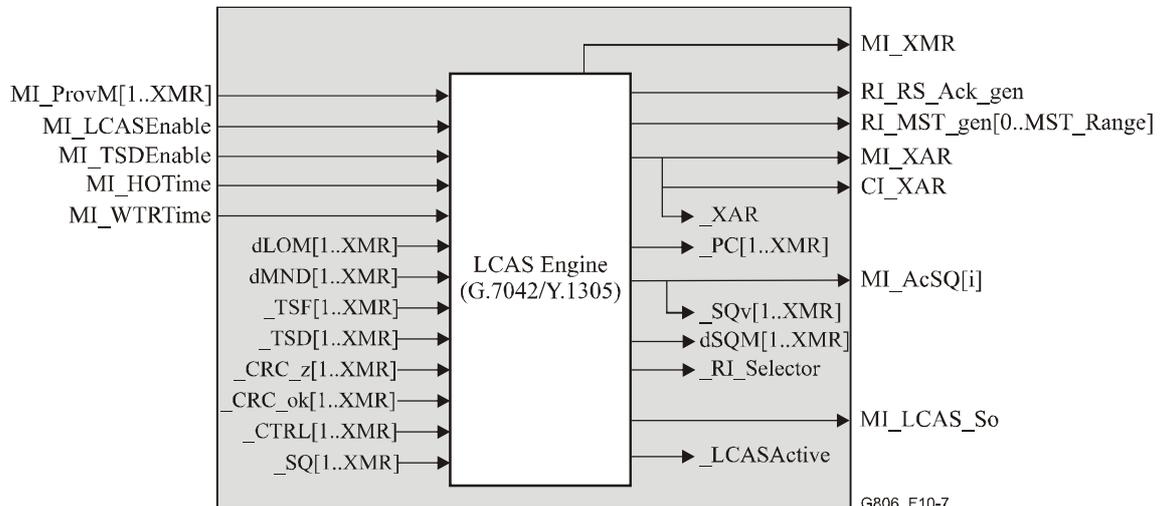
NOTE – The internal input _VLI[i] is sourced at the "VLI, TSx extract" process.

"Delay calculation"



NOTE – The internal inputs dLOM[i], _MFI[i] are sourced at the "MFI extract" process.

"LCAS Block"



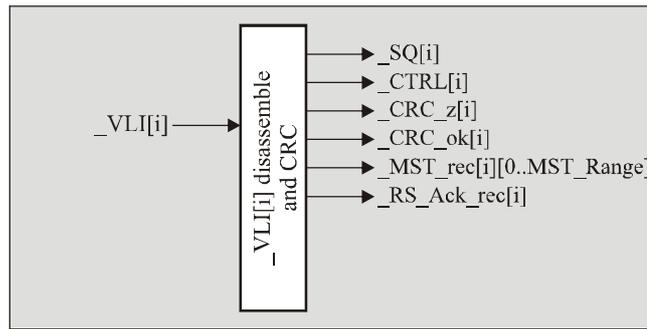
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NOTE – The internal inputs dLOM[i], _MFI[i] are sourced at the "MFI extract" process, dMND[i] at the "Delay calculation" process, _TSF, _TSD at the "VLI, TSx extract" process and _CRC_z[i], _CRC_ok[i], _CTRL[i], _SQ[i] at the "VLI disassemble" process.

Figure 10-7/G.806 – P-Xv/P-X-L_A_Sk processes (subdiagrams I)

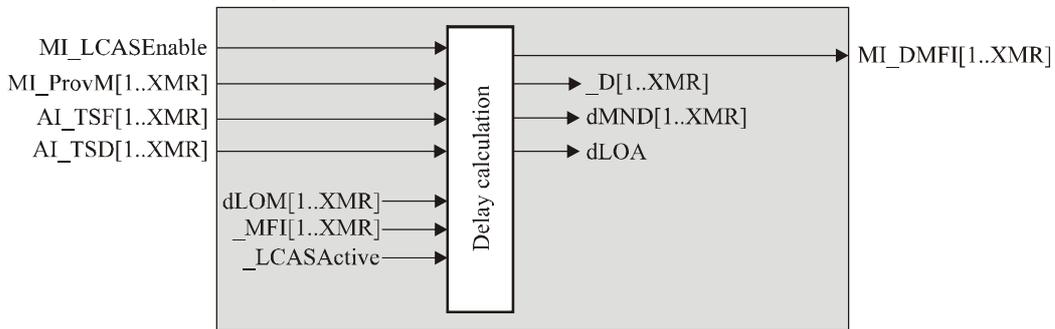
With:

"VLI[i] Block" (repeat for k=1..XMR)



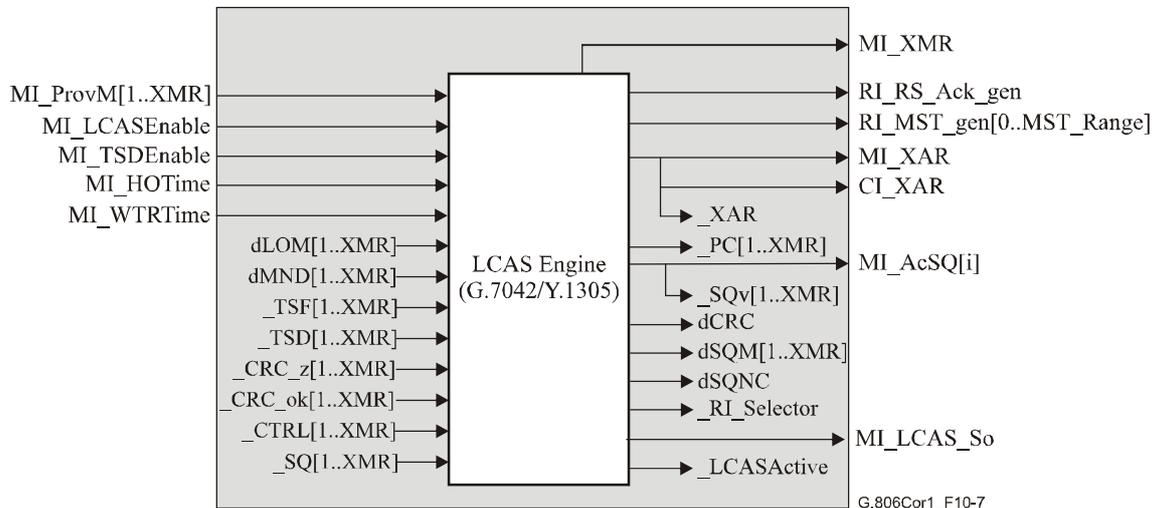
NOTE – The internal input $_VLI[i]$ is sourced at the "VLI, TSx Extract" process.

"Delay calculation"



NOTE – The internal inputs $dLOM[i]$, $_MFI[i]$ are sourced at the "MFI Extract" process.

"LCAS Block"



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NOTE – The internal inputs $dLOM[i]$, $_MFI[i]$ are sourced at the "MFI Extract" process, $dMND[i]$ at the "Delay Calculation" process, $_TSF$, $_TSD$ at the "VLI, TSx Extract" process and $_CRC_z[i]$, $_CRC_ok[i]$, $_CTRL[i]$, $_SQ[i]$ at the "VLI Disassemble" process.

Figure 10-7/G.806 – P-Xv/P-X-L_A_Sk processes (subdiagrams I)

Replace Figure 10-8:

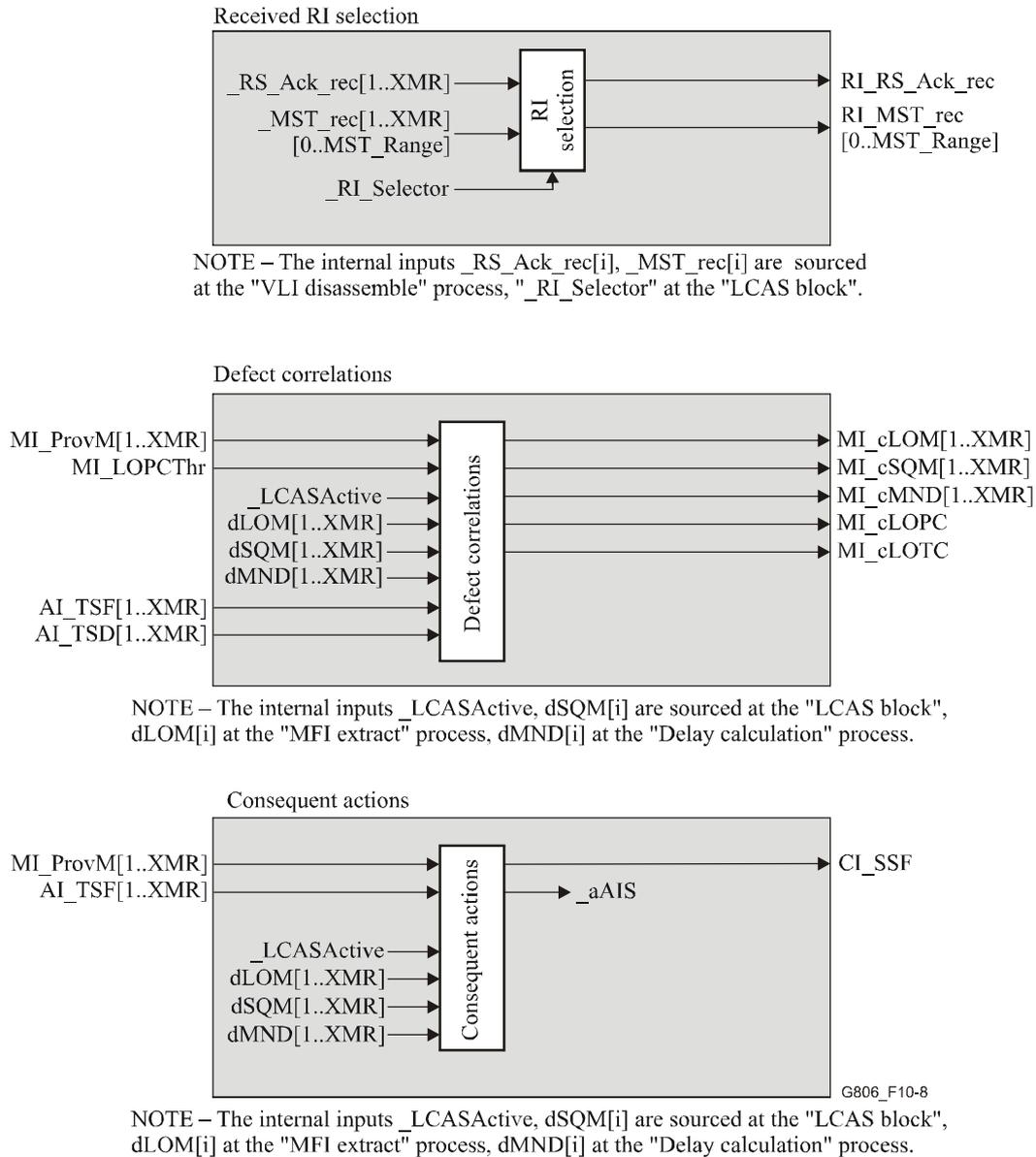
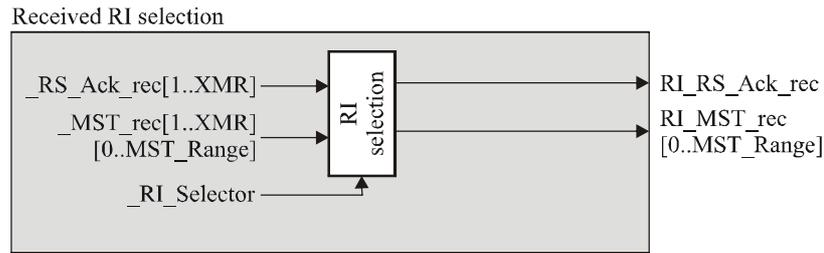
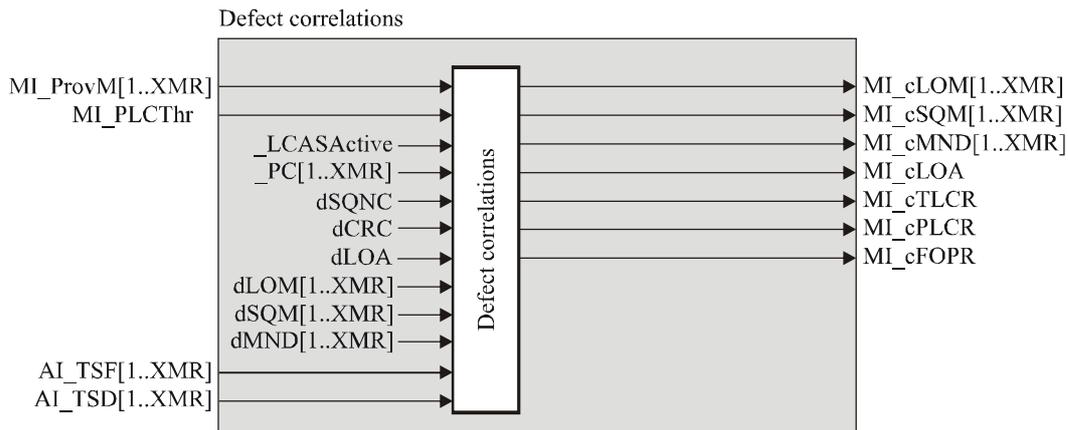


Figure 10-8/G.806 – P-Xv/P-X-L_A_Sk processes (subdiagrams II)

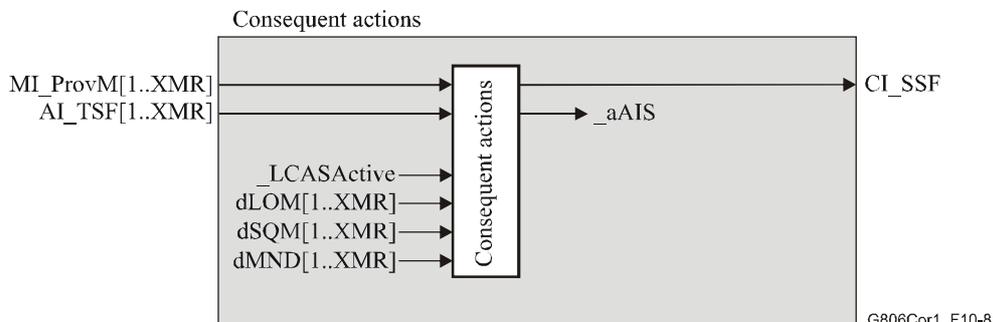
With:



NOTE – The internal inputs `_RS_Ack_rec[k]`, `_MST_rec[k]` are sourced at the "VLI Disassemble" process, "`_RI_Selector`" at the "LCAS block".



NOTE – The internal inputs `_LCASActive`, `_PC`, `dSQNC`, `dCRC`, `dSQM[k]` are sourced at the "LCAS block", `dLOM[k]` at the "MFI Extract" process, `dMND[k]`, `dLOA` at the "Delay Calculation" process.



NOTE – The internal inputs `_LCASActive`, `dSQM[k]` are sourced at the "LCAS block", `dLOM[k]` at the "MFI Extract" process, `dMND[k]` at the "Delay Calculation" process.

G806Cor1_F10-8

Figure 10-8/G.806 – P-Xv/P-X-L_A_Sk processes (subdiagrams II)

Replace the *_PC* generation in the LCAS Engine process:

- *_PC*[1..*X_{MR}*]: Indication of whether a particular member is an active member (i.e., is carrying payload) at a particular time. For each index *i*, the process shall calculate *_PC*[*i*] as follows:
 - *MI_ProvM*[*i*]=0 → *_PC*[*i*]=0
 - *MI_ProvM*[*i*]=1 →
 - (*_TSF*[*i*] or (*dLOM*[*i*] or (*dMND*[*i*])) → *_PC*[*i*]=0
 - not ((*_TSF*[*i*] or (*dLOM*[*i*] or (*dMND*[*i*])))) → *_PC*[*i*] as determined by the LCAS G.7042 protocol (=1 if the protocol determines it is carrying payload, =0 otherwise)

With:

- *_PC*[1..*X_{MR}*]: Indication of whether a particular member is an active member (i.e., is carrying payload) at a particular time. For each index *i*, the process shall calculate *_PC*[*i*] as follows:
 - *MI_ProvM*[*i*]=0 → *_PC*[*i*]=0
 - *MI_ProvM*[*i*]=1 → *_PC*[*i*] as determined by the LCAS protocol in G.7042/Y.1305 (=1 if the protocol determines it is carrying payload, =0 otherwise)

Replace the *_SQv*[1..*X_{MR}*] generation in the LCAS Engine process:

- *_SQv*[1..*X_{MR}*]: Validated sequence number. For each index *i*, the process shall calculate *_SQv*[*i*] as follows:
 - (*MI_ProvM*[*i*]=0) or (*_TSF*[*i*] or (*dLOM*[*i*] or (*dMND*[*i*])) → *_SQv*[*i*]=n/a
 - else
 - *_CRC_ok*[*i*]=1 → *_SQv*[*i*]=*_SQ*[*i*]
 - *_CRC_ok*[*i*]=0 → *_SQv*[*i*] retains its previous value

With:

- *_SQv*[1..*X_{MR}*]: Validated sequence number. For each index *i*, the process shall calculate *_SQv*[*i*] as follows:
 - (*MI_ProvM*[*i*]=0) or (((*_TSF*[*i*] or (*dLOM*[*i*] or (*dMND*[*i*])))) and (*HOTimer* not running)) or (*_CTRL*[*i*]=*IDLE*) → *_SQv*[*i*]=n/a
 - else
 - *_CRC_ok*[*i*]=1 → *_SQv*[*i*]=*_SQ*[*i*]
 - *_CRC_ok*[*i*]=0 → *_SQv*[*i*] retains its previous value

Replace the *_RI_Selector* generation in the LCAS Engine process:

- *_RI_Selector*: Member index used to select the remote information set sent to the source (see *RI Selection* process below). *_RI_Selector* shall be chosen from among those indexes *i* satisfying:
 - (*_PC*[*i*]=1) and (*_CRC_ok*[*i*]=1)

If this is an empty set, then *_RI_Selector* shall be sourced as "n/a".

NOTE 8 – This value is an error indication towards the *RI Selection* process.

Otherwise the specific choice of *_RI_Selector* is implementation-specific since the LCAS Source sends the same *MST* values simultaneously in the control packets of all members of a VCG.

NOTE 9 – The LCAS protocol as defined in ITU-T Rec. G.7042/Y.1305 is used in this process to calculate some outputs. The instance of the protocol used here shall have the following characteristics:

With:

- _RI_Selector: Member index used to select the remote information set sent to the source (see RI Selection process below). _RI_Selector shall be chosen from among those indexes *i* satisfying:
 - (MI_ProvM[*i*]=1) and not ((_TSF[*i*) or (dLOM[*i*) or (dMND[*i*])) and (_CRC_ok[*i*]=1)

If this is an empty set, then _RI_Selector shall be sourced as "n/a".

NOTE 8 – This value is an error indication towards the RI Selection process.

Otherwise the specific choice of _RI_Selector is implementation-specific since the LCAS Source sends the same MST values simultaneously in the control packets of all members of a VCG.

NOTE 9 – The LCAS protocol as defined in ITU-T Rec. G.7042/Y.1305 is used in this process to calculate some outputs. The instance of the protocol used here shall have the following characteristics:

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