

ITU-T

TELECOMMUNICATION
STANDARDIZATION SECTOR
OF ITU

G.727

Corrigendum 1
(05/2005)

SERIES G: TRANSMISSION SYSTEMS AND MEDIA,
DIGITAL SYSTEMS AND NETWORKS

Digital terminal equipments – Coding of analogue signals
by methods other than PCM

5-, 4-, 3- and 2-bit/sample embedded Adaptive
Differential Pulse Code Modulation (ADPCM)

**Corrigendum 1: Correction to Annex A:
Extensions of Recommendation G.727 for use
with uniform-quantized input and output**

ITU-T Recommendation G.727 (1990) – Corrigendum 1

ITU-T G-SERIES RECOMMENDATIONS
TRANSMISSION SYSTEMS AND MEDIA, DIGITAL SYSTEMS AND NETWORKS

INTERNATIONAL TELEPHONE CONNECTIONS AND CIRCUITS	G.100–G.199
GENERAL CHARACTERISTICS COMMON TO ALL ANALOGUE CARRIER-TRANSMISSION SYSTEMS	G.200–G.299
INDIVIDUAL CHARACTERISTICS OF INTERNATIONAL CARRIER TELEPHONE SYSTEMS ON METALLIC LINES	G.300–G.399
GENERAL CHARACTERISTICS OF INTERNATIONAL CARRIER TELEPHONE SYSTEMS ON RADIO-RELAY OR SATELLITE LINKS AND INTERCONNECTION WITH METALLIC LINES	G.400–G.449
COORDINATION OF RADIOTELEPHONY AND LINE TELEPHONY	G.450–G.499
TRANSMISSION MEDIA CHARACTERISTICS	G.600–G.699
DIGITAL TERMINAL EQUIPMENTS	G.700–G.799
General	G.700–G.709
Coding of analogue signals by pulse code modulation	G.710–G.719
Coding of analogue signals by methods other than PCM	G.720–G.729
Principal characteristics of primary multiplex equipment	G.730–G.739
Principal characteristics of second order multiplex equipment	G.740–G.749
Principal characteristics of higher order multiplex equipment	G.750–G.759
Principal characteristics of transcoder and digital multiplication equipment	G.760–G.769
Operations, administration and maintenance features of transmission equipment	G.770–G.779
Principal characteristics of multiplexing equipment for the synchronous digital hierarchy	G.780–G.789
Other terminal equipment	G.790–G.799
DIGITAL NETWORKS	G.800–G.899
DIGITAL SECTIONS AND DIGITAL LINE SYSTEM	G.900–G.999
QUALITY OF SERVICE AND PERFORMANCE – GENERIC AND USER-RELATED ASPECTS	G.1000–G.1999
TRANSMISSION MEDIA CHARACTERISTICS	G.6000–G.6999
DATA OVER TRANSPORT – GENERIC ASPECTS	G.7000–G.7999
ETHERNET OVER TRANSPORT ASPECTS	G.8000–G.8999
ACCESS NETWORKS	G.9000–G.9999

For further details, please refer to the list of ITU-T Recommendations.

ITU-T Recommendation G.727

5-, 4-, 3- and 2-bit/sample embedded Adaptive Differential Pulse Code Modulation (ADPCM)

Corrigendum 1

Correction to Annex A: Extensions of Recommendation G.727 for use with uniform-quantized input and output

Summary

ITU-T Rec. G.727 contains the specification for the embedded adaptive differential PCM voice-coding algorithm at 16, 24, 32, and 40 kbit/s. Its Annex A contains the specification for linear PCM input and output. This corrigendum fixes an omission in Decoder Block LIMO of Annex A/G.727, which limits the output linear two's complement sample to a 14-bit value. The pseudo-code, as originally published, misses the case for reconstructed samples SR_{FF} equal to 57 344.

Source

Corrigendum 1 to ITU-T Recommendation G.727 (1990) was approved on 14 May 2005 by ITU-T Study Group 16 (2005-2008) under the ITU-T Recommendation A.8 procedure.

FOREWORD

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ITU-T Recommendation G.727

5-, 4-, 3- and 2-bit/sample embedded Adaptive Differential Pulse Code Modulation (ADPCM)

Corrigendum 1

Correction to Annex A: Extensions of Recommendation G.727 for use with uniform-quantized input and output

Description

In the LIMO (decoder only) routine in A.3.5/G.727, *Output limiting (decoder only)*, "SR_{FF} & 16 383, SR_{FF} < 8192 or SR_{FF} > 57 344" should be replaced with "SR_{FF} & 16 383, SR_{FF} < 8192 or SR_{FF} > 57 343". The code is missing the case for SR_{FF} = 57 344.

Amend Block LIMO as indicated:

LIMO (decoder only)

Input: SR_{FF}

Output: SO

Function: Limit output to 14-bit two's complement value

SO = | 8191, SR_{FF} > 8191 and SR_{FF} < 32 768
| SR_{FF} & 16 383, SR_{FF} < 8192 or SR_{FF} > 57 3434
| 57 344, SR_{FF} > 32 767 and SR_{FF} < 57 344

SERIES OF ITU-T RECOMMENDATIONS

Series A	Organization of the work of ITU-T
Series D	General tariff principles
Series E	Overall network operation, telephone service, service operation and human factors
Series F	Non-telephone telecommunication services
Series G	Transmission systems and media, digital systems and networks
Series H	Audiovisual and multimedia systems
Series I	Integrated services digital network
Series J	Cable networks and transmission of television, sound programme and other multimedia signals
Series K	Protection against interference
Series L	Construction, installation and protection of cables and other elements of outside plant
Series M	Telecommunication management, including TMN and network maintenance
Series N	Maintenance: international sound programme and television transmission circuits
Series O	Specifications of measuring equipment
Series P	Telephone transmission quality, telephone installations, local line networks
Series Q	Switching and signalling
Series R	Telegraph transmission
Series S	Telegraph services terminal equipment
Series T	Terminals for telematic services
Series U	Telegraph switching
Series V	Data communication over the telephone network
Series X	Data networks, open system communications and security
Series Y	Global information infrastructure, Internet protocol aspects and next-generation networks
Series Z	Languages and general software aspects for telecommunication systems