Bar Shapira, Senior Software Architect, Nvidia, Israel



NVIDIA's NIC and DPU Architecture team. features. related to network synchronization systems. from Tel Aviv University in 2020."

- Bar Shapira is a Senior Software Architect within
- He leads the SW architecture and development of timing
- and synchronization solutions, including SyncE and PTP,
- alongside essential network monitoring and diagnostic
- He is an innovator in the field, holding multiple patents
- Bar earned his B.Sc. in Computer Science and Physics





Time Synchronization in Data Centre Networks

Bar Shapira | ITU workshop on Future Optical Networks for IMT2030, AI, broadband and more, June 2025





Agenda

- Use Cases and Requirements for Time Sync in Data Centers
 - Distributed Databases
 - Synchronized High-frequency Telemetry and Profiling
 - Congestion Control based on One-way Delay
 - Synchronized Collectives
- G.Sub.DCSync Accuracy levels

Suggested Synchronization Solutions for Data Centers





End to End synchronization unlocks the next level of application performance



Time is an accelerator







• Scalable Databases

Accuracy requirements



Distributed Databases

Use Cases and Requirements for Time Sync in Data Centers

• Scalable Databases with lock-free distributed read transactions based on an API that exposes time uncertainty. Utilize time synchronization to achieve external consistency of distributed transactions.

• Upon a read transaction, a server must wait a period equal to the time uncertainty. To avoid this being the bottleneck, the time uncertainty should be lower than the network delay and processing time. Absolute time accuracy is a must.



Server 1 (New York)





Synchronized High-frequency Telemetry and Profiling Use Cases and Requirements for Time Sync in Data Centers

Synchronized high-frequency telemetry and Profiling

Accuracy requirements

 Accuracy between endpoints must be an order of magnitude better than the sampling frequency. The accuracy is needed at the clock that timestamps the events – could be NIC, Switch or CPU clock, depending on the measured events.



PCIe link latency max read time (ns)

• HFT tools sample telemetry data with associated timestamps from elements in the data center at high rates. Accurate synchronization between devices is required to align the data collected from multiple devices on a single timeline.



Synchronized HFT can help identify stragglers

PCIE TX throughput



Congestion Control based on One-way Delay Use Cases and Requirements for Time Sync in Data Centers

Congestion control based on one-way delay

differentiate between forwarded and reverse path congestion.

Accuracy requirements

the NIC is sufficient.



- Forward delay = TS2-TS1
- Backward delay = TS4-TS3

• Measure the per-path direction (TX vs. RX) delay of packets in the network for the use of congestion control. Can help

• Accuracy between end points must be an order of magnitude better than the actual delay. Relative time accuracy in

Requestor (RP) Common clock using PTP TS1 Forward Path CC at data priority Network TS4 **Reverse Path** CC at high priority

Responder (NP)





Synchronized collectives

reduced network congestion.

Accuracy requirements

the PHC; relative time accuracy in the NIC is sufficient.

Time slot		I
Node 0	Compute	
Node 1		С
Node 2	Compu	te
Node 3		

	Synchronized																																	
Time slot	Ī]				2				2				3					1							1				2				
Node 0	Compute	1	1	1	1	2	2	2	2	2	2	2	2	3	3	3	3	3	3	3	3	1	1	1	1									
Node 1		Со	mp	but	е					3	3	3	3	0	0	0	0	0	0	0	0	2	2	2	2	2	2	2	2	3	3	3	3	
Node 2	Compu	te		3	3	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	3	3	3	3	3	3						_	
Node 3	Compute										2	2	2	2	0	0	0	0	0	0	0	0	1	1	1	1	1							

Synchronized Collectives Use Cases and Requirements for Time Sync in Data Centers

• Reduce network load and improve completion times by aligning the operations of all nodes to designated timeslots, resulting in

• The accuracy of time should be an order of magnitude smaller than the timeslot duration. The scheduling is done at the NIC using







• Simplified datacentres example



Class levels of End Application	Addition for End
A	
B	2
C	

G.Sub.DCSync Accuracy Levels (WIP)

Class level at Time Sync Clocks	Relative Time error between Time Sync Clocks	Typical application (for information)
1	5µsec	Distributed databases applications profiling
2	1µsec	High-Frequency Telemetry,
3	200nsec	CC based OWD, Synchronized collect

Typically, with physical edge clock signal (e.g. PPS output of the Time Sync Clock to PPS input of the End Application Time Clocks when available).







• Main challenge: very high scale

specific needs and network setups.

Option	Layer	Network support	
G.8275.1	PTP over Eth - Local link	Boundary clocks	G.8275.1 mature standard, u
	multi cast		issue, require fully PTP awa
IEEE 1588.1	PTP over IP - Unicast	Transparent clock	Reduce CPU, memory, and
CSPTP			In development (private imp



NX GPU

G.Sub.DCSync Suggested PTP Profiles (WIP)

• There are two suggested PTP Profiles for data centers in G.Sub.DCSync. The right profile can be chosen depending on the

N X GPU

Notes

used in telecom, easily scalable, no path asymmetry are network network utilization compared to traditional unicast PTP

plementation - Simple PTP by Meta)

NX GPU

N X GPU





