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Patent Holder:

Legal Name Arthur Granville Scott III

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(*)Number ITU-T Rec. L.1318

(*)Title Q factor: A fundamental metric expressing integrated circuit energy efficiency

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No.	Status [granted/ pending]	Country	Granted Patent Number or Application Number (if pending)	Title
1			63/816,458	Symmetric Semiconductor Device with Co-Optimized Doping for Reversible Computing (HeartMOST™)
2				
3				
4				
5				
6				
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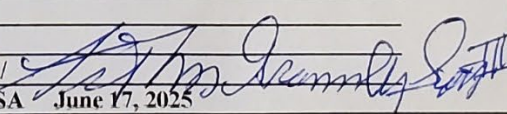
Signature (include on final page only):	
Patent Holder	<u>Arthur Granville Scott III</u>
Name of authorized person	<u>Arthur Granville Scott III</u>
Title of authorized person	<u>Inventor</u>
Signature	<u>/Arthur Granville Scott III/</u>
Place, Date	<u>Menlo Park, California, USA June 17, 2025</u>

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No.	Status [granted/ pending]	Country	Granted Patent Number or Application Number (if pending)	Title
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Signature (include on final page only):	
Patent Holder	<u>Arthur Granville Scott III</u>
Name of authorized person	<u>Arthur Granville Scott III</u>
Title of authorized person	<u>Inventor</u>
Signature	<u>/Arthur Granville Scott III/</u> 
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